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ELECTRONICS & WIRELESS WORLD

MARCH 1987

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Industrial soldering technology

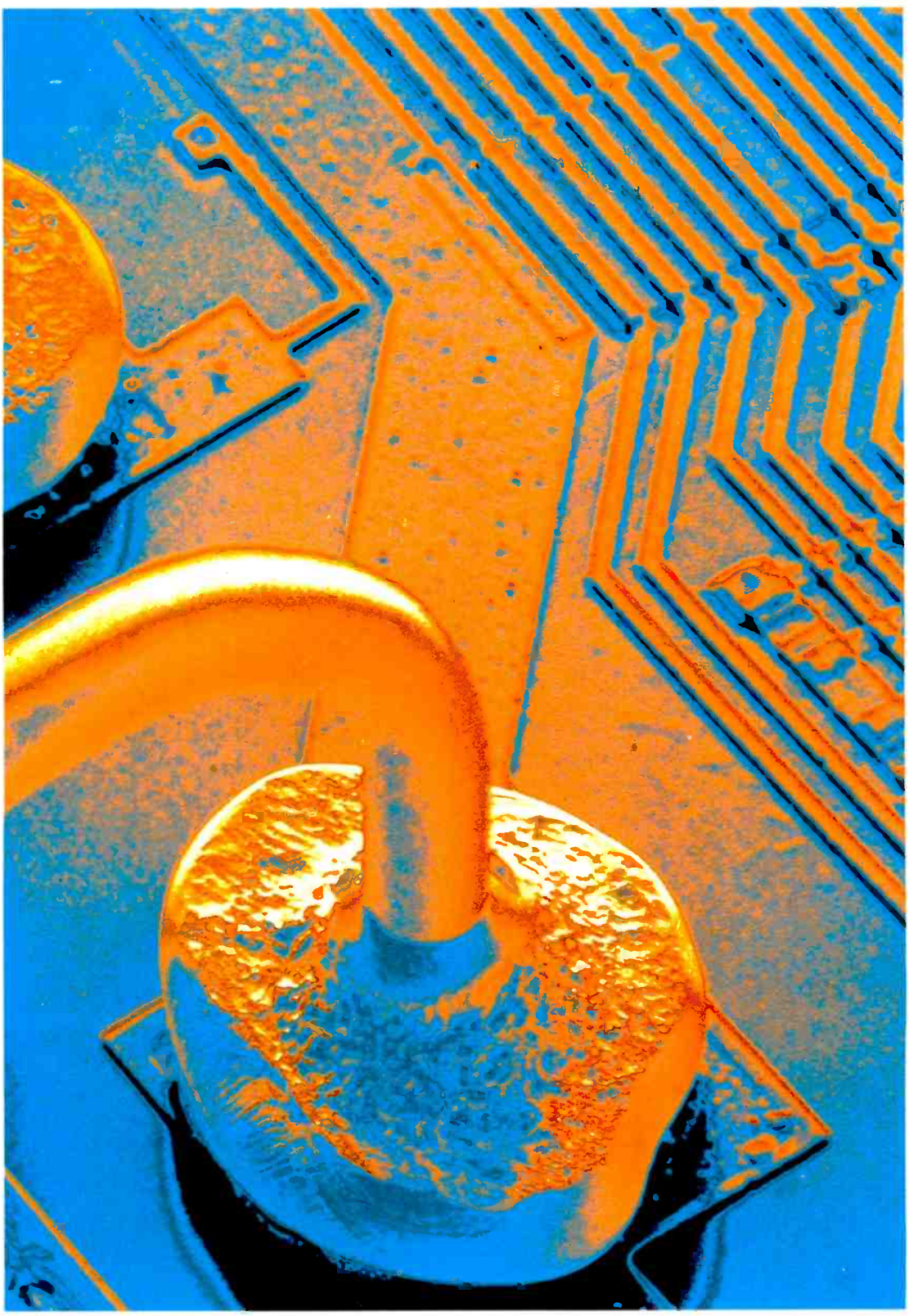
Orthogonality and Walsh functions

Testing surface-mounted devices

Measuring micro-ohms

Designing toroidal transformers

Scientific interface for PCW8256/ 8512



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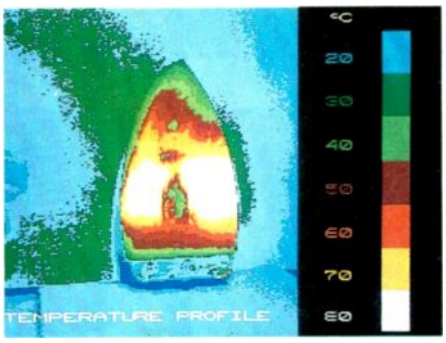

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It is encouraging to see that someone, somewhere is paying attention to compatibility. Traditionally, the emergency of a novel invention or development has been accompanied by a welter of alternative ways of going about it, with utter confusion reigning supreme.

One can think of hundreds of examples, from railway-line gauges to video recorders, in which companies have been determined to plough their own furrow, regardless of the rest of the world, so that a 110V, 60Hz sprocket dangler built in the US is held in place by a screw that is totally unknown in the rest of the 240V, 50Hz civilized world. Right-hand drive VHS video recorders won't play left-hand drive Betamax tapes and the problems of getting computers to talk to peripherals and to other computers are manifold. Any efforts to reduce this cacophony are greatly to be welcomed.

A report in the *Times* recently drew attention to a concerted effort by seven large manufacturers of domestic electronic goods to pave the way for the 'intelligent home' of the near future. Leaving aside the question of just how bright one wants a home to be in comparison with one's own mental capabilities, the business of ensuring compatibility between personal computers, television receivers and displays, telephones, sensors, actuators, heating, lighting, cooking, radio and anything else that comes to mind is an essential pre-requisite to the establishment of homes with any claim at all to intelligence, as opposed to the dumb kind which one simply lives in, periodically trekking wearily across the room to change a record (or compact disc, in this context).

The seven companies – G.E.C., Mullard, Siemens, Thomson, Philips, Thorn-EMI and Electrolux are spending, says the *Times*, £12 million on making sure that all the equipment will work happily together and that a British computer expressing a desire that the Swedish cooker should begin to think about dinner in a constructive manner shall not meet a sulky refusal to do anything of the sort. Thorn-EMI are leading the project and have built a test house at Hayes to investigate methods of communication between the equipment.

All this effort is, of course, later than it should be and much later than it could be. The Japanese, predictably, have had it well in mind for some time, and could be on the point of producing the hardware.

A funding of £12 million seems a great deal of money to devote to reaching an agreement to make equipment friendly, since all the techniques needed to do that are already there. But, at least, it is happening, and one must not be churlish about it. Let us hope that the Europeans and Japanese do not insist on having two different 'standards', when it would be so simple to set up a global one. If they can get computers to talk to washing machines, one might be permitted to hope that they will find it possible to talk to each other.

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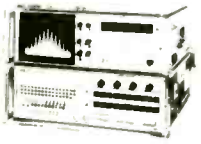

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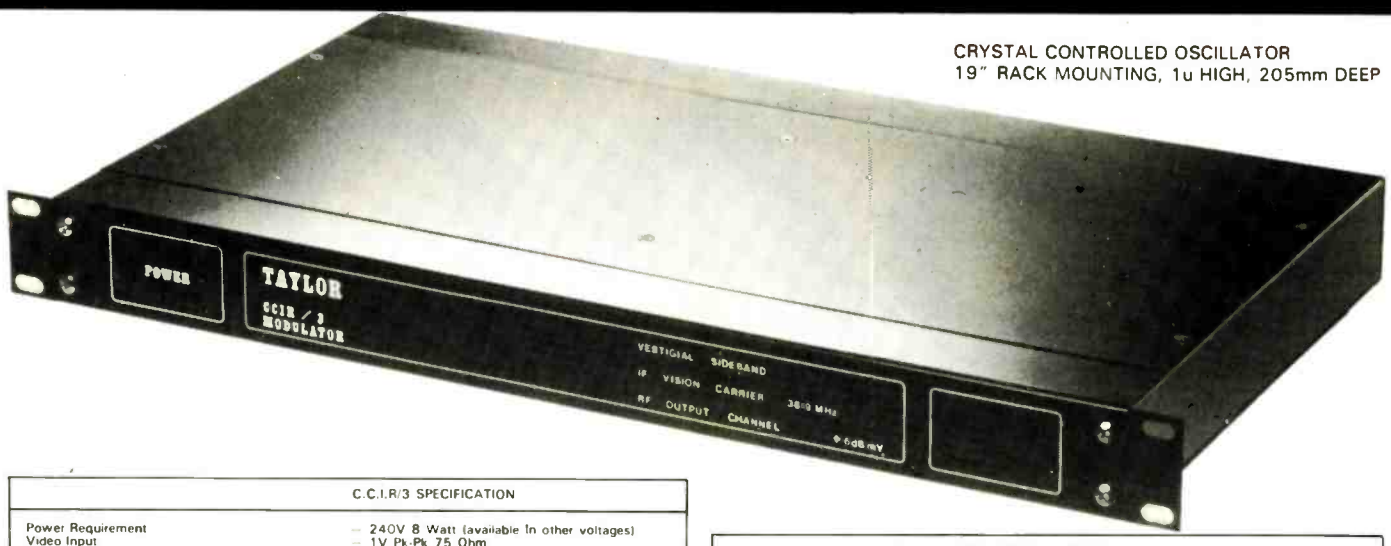

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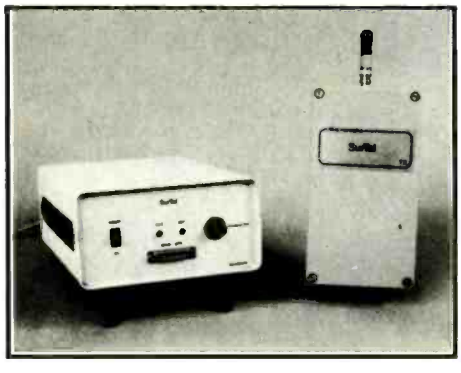

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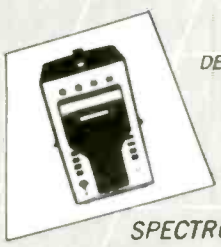

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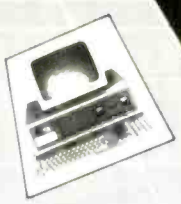

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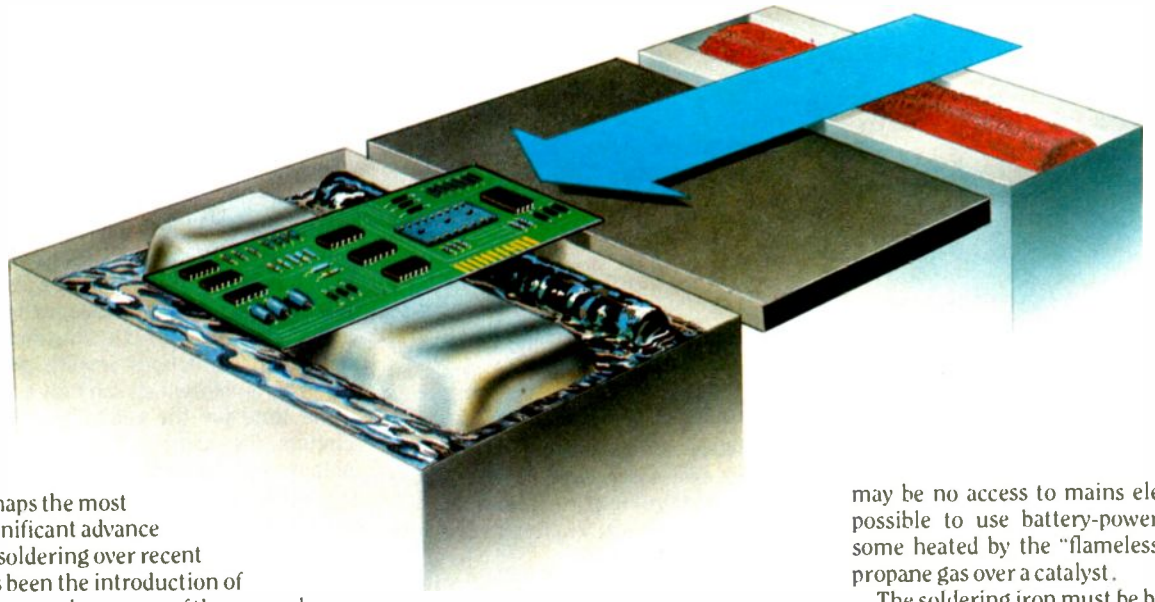
Soldering technology

Soldering methods have changed rapidly in recent years. This survey describes that techniques used and production practices.



Perhaps the most significant advance in soldering over recent years has been the introduction of machines to replace many of the manual assembly processes. Wave and reflow soldering are examples. Despite this, for many applications, manual soldering is still cost effective, depending on the volume of soldering to be completed. The production of greater than 400 p.c.bs a week is needed to make the purchase of a wave-soldering machine financially viable. Similarly more than 1000 boards a week would justify investing in pick-and-place machinery and reflow soldering for surface-mounted components.

HAND SOLDERING

Hand soldering is still the most widely used method for small to medium scale production and it is worth remembering some of the practical advice offered by solder manufacturers; not least because the same techniques apply to all methods of soldering.

Solder flows spontaneously into a joint if the correct conditions are provided. The joint must therefore be designed with a narrow gap between the parts to be joined, into which the solder can be drawn by capillary action. The gap should be open at the sides or the other end so that solder flow is not prevented by trapped air or flux. Maximum strength is obtained when the gap to be filled by the solder is between about 0.08 and 0.1mm wide. Where possible the joint should be designed so that the components interlock or support each other during soldering, thus avoiding the need for clamps close to the joint, which could take heat away, and providing extra strength in the finished joint. A good mechanical joint needs the solder to make it permanent but, ideally, should provide an adequate contact for current flow without the solder. All surfaces to be joined must be clean and free of grease.

Element	BS219	ITRI	Effect
Tin	59 to 60	—	
Arsenic	0.03	—	Decrease of spread area (1%)
Bismuth		0.1—	Discolouration/oxidation of solder coating (0.5%)
Iron	0.02	—	—
Copper	0.08	0.25	Grittiness (0.29%)
Aluminium	0.001	0.0005	Oxidation (0.0005%)
Silver	n.s.	2.0	
Cadmium	0.005	—	Decrease in spread area (0.15%)
Zinc	0.003	—	Oxidation
Gold	n.s.	0.1	
Antimony	0.2	—	Decrease in spread are (1.0%)
Phosphorous	n.s.	0.005	Dewetting/grittiness (0.01%)
Sulphur	n.s.	0.001	Grittiness (0.0015%)
Others		Total 0.08	
Lead		Balance	

The composition of KP grade solder as used in electronics. The percentages of maximum levels of impurities are specified in BS219. The next column lists the maxima recommended by the International Tin Research Institute where they differ from BS219. These result from tests carried out at ITRI, which showed the deterimental effects of the impurities caused by the proportion in brackets.

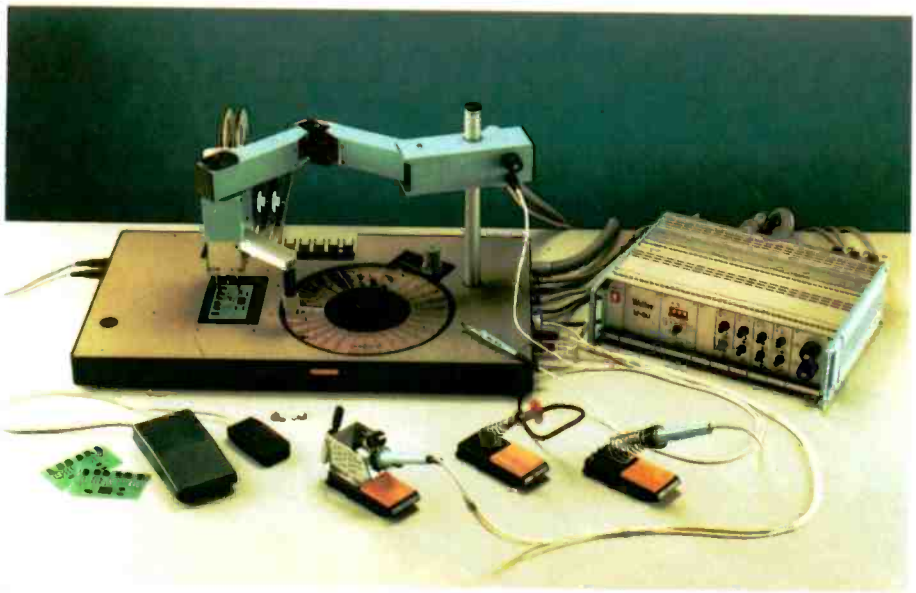
Many designs of soldering iron are available and some incorporate systems for feeding a length of soldering wire automatically, or to deposit molten solder from a reservoir within the iron. There are miniature soldering irons for hand-assembly of surface-mounted boards. For field work, where there may be no access to mains electricity, it is possible to use battery-powered irons, or some heated by the "flameless" burning of propane gas over a catalyst.

The soldering iron must be big enough for its working face to cover the width of the joint, and hot enough to heat the workpiece surfaces to a sufficiently high temperature for the solder to flow. The working surface of the iron must also be clean, free from charred flux and coated with a thin layer of molten solder so that heat can be conducted to the joint. Solder wire should be of a diameter thin enough to melt quickly but thick enough to supply sufficient solder for the joint without having to use too great a length of wire. The solder should be applied to the heated joint and not carried to the joint by the iron.

SOLDER

The combination of tin and lead used in solders is selected to give them a common melting temperature (eutectic mixture) as if they were a pure substance. The proportion also permits the lowest possible soldering temperatures (250 to 260°C) with less risk of damage to sensitive components, the standard mixture is 60 to 63% tin and 40 to 37% lead. There are various national standards for the permitted levels of inpurity. The British standard and the effects of the impurities, from research undertaken at the International Tin Research Institute (ITRI), are given in the table. This also includes maximum levels recommended by ITRI where they differ from BS 219 for KP grade solder.

Flux acts as a cleaner to remove oxides from the surfaces to be joined and also assists wetting, the reduction of the contact angle between the workpiece and the solder. For hand soldering of p.c.bs, it is convenient to use solder wire that incorporates the flux such as that provided by Multicore. Rosin flux leaves a deposit that usually has to be cleaned off after soldering but there have



A pick-and-place robotic system for assembling s.m.cs for hand soldering. The Weller station from Cooper Tools offers a choice of fine-tipped and shaped soldering irons, all temperature controlled.

been advances in this field too and Multicore has announced a synthetic flux, X32, which is virtually fume free and leaves no discernible residue, eliminating the need for cleaning a p.c.b. after soldering.

Depending on the components being used extra care may need to be taken: some components are sensitive to excessive temperature and others to static charges. It may be necessary to use temperature-controlled soldering irons and match the melting-point of the solder to the application. Several companies make such irons. One example is the 4710 soldering station made by Teleproduction tools. This uses a 100W element fitted into a lightweight pencil iron and offers a very fast heat recovery cycle. It is continuously variable between 100 and 400°C and is equally suitable for fine p.c.b. work as well as large solder tabs and other components which represent a considerable heatsink. The Stiron 4710 has no moving parts: spiking or magnetic effects and leakage is negligible. It is completely safe for use on mos-fets and other 'sensitive' components. It has a range of 14 iron-clad tips from a 0.8mm conical tip to a 5mm screwdriver for heavy-duty soldering.

A number of suppliers offer static protection materials. When working with c-mos devices it is necessary to use conductive, earthed work surfaces, tools and packaging. The environment should be as static-free as possible. This involves, for example, not using nylon carpets. The operators should wear non-synthetic work-clothes to avoid generating static and should be provided with earthed wrist straps to conduct away any static that may have been generated. For the safety and comfort of the operator it is also necessary to provide extractor fans to remove the fumes generated by burning flux. Hand assembly can be partially automated by the provision of workstations which have, for example, rotating component trays to present the user with the next component or lights to indicate the container of the component to be inserted. This is often combined with a projection system which will focus an indicator light onto the position on the p.c.b. where the component is to be inserted. Such systems vary in complexity and are usually computer controlled.

DESOLDERING

Hand processes are often used in the reworking and repair of defective p.c.bs, which may have been hand or mass assembled. The joint is remelted with an iron and the solder extracted by suction. Often a iron with a hollow bit connected to a vacuum line and a solder-collecting reservoir is used for solder removal. Antex have produced an iron with a spring-loaded hollow bit which is pushed onto the joint and sucks the solder as it is withdrawn. There are of course also spring-loaded hand solder suckers and the capillary action of a woven braid of fine tinned copper wire. Other methods involve the use of shaped iron bits which can heat joints at the same time for the removal of integrated circuits, for example. It is also possible to obtain semi-automatic machines which are provided with heated solder baths small enough and shaped to reheat just the pins of

Multicore GS60 solder has added 0.17% copper to reduce drossing. Here used in a Zevatron wave machine.

Fry's Metals have been manufacturing solder for 75 years. They invented the wave soldering process and have developed a number of new fluxes for use in wave machines. Their low-dross solder, Super LDC, has a minimum of trace elements, but a new solder, as yet unnamed has been developed which ads to the high purity a special finishing process which provides a highly polished finish and keeps oxidation to a minimum. This will add to their ranges of solders, fluxes, solder creams and pastes.

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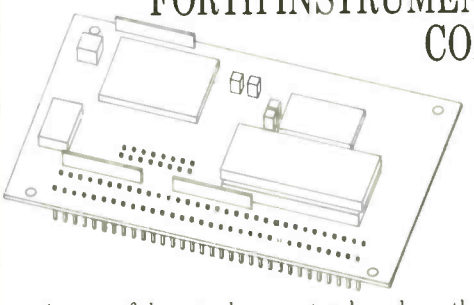

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one multi-joint component. In a single process the defective component can be replaced. The p.c.b. is positioned so that the component's leads can be immersed in the bath. A lever action brings the solder bath up to the component which can be withdrawn while its leads are immersed; a replacement is inserted and the bath removed, leaving the replacement in position and soldered.

SOLDERABILITY

Satisfactory wetting of a solder joint can only be obtained when the surfaces are completely free of oxide and foreign matter. Acid fluxes are available which are capable of cleaning the surfaces during the soldering operation itself, but the residues from these fluxes are usually highly corrosive. Electronic assemblies are especially vulnerable to this corrosion, and in our industry, nearly all soldering work involves the use of mild rosin fluxes of limited cleaning power. Therefore it is essential that the surfaces to be soldered are carefully selected and treated.

Clean copper is a highly solderable metal but oxidation films tarnish the surface and begin to form on copper immediately after cleaning, and these films are not easily removed by weak rosin fluxes. The copper tracks of a circuit board are therefore protected by a solderable coating during subsequent storage and handling. The soldering coat may also act as an etch resist during the manufacture of the p.c.b.

Solderable coating fall into two main categories: those that are wetted by the solder but not completely dissolved; and coatings that are displaced or dissolved by the molten solder at normal soldering temperatures (250° to 260°C). The first category usually implies the use of gold or silver plated onto the tracks. The plated layers need to be thick (and expensive) and gold/tin alloys form brittle joints with poor bond strength. Similarly thick silver deposits can lead to silver migration in the presence of moisture and direct current.

Thus the majority of solderable coatings fall into the second category and are dissolved or displaced by the solder. Rosin lacquers are widely used in the production of p.c.bs for domestic electronics. They offer protection for a few weeks. However, the lacquers are permeable to the atmosphere and allow oxidation of the underlying copper.

Thin precious metal coatings are dissolved by the solder. Silver coatings are prone to sulphide tarnishing which impairs its solderability and can be given a gold flash. This is turn is porous and so offers only a limited protection against tarnishing. It has a similar effect when used directly on the copper, and only prevents oxidation for a short period.

Good solderability and electrical contact properties are obtained from a tin/nickel coating with a gold flash covering, but the most convenient and widely used is thick pure tin or a tin/lead coating which has the same composition and properties as the solder to be used (60% Sn to 40% Pb). These coatings provide the best surface finish and retain solderability for long periods under good storage, i.e. clean and dry conditions.

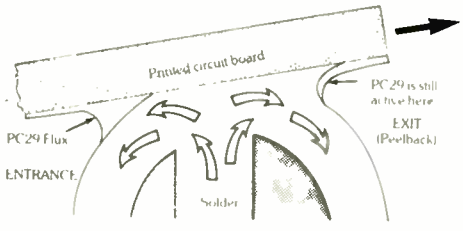
A reflow and curing oven for surface-mounted devices. Four heating zones can vary the heating cycle profile. – Zevatron GB, Dover.

There are several methods of applying such a coating. Hot methods involve dipping or roller tinning the p.c.b. but probably the most consistent, offering a uniform layer with longer protection (measured in years), is offered by electroplating. The advantages of hot coating and electroplating can be combined by the reflow soldering of electroplated coatings. This technique is often combined with the production of the p.c.b. tracks. The electroplated tin/lead coatings are widely used as etch resists. When the bare copper is removed by etching, the deposit then acts as a solderable coating. The fusing of the coating onto the tracks offers a number of advantages: A firm metallurgical bond is formed between the copper track and the tin/lead deposit; it leaves a bright, smooth surface which increases the shelf-life of the board; it reveals any soldering faults, and the shape and quality of the etched tracks; it covers the edges of tracks left bare by the etching process.

The same considerations apply to the solderability of component terminations which are to joined to the p.c.b. or indeed to each other. Tinned copper wires have the same characteristics as the copper on the board, but it must be stressed that the coating must be of sufficient thickness to retain solderability. Hot-dipped wire often displays very uneven coating when examined closely. When brass terminals are plated directly with tin/lead or tin coatings, zinc will diffuse rapidly to the surface of the coating and form zinc oxides which cannot be removed by rosin fluxes. It is therefore necessary that a barrier layer of matt copper or nickel is used as an undercoat to the coating. Kovar is a nickel-iron-colbalt alloy used in glass-to-metal seals and shows poor solderability and is difficult to clean chemically. It is often necessary to electroplate the Kovar with nickel to improve the adhesion of the solder.

Testing for solderability is by rotary-dip, which measures the time taken to give complete wetting of a surface. Round wires are dipped into a globule of molten solder and the time taken for the globule to join round the wire is measured. Another test is the area covered by a given quantity of solder over a surface. A wetting balance measures the force of the surface tension against the buoyancy of a component or wire immersed in molten solder. Visual inspection is also necessary. Tests should be carried out on p.c.bs and components before commencing a production run.

How wave soldering works.



WAVE SOLDERING

A wave soldering machine transports a p.c.b across a 'wave' of solder, produced by pumping molten solder up through a slot across the path of the feed and above the level of the surrounding solder bath. This forms a moving sheet of liquid solder. Machines vary in minor details; differences are mainly in the wave shape and size. For example, the provision of a deep wave for soldering extra-long terminations on boards, such as backplane wire-wrapping pins. Other refinements include the injection of oil into the wave or onto the surface to improve wetting by the solder and eliminate solder dross. Oil injection has also been shown to reduce solder bridging as has the use of an inclined conveyor which assists the drainage of solder back down the circuit board.

Component leads can be formed or bent to anchor them to their locating holes but for economic reasons this is often omitted. Straight leads plugged into the board have a tendency to float while passing through the wave and this can lead to complete displacement of the component. Pressure pads or

rollers can be applied to the upper, component, side of the board to prevent this. Re-useable, heat-resistant jigs can also be used to hold the components in their correct positions. Another solution is provided by passing the board with long, uncut leads over a deep wax wave. The projecting leads can then be cut by tungsten carbide slitting discs. The wax is melted off in an oven and the assembly is wave soldered. This process is an alternative to a double solder wave where the long leads are soldered, then cut and resoldered to cover the cut wires.

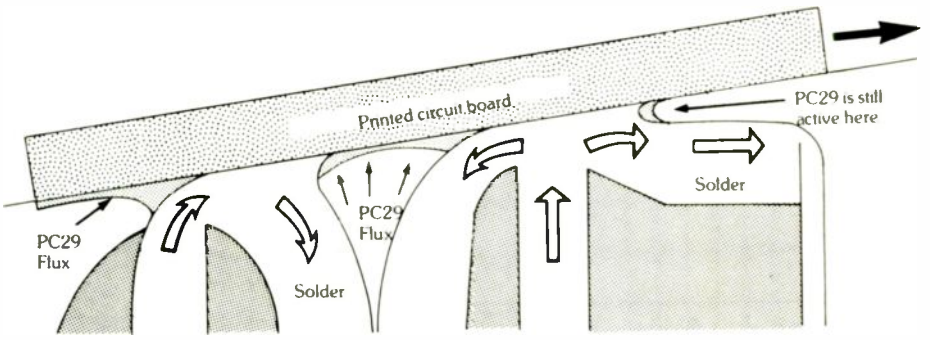
One recurring problem in some wave soldering processes is the formation of 'icicles' or stalactites of solder running down from the pins, which could cause short circuits. These can be removed by the use of a hot air jet or 'knife' to blow off the excess solder immediately after the soldering wave.

A new low-cost, compact wave soldering system for p.c.b. assembly comes from Hollis Europe. Prices for the new Europa machine, which is manufactured in the UK, start from as little as £4,000. This low price means that it is possible to buy a full wave soldering system for a cost typically associated with the less effective jet-wave soldering systems on the market. The cost of a new Europa is similar to that of many second hand machines currently available. The Europa features an ergonomically designed front panel to simplify operator control and monitoring while also being equipped with an efficient wave. In addition there is an oil-intermix capability that enables a user to select either dry soldering or oil-intermix operation if required.

The totally self-contained machine can fit either a customer-supplied bench or a rugged cabinet designed by Hollis for the purpose. The machine is designed to handle a maximum p.c.b. width of 305mm and features adjustable conveyor speeds from zero to 3m/min with a full Z-wave configuration and optional quartz pre-heaters. Either a pallet style (carrier type) conveyor or palletless (finger type) adjustable-width conveyor is available. All the conveyors are inclined to achieve optimum soldering results. Fluxing is carried out using a corrosion resistant polyethylene foam flux. The standing head of foam is adjustable to a maximum height of 15.9mm using either a rosin-based or water soluble foamable flux. Flux density control is an option. The wave is about 65mm wide to allow for longer component lead immersion at faster soldering speeds. The maximum wave depth is 12.77mm. The Europa is able to accept a dual-wave option if surface mount wave soldering is required. Hollis Europe is the UK offshoot of a US parent; Hollis Automation. They have set up a manufacturing and marketing centre in Chatham, Kent.

The best performance for wave-soldered p.c.bs is obtained when attention is paid to the design of the board itself. Studies have shown that the land and conductor geometry and size, and the pad shape and orientation with respect to the soldering direction can all have an effect on the quality of the product.

Some components not previously considered for wave soldering can now be assembled in this way. For example, leadless chip capacitors can be attached to the non-component side of a p.c.b. by heat-resistant adhesive. The solder-coated end pads can then be wave soldered to the board.



A double wave is used to solder s.m.cs.

There are areas of the board that do not require soldering and these can be protected by solder-resistant coatings or films. These help to reduce the percentage of defects and the solder consumption. Du Pont have recently announced a range of 'Vacrel' dry film solder masks. There are a number of solder resist films that can be cured by ultraviolet light.

REFLOW SOLDERING

Reflow soldering is the process of fusion of preplaced solder which may be printed on as a cream or paste, hot dipped onto the surface, or applied as an electroplated coating. The procedures can be applied to individual joints as well as being used for mass soldering a complete p.c.b.

The commonest forms of heating for this process are: immersion in a hot fluid, radiant, electrical conduction or induction heating. One of the widely used systems is the liquid which is usually a proprietary mixture of oils and waxes, or glycols and glycerols. To avoid thermal shock the board is preheated in a warm bath before the reflowing by brief immersion in the hot fluid. The trend is towards fully automated conveyorized systems. The other system most often used is heating provided by infrared radiation. The heating often takes place in an enclosed chamber or oven in an inert atmosphere which reduces oxidation and can remove the need for flux. Vapour phase soldering (also called condensation soldering) is a similar system which uses the vapour of a boiling heat-transfer organic fluid condensing on the surface of an electronic assembly containing solder preforms in such a manner that the heat from the condensing fluid melts the solder. The fluids are typically fluorinated compounds which boil at 20 or 30°C above the fusion point of tin/lead solder. Montedison UK produces the Galden range of perfluorpolyether fluids with boiling points between 165° and 265°C which in addition to their use in soldering can also be used for curing polymers and resins, and for thermal shock testing. The vapours from these fluids are inert and non-flammable.

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Hollis Europe, 109 Hopewell Drive, Chatham, Kent ME5 7NP. Tel: 0634 404677
Du Pont (UK) Ltd, 105 Wigmore Street, London W1H 0EL. Tel: 01-408 6362
Montedison UK Ltd, 7-8 Lygon Place, Edbury Street, London SW1W 0JR. Tel: 01-730 3405
Multicore Solders Ltd, Wood Lane End, Hemel Hampstead, Herts HP2 4RQ. Tel: 0442 233233
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Zevatron GB Ltd, 1/3 Waterloo Crescent, Dover, Kent CT16 1LA

Soldering equipment will be featured in our New Products columns next month.

SURFACE-MOUNTED COMPONENTS

Surface mounting offers the highest component density on a p.c.b. The components are designed to be as small as possible and consequently the leads are thin and offer very little surface area for soldering. Unlike through-hole boards, the solder joint must also provide the mechanical anchor to hold the component on to the board. The methods have already been described; wave and vapour-phase soldering are used. There are additional techniques to ensure that the joints are successful. In wave soldering, closely spaced s.m.cs may mask each other in the solder wave so some joints may be inadequately soldered; jets or two successive waves of different velocity/shape are used to overcome this. It is also necessary to control the stand-off, or gap beneath the component to allow solder to fill the joint space. This can be done by including stand-off bumps on the component.

Vapour-phase and infrared reflow processes use solder pastes or creams and it is possible to include, for example small glass spheres in the cream to ensure stand-off Surface tension of good solderable joints will pull the leads of a chip carrier on to the connecting pads, but leadless resistors and capacitors with only two connectors can suffer from lifting or twisting if the ends have different solderability. The non-metal body of the component is often glued to the board with a heat-resistant adhesive before soldering.

The major component of solder is tin and soldering is the principle use of tin. The International Tin Research Institute, in Uxbridge, Middlesex is therefore very interested in solder and has undertaken many research projects.

Our thanks are due to the Institute for the information included in this survey, and to the manufacturers who have sent us details of their products. Particularly Multicore whose Soldering Manual was a useful source of information.

NEW ELECTRONICS AND RADIO BOOKS

Practical Electronics Handbook
Ian Sinclair
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Ian Hickman
0 600 3373 6 £5.50

Radio Amateur's Guide to Radio Wave Propagation (HF Bands)
F.C. Judd G2BCX
0 434 90926 2 £8.95

Guide to Broadcasting Stations
Philip Darrington
0 434 90303 5 £6.95

Beginner's Guide to Amateur Radio
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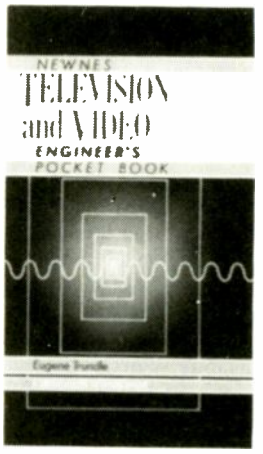

An invaluable reference source for practitioners in 'entertainment' electronic equipment – in all its guises. The wide-ranging text covers TV reception from VHF to SHF, display tubes, colour camera technology, videorecorder and videodisc equipment, videotext and hi-fi sound (with picture) systems. Without neglecting the basic theory, the emphasis is on modern equipment: frequency-synthesis tuning, satellite-dish head-ends, single-tube colour cameras, Video 8 tape format, depth-multiplex sound and digital servo systems. A long chapter describes test instruments, fault-finding and repair techniques, illustrating the points made with off-screen photographs.

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Toroidal transformers

Low profile, low cost and efficiency make the toroid an attractive alternative to conventional transformer cores

TERRY MONAGHAN

Over recent years there has been a large increase in the production of toroidal transformers at the expense of laminated types. Toroidal cores have always been ideal for use in instrument transformers because of their superior magnetic characteristics, but they were not widely used for power transformers mainly because of the high cost of the finished product.

With the modern toroidal winding machines and materials currently available it is possible to produce high-quality toroidal transformers at a low cost. Not only has the price differential between toroidal and laminated transformers narrowed over the last few years, but in many cases it is possible to produce toroidal transformers for a lower cost than the laminated equivalent. However, cost is not the only criterion used when choosing a transformer: there are certain physical advantages to be gained from choosing a toroidal.

THE HISTORICAL ASPECT

In 1831 Michael Faraday invented the transformer. For his prototype Faraday wound the coils on a soft iron ring or toroid, as it provided a continuous magnetic circuit with a uniform cross sectional area per magnetic length.

There were several drawbacks with the construction of Faraday's ring transformer for practical a.c. use, including eddy current

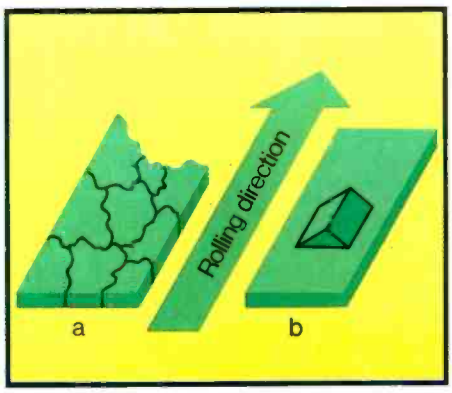


Fig.1(a). The steel sheet consists of grains which are joined together and arranged in a random manner. At (b), the grains are divided into small, cube-shaped domains less than a millimetre wide. The iron crystals are easiest to magnetize along the edge of the cube (domain axis). In g.o.s.s., most of the crystals align in the direction of rolling and act as one large crystal. In the unmagnetized condition, the crystals are magnetized in random directions, when an external magnetic field is applied the domain axes are brought in line to reinforce the field, making the flux far more intense.

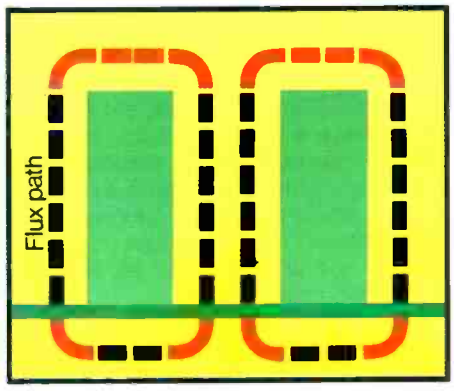


Fig.2. Flux path in E and I laminations. (Flux across the grain in red.) Approximately 40% of the flux is against the grain. As the magnetic properties of g.o.s.s. used across the grain are inferior to lower grade steels, flux densities as low as 1.3T are required to prevent high iron losses.

problems associated with a solid core. The shape of the core did not readily allow the use of mechanical winding techniques and the insulating materials (calico and twine) were not only bulky but were also hygroscopic and had to be impregnated.

To overcome the problems associated with high iron losses due to eddy currents, transformer cores were constructed from thin steel laminations, each insulated from one another. Initially, low-carbon steels were used; these not only suffered from high losses but they were also prone to ageing, (their magnetic properties deteriorated with time).

During the early 1900s silicon steel (4% silicon) was developed, which had certain improved magnetic characteristics and overcame the problem of ageing. Although iron losses decreased with increased silicon content, permeability and ductility also decreased, the latter causing problems with the punching out of laminations.

Further improvements were made to silicon steel's magnetic properties during the 1930s by cold rolling the sheets. As a result of the cold rolling process the grains (individual metal crystals) align in the direction of rolling. The resultant grain-oriented silicon steel (g.o.s.s.) has increased working flux density, increased permeability and reduced losses. However, the improvements in magnetic characteristics in g.o.s.s. are only realised when the magnetic flux is in the preferred direction, along the aligned grains (Fig.1).

To allow the use of mechanized coil winding and to avoid undue stress on the winding wires (which were originally textile wrapped), coils were wound on preformed bobbins, which were only suitable for use with stacked laminations such as E and I constructions. Unfortunately E and I and similar core assemblies have many drawbacks. Approximately 40% of the flux is at 90° to the preferred direction, allowing the use of relatively low flux densities in the order of 1.3 tesla (Fig.2); a large percentage of the core is only used as a return path of the flux; the stacking factor (percentage of steel per cross sectional area) is difficult to control; and the air gaps in the construction lead both to high reluctance (magnetic resistance) and fringing flux, where the flux leakage causes stray e.m.fs to be developed in adjacent leads, p.c.b. tracks and components.

To overcome some of the drawbacks of such constructions as E and I stacked laminates the C core was developed. The C cores are wound around a rectangular mandrel from strip g.o.s.s. (steel ribbon formed by slitting g.o.s.s. along the grain). The core is then stress-relief annealed for several hours in a special atmosphere, lightly impregnated and then cut in two to form two C shaped cores (Fig.3). To ensure that air gaps are kept to a minimum, the cut surfaces are ground, lapped and sometimes finally etched to form good mating surfaces. Because the winding tension is carefully controlled to give a stacking factor of 95%, excessive noise due to loose laminations or high iron losses due to excessive winding tension can be avoided.

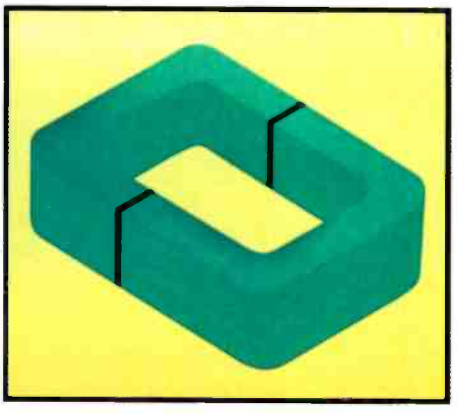
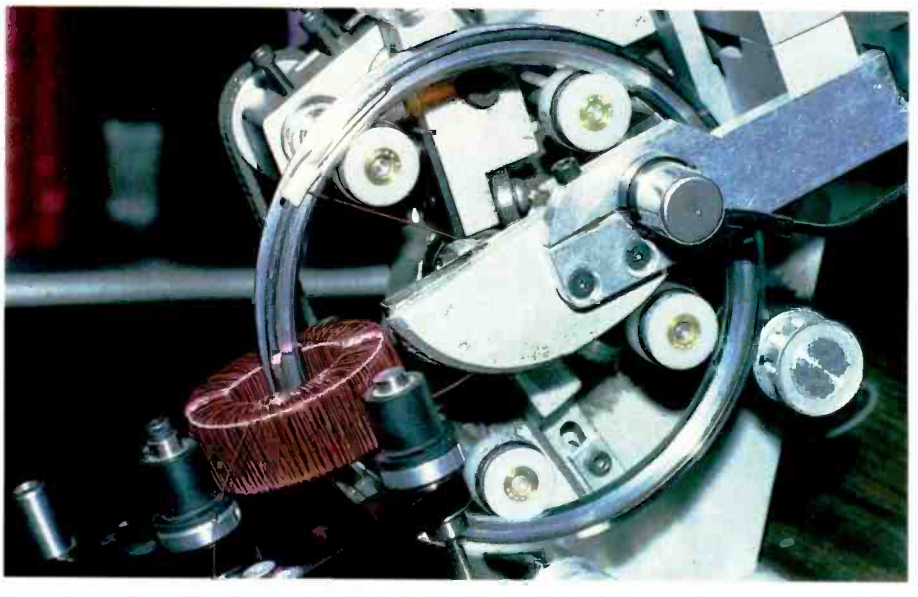
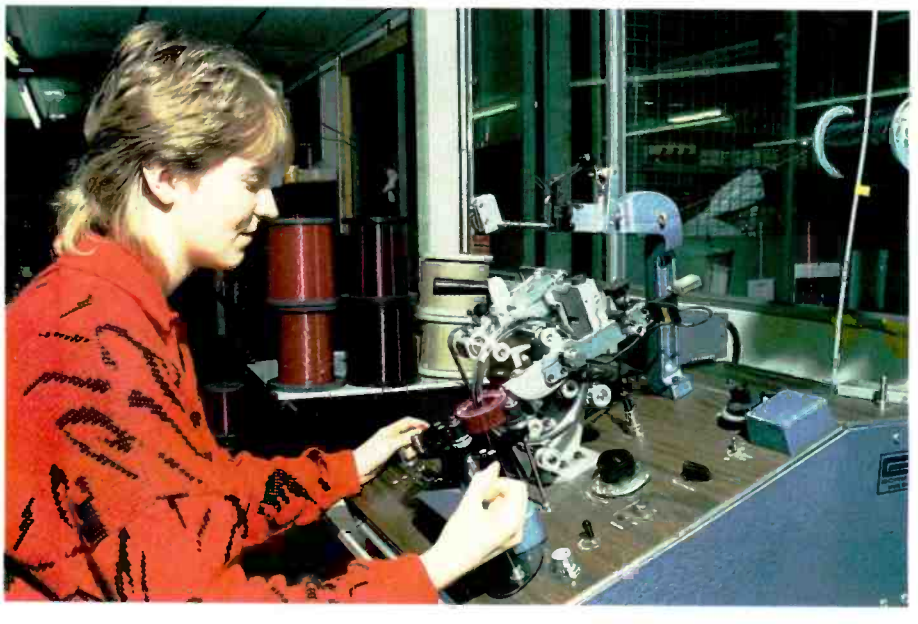


Fig.3. C Core. The C core allows the use of conventional bobbin winding and a performance that approaches that of a toroidal core. The finished transformer, however, is expensive to produce and magnetizing current is relatively high.

Fig.4. Random winding head. The use of random winding techniques allows the wires to be tightly packed, leading to more compact windings and better thermal conductivity than is offered by layer winding.

Fig.5. Winding machine. The winding process is labour intensive and dependent on the skills of trained operators.





The C core allows the use of conventional coil winding with a performance that approaches that of a toroidal core. However, the introduction of air gaps and bonding strains lead to higher magnetizing currents, and to a lesser extent higher iron losses than are achieved using toroidal cores. Because of the high labour content involved in producing the cores, the resultant high price rules out the use of C cores in all but very specialized applications.

The introduction of high-quality insulating materials has allowed a return to the toroidal core. Modern winding wires have polyster or polyurethane enamels which are extremely tough, flexible, high thermally rated and have excellent electrical properties. Not only is it possible to use random winding techniques, thus allowing more turns per square metre (Fig.4), but it is no longer necessary to impregnate coils to render insulation humidity resistant or to improve on thermal conductivity between turns.

Interwinding insulation based on polymeric materials is also tough, has high thermal and electrical properties and can easily be applied to the coils. Insulating

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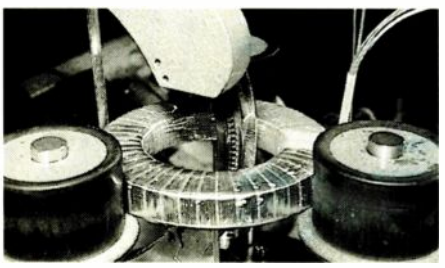
CONSTRUCTION OF TOROIDAL TRANSFORMERS

Toroidal core production is similar to C core production, except a circular mandrel is used; also, there is no need to fully bond and cut the core. If extremely low acoustic noise is required it is sometimes necessary to lightly bond the edges of the core to help damp any acoustic noise due to magnetostriction (the windings also help damp



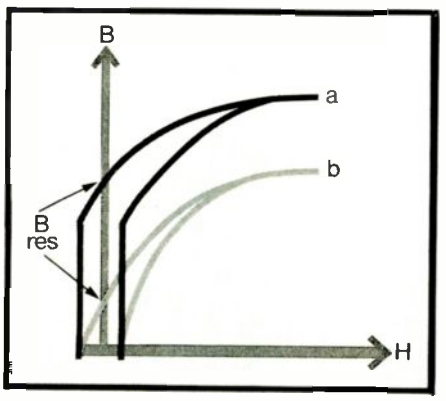


Fig.6. Copper screening tape applied by machine as well as polyester film, is applied by machine in one operation.

Fig.7. Effect of air gap. As air gap increases, residual flux (B_{res}) decreases: (a) without air gap; (b) with air gap. A high level of residual flux can result in momentary saturation (with resultant high inrush current) if energized in the same direction.

mechanical noise). The stress-relieved core is then covered with an insulating material such as expoxy or plastic to allow the use of direct winding. The finished core can be tested at this stage, and if it is not subjected to any undue knocks, it will retain its qualities through subsequent processes.

It was the introduction of high-speed toroidal winding machines (Fig.5) that helped reduce the labour costs involved in toroidal winding. Toroidal core winding is more dependent on the skill of the individual operator than conventional bobbin winding; the operator determines the correct winding tension, core rotation pitch and ensures the winding wires are not damaged in any way. The winding process is a mystery to most people until they see the operation, (after which some are still none the wiser). The winding process utilizes a ring-shaped shuttle which can be split to allow the core to be threaded on to it. The shuttle is first filled with the correct length of wire, the wire is then off-loaded on to the core whilst the core is rotated at the correct pitch to ensure complete core revolutions for the number of turns. Depending on the final application of the transformer, various winding techniques can be employed; for example, low stray field or low voltage between adjacent turns may be required. It is important that the correct quality of winding wire is used, as the winding speed is high and the wire follows a tortuous path.

When the correct number of turns has been wound onto the core, the shuttle is again split and the wound core removed.

Layers of interwinding and final insulation are applied on similar machines. Polyester films (Melinex, Mylar) have replaced cotton and other fabric tapes in most modern designs. They have a working temperature of 130°C, are extremely tough and have excellent electrical properties. For higher working temperatures, Nomex tape is used. With three layers of polyester film between primary and secondary windings, breakdown voltages in excess of 4kV are easily achievable as long as care is taken to avoid gaps in the insulation.

The finished transformers are suitable for use in most environments, but for high-humidity conditions, the transformers can be totally potted or varnish impregnated. Copper screening tape (consisting of copper strip encased in a Mylar envelope), is applied using the same machine (Fig.6).

ADVANTAGES OF TOROIDS

It is easier to control the magnetic characteristics and the stacking factor of a toroidal core than it is for a conventional stacked core, which is assembled after coil winding. A low stacking factor would not only lead to mechanical vibration, but it could also lead to there being an insufficient cross sectional area of steel, causing increased flux density. A high stacking factor, on the other hand, would cause a mechanical stress on the material, which would not only lead to increased iron losses, but permeability would decrease and magnetizing current would increase. Because all of the g.o.s.s. is in the preferred direction it is possible to take full advantage of the material. Toroidal cores are usually run at the top of the knee on the B/H curve (1.6 to 1.7T) whereas laminated cores are usually run at 1.3T to avoid high iron losses. By referring to the transformer equation, $E = 4.44 BANf$ (where E = induced voltage, B is peak flux density in tesla, N is number of turns and f is frequency in Hz), it can be shown that less turns per volt are required when operating a given cross-sectional area, A, at increased flux density. By using 1.6T as opposed to 1.3T, large savings can be made in the amount of copper used, leading not ony to large cost savings, but also space savings and reduced copper losses.

The missing air gap gives a lower-reluctance magnetic circuit than is possible using conventional and C cores, so that the magnetizing current tends to be extremely low on toroidal transformers. Iron losses also tend to low — typically 1.1W/kg, again due to the g.o.s.s. being used in the preferred direction.

As the windings are spread evenly over the entire core, the effective surface area for dissipating heat generated by copper losses is large. High current densities can be used to take full advantage of the insulating materials used. For a temperature rise of 60-70°C, a current density of $5A/mm^2$ is used on low-power transformers and $3A/mm^2$ on high-power transformers (although in many applications low regulation or low temperature rise is required).

Due to the virtual absence of fringing flux, stray field tends to be much lower on toroidal transformers than conventional and C core types, and by wrapping layers of g.o.s.s. around the circumference, stray flux can be further reduced.

Single-hole fixing simplifies mounting, (toroidal transformers are usually supplied with a mounting kit). The transformers can also be supplied with threaded inserts, studs and mounting frames.

The use of high flux and current densities accounts for toroidal transformers being approximately half the weight and volume of conventional transformers.

DISADVANTAGES

The missing air gap leads to a square hysteresis loop, which means that if the supply were to be removed when the flux is high, the core would be left with a high level of residual flux (Fig.7). If the core were energized in the same direction the core would saturate, causing a high current transient limited only by the primary impedance. By using anti-surge fuses (or soft-start circuitry on very large transformers) the high inrush current ceases to be a problem.

On a very distorted supply or if half-wave rectification is used, the core can become polarized and saturated in one direction. By using a symmetrical waveform in inverter designs, or by using full-wave rectification, the problem of core polarization is avoided.

APPLICATIONS

Toroidal transformers are very popular for use in low-profile equipment, such as racked audio systems. Another popular application for toroidal transformers is in tungsten halogen lighting, where both the efficiency and the shape of the transformer are prerequisites.

As it appears to be the norm to leave the transformer as one of the last components to be specified, the reduced volume of the toroid is the only option to be pursued in cases of very limited space.

The efficiency of toroidal transformers is extremely high, typically 90% at 120VA rising to 95% at higher powers, making them particularly useful in invertor applications.

Other applications include: 100V line systems, where good frequency response is required; use in monitors or preamplifiers where stray field is critical; use in medical equipment where high isolation is required and in any application where weight is to be kept to a minimum.

Terry Monaghan is a director of ILP Electronics Ltd, Canterbury.

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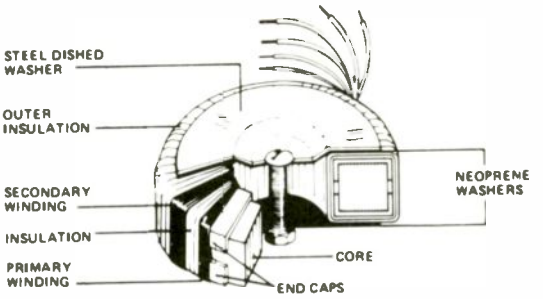

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ENTER 29 ON REPLY CARD

Wide-range absolute-value circuit

Full-wave rectifier offers design trade-offs of its 75dB dynamic range and 20kHz bandwidth.

T.J. WILMSHURST

Many circuits to full-wave rectify or find the 'absolute value' of a signal have significant limitations in terms of signal amplitude or frequency. Especially critical in the design of precision rectifiers is the handling of low-amplitude signals, when the rectifying property of a diode is not directly available, and the provision of adequate bandwidth to accommodate the extended harmonics that may arise due to the rectification.

The circuits discussed recently in this journal and elsewhere (refs 1,2 and 3) have wide dynamic range at frequencies up to 1kHz and extended frequency response for largish input amplitudes. Their performance progressively deteriorates for smaller signal amplitudes (10mV) at higher frequencies.

The circuit described here was developed to operate over a bandwidth up to 20kHz, with peak-to-peak signal amplitudes of 4mV to 24V, a dynamic range of 75dB. Some trade-off between bandwidth and dynamic range exists, however, and component values can be modified to optimize performance over other operating conditions.

The design is based on the conditional inverter circuit of Fig.1, which inverts if the switch is closed, and buffers if it is open.

Alternatively, if the switch is solid-state with a finite on and off resistance,

$$\frac{V_0 R_2}{R_1+R_2}+\frac{V_1 R_1}{R_1+R_2}=\frac{V_1 R_s}{R_3+R_s}$$

and if $R_1=R_2$

$$V_0=V_1\left(\frac{2R_s}{R_3+R_s}-1\right) \qquad (1)$$

Normally R_s(on) is a few tens of ohms, R_s(off) is very high indeed, and it should be possible to choose a value for R_3 to give $V_o = \pm V_1$. In this case however R_s(on) will be significant compared to R_3 (which must be kept low), so that with the switch on $|V_o|<|V|$ (slightly). A parallel resistor is added to modify R_s(off) so that a slight (and equal) attentuation also occurs with the switch open.

A further complication arises from the non-ideal behaviour of solid-state switches. When the control input changes state a

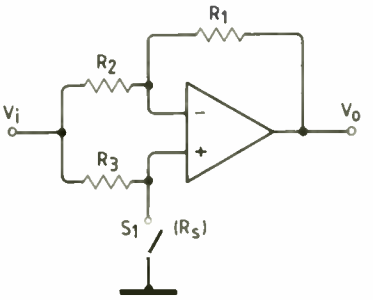
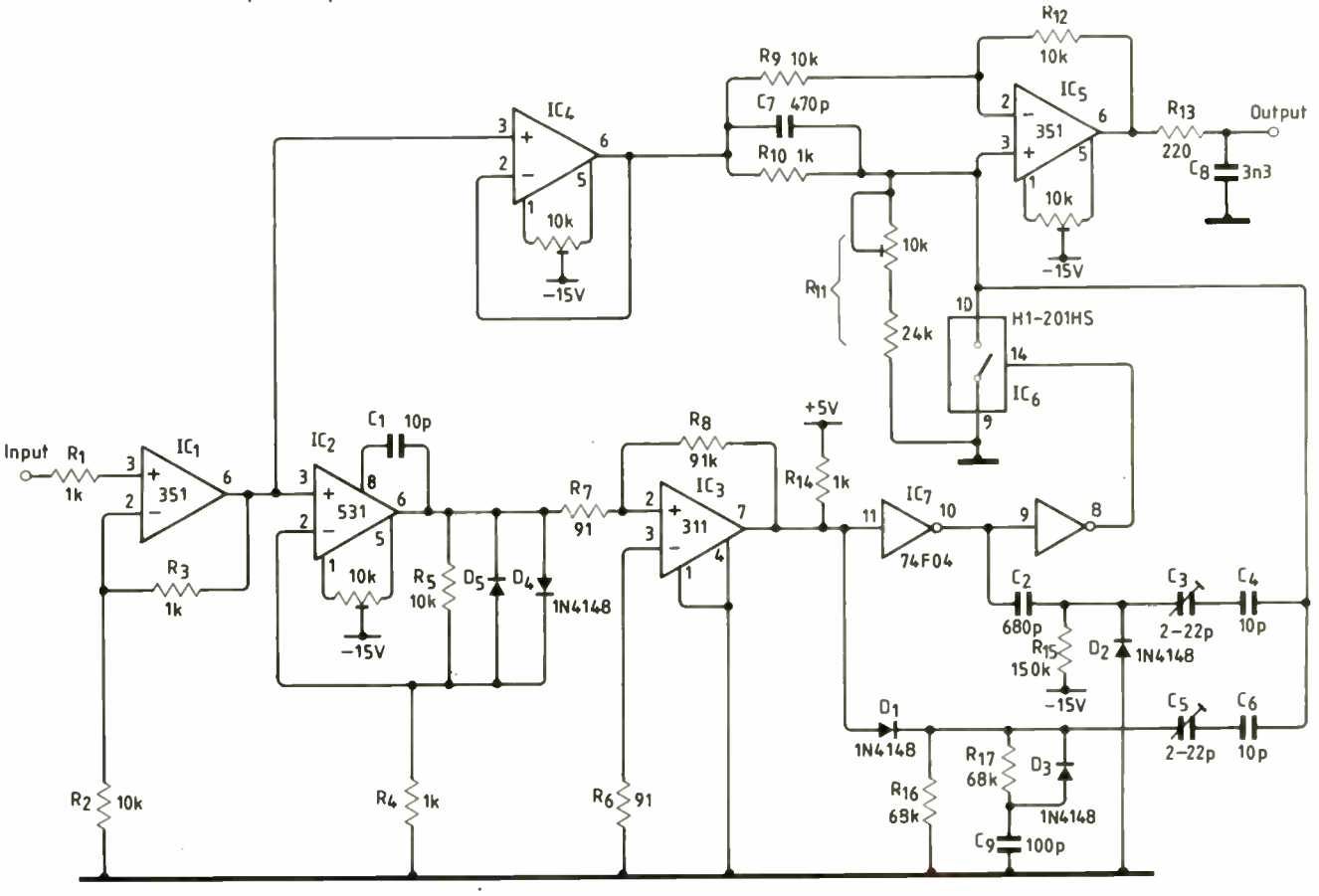


Fig.1. A 'conditional inverter' circuit inverts when S_1 is closed, and buffers when open. For unity inverting gain $R_1=R_2$, R_3 is non-critical.

Fig.2. Absolute value circuit in which ICs 5 and 6 form a 'conditional inverter', with polarity detection performed by ICs 2 and 3. Charge injection compensation is provided via the resistor-capacitor-diode networks driven from IC_7.



small charge is injected into the analogue circuit (due to the capacitive coupling between the two), and manifests itself as a voltage spike or step (depending on the associated circuitry) superposed on the analogue signal. If specified at all, the manufacturer either quotes the charge injected itself (ΔQ), or the voltage step (ΔV) induced in a specified load capacitance (C_L), where

$$\Delta Q = C_L \times \Delta V.$$

For the best complementary m.o.s. switches, $\Delta Q = 10pC$, giving for a load capacitance of 10pF, a spike of 1V! And other switches will be far worse.

Three techniques available to alleviate this problem are used here, each contributing toward its reduction. These are minimization of source impedance, low-pass filtering (usually possible because spike widths are very narrow), and charge cancelling. In the latter a charge equal and opposite to that induced by the switch is simultaneously introduced into the appropriate node. The switch used (a Harris HI-201HS, obtainable from RR Electronics) is a low charge injection type, typically with $\Delta Q = 10pC$.

In the final design (Fig.2) the conditional inverter is formed by op-amp IC_5 and switch IC_6. Polarity is determined by comparator IC_3, configured with a smaller amount of hysteresis, and preceded by a non-linear amplifier which provides necessary boost for low-level signals. The source impedance to the switch is effectively R_{10} in parallel with C_7. R_{10} is selected under the triple constraint of providing low source impedance (to allow rapid discharge of the injection charge), being large compared with $R_s(on)$, and providing a sufficently high load resistance for IC_4 when the switch is on. With the value chosen the 351 is on the verge of current limiting for maximum negative-going voltage amplitudes. Substituting now into equation 1 for the switch-on, $R_s(on) = 30\Omega$Y, gives

$$\frac{V_0}{V_1} = \frac{2\times 30}{1k+30} - 1 = -0.94$$

and to find R_{11} with the switch off:

$$\frac{V_0}{V_1} = 0.94 = \frac{2R_{11}}{1k+R_{11}} - 1$$

$$R_{11} \approx 32k\Omega.$$

The small amount of gain introduced at IC_1 compensates for this attenuation.

With the HI-201HS the charge injection was negative for both transitions of switch control input, so each had to be cancelled by a positive charge packet, the compensating transition coinciding precisely with the one to be cancelled. Cancelling is obtained with the pulse-shaping networks associated with IC_7. The rising edges of the waveforms (Fig.3) provide the cancellation, in each case the decay occurring when the switch is closed, so that no coupling of this part of the waveform into the signal takes place. Cancellation is optimized by the adjustment of C_3 and C_5. Residual spikes are further attenuated by the R_{13}-C_8 filter section. Having a 3dB point at around 220kHz, this meets the circuit bandwidth requirement described in the first paragraph.

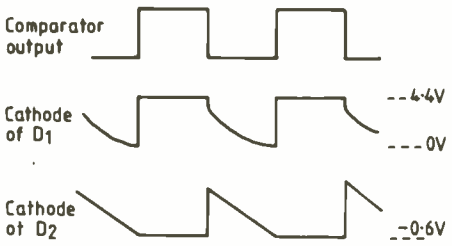


Fig.3. Waveforms that provide charge cancelling. Lower two waveforms are capacitatively coupled to the solid-state switch, the rising edges cancelling the charge induced by the switch. The decaying part of the waveform has no effect on the main signal as it occurs when the switch is closed, and the node in question effectively earthed.

RESULTS

The circuit as described has been constructed and tested using several samples of the HI-201HS switches. Some difference between these was found, but a reduction of charge injection to less than 1mV at output was obtainable. The circuit continues to operate with inputs of less than 1mV pk-pk, although here the charge injection, as well as distortion introduced by the comparator hysteresis and charge-cancelling circuits, renders the output of little use.

Four millivolts was reckoned to be the smallest input voltage leading to acceptable levels of output distortion, although this must depend on application. Frequency-dependent components, such as those governing decay time in the charge-cancelling circuits and those of the output filter, have been selected for the 20kHz bandwidth. If some increase in minimum signal amplitude is acceptable the bandwidth may be extended, bearing in mind also that the maximum output amplitude will not be available at higher frequencies.

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T.J. Wilmshurst, M.Sc., M.I.E.E., is technical officer at the University of Cambridge engineering department.

Brunel given Siemens v.l.s.i. design tool

A v.l.s.i. design system recently donated to Brunel University provides graduate and post-graduate students with state-of-the-art facilities for education and research on current integrated circuit design techniques. The Venus system, developed by Siemens and already installed at numerous facilities within Siemens AG as well as at 18 German universities, is claimed to be the most extensive system available in Europe. The gift is a 7·530 mainframe computer with BS 2000 operating system, disc file storage, graphic workstations and various software from Siemens. It is of vital importance, according to Brunel's electrical engineering and electronics department, because funding of higher education being what it is, universities often have to make use of yesterday's technology, and it is through the generosity of such firms as Siemens and earnings from research contracts that it is possible to keep abreast of the latest facilities, say Brunel.

The gift goes beyond the provision of basic hardware and software, because the University will now be able to have designs manufactured on the 1.5-μm c-mos process and then have the chips returned to the researchers for evaluation. "These facilities are basic to modern electronic engineering" commented Prof. Gerald Musgrave, head of Brunel's department of electrical engineering and electronics and a director of Siemens Ltd, "as basic as having the latest microprocessor components, and play an essential role in the training of engineers for the next quinquennium".

Implementation of custom-designed circuit functions directly onto a v.l.s.i. chip has become of interest because of the sometimes costly programming associated with standard microprocessors, explained Dr E. Hoerbst, head of Siemens Venus project, and the combined advantages of higher processing speed, shorter development times, reduced component-count and improved design security all add to make application-specific circuits attractive.

It was to meet the shortage of trained circuit design specialists conversant with the use of c.a.d. tools that Siemens established the Venus design centres, said Prof. Schwaertzel, head of Siemens i.t. research, enabling industry-oriented design to be introduced into teaching courses and research programmes. Siemens is already manufacturing circuits designed by these universities on its Munich production lines.

The individual design steps for which c.a.d. support exists are development, simulation, construction, testing and the preparation of fabrication and test data. Development support begins with the logic synthesis, when a particular mathematical function is synthesized from basic logic elements, but the most vital part of c.a.d. support is the use of simulation to replace the costly and time-consuming testing of prototypes. Simulators and models are available for each level of chip design and there are tools for design analysis and evaluation at each level as well as the data-base holding the documentation – all necessary to ensure design security.

Conversion into a layout for the particular 'technology' employed is accomplished by cad-based chip construction procedures that place and route prefabricated or computer-generated circuit components; then a 'testability' analysis shows to what extent the subsequently produced v.l.s.i. chip has to be tested to guarantee fault-free working. The cad system provides fabrication test data ready to be passed on to the silicon foundry.

SATELLITE SYSTEMS

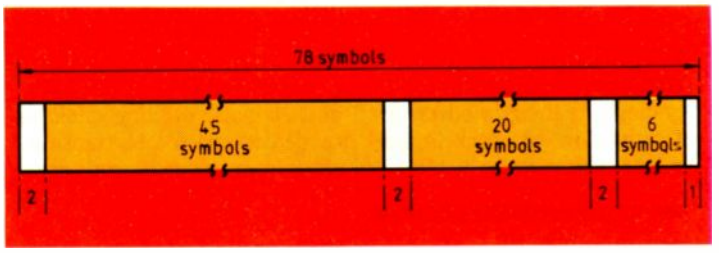
TOM IVALL

Bookie-MAC

The B-MAC television transmission system, as well as being used for satellite signal distribution in Australian broadcasting (December 1986 issue, p.65) is now appearing in a quite different role in the UK. Through a transponder of an Intelsat V comsat, it is sending live, private television coverage, including sound and teletext, of horse races and other betting-related events to thousands of betting shops throughout the country. The idea, of course, is to provide the punters in these shops with immediate information on which to base their bets. By the end of 1987 at least half of Britain's 10,000 betting shops are expected to be fitted up with the necessary t.v.r.o. receiving and display equipment.

B-MAC has been chosen for signal transmission because it was specifically designed to combine the advantages of the multiplexed analogue components (MAC) principle with means of encryption, scrambling and individual addressing of receivers. It was originally developed, in fact, for such private service use in a scrambled pay-tv system. British Telecom are supplying the satellite network, claimed to be the largest private one in Europe. For the signal transmission part they have chosen B-MAC equipment made by Digital Video Systems (now owned by Scientific Atlanta), who developed the B-MAC system in the first place. The hardware and software will be used to deliver a scrambled vision signal plus six separately addressed audio channels and a data channel for the teletext.

The whole network, valued at £70 million with £26 million going to BT, was ordered by a new company called Satellite Information Services Ltd (SIS). This is 45% owned by the big four UK betting groups, namely Ladbrokes, Coral, William Hill and Mecca, while the remaining 55% of the equity is held by the Racecourse Association, the Horse Totaliser Board and a number of independent directors. The system arrangement specified by the four betting groups was for a common television picture of an event but with an individual sound commentary from each group to be sent exclusively to the betting shops belonging to that group.



The private vision and sound signals from events are sent via BT's existing outside broadcast vision network to the Telecom Tower in London and from there to a nearby control centre operated by SIS Ltd. Here the programme material is edited and packaged for its recipients. The edited signals – vision, sound and data – then pass from the SIS control centre back to the Telecom Tower, which relays them via dedicated links to BTI's London Teleport in the docklands. At this earth station, an earth terminal transmits the encoded B-MAC signals to the Intelsat V comsat, in geostationary orbit at 27.5° W. Then the leased full transponder in the satellite broadcasts the signals to t.v.r.o. earth stations at betting shops throughout the country.

Each earth station has a 1.3-m receiving dish, an external frequency downconverter (jargon term 'low-noise block' or l.n.b.), a receiver, a B-MAC decoder with de-scrambling and decryption, and picture display equipment. Various display options are available, ranging from one tv screen with full sound and teletext services up to 12 screens continuously showing pictures or teletext with means for punters to select teletext pages not already on display.

The B-MAC system uses the same principle of time division multiplexing of time-compressed vision signal luminance and chrominance (analogue *Y*, *U* and *V*) as other MAC sytems. It is also similar to other MAC systems in the general sense of using the remaining time in the tv waveform to transmit digital data. This data carries digital audio and other information such as teletext and system control signals. But the B-MAC system differs, first in the way it organizes the digital data and secondly because it scrambles the vision part of the signal and encrypts the data in order to prevent unauthorised reception.

The time division multiplexer works in sample increments of about 47ns duration (1365 of them making up one tv line of the 625-line standard). In one B-MAC line, 750 samples are allocated to the luminance components and 375 samples to the chrominance component. These are separated by a transition period of six samples. The remaining 234 samples are allocated to data symbols (see below). Each data symbol is three sample periods in duration, making 78 symbols (234/3) in a 625 tv line. The instantaneous symbol rate is 455 + line frequency, or 7.11 Msymbols/s, and the average rate is 1.22 Msymbols/s.

Six digital audio channels and a clock reference burst (for recovering system clocks at the receiver) are transmitted during the line blanking interval of the television waveform. The time available, 11 μs in the 625-line waveform, is occupied by 78 symbols, as mentioned above. Each symbol is in fact a pair of bits in a four-level system. As shown in Fig.1, 20 symbols are reserved for the clock reference burst, which is generated as two-level data and consists of 10 cycles of 277.5 + line frequency. The average level of this reference burst defines zero chrominance level. The remainder of the time is filled with data proper and four short separation periods.

Dolby Deltalink encoders supply the digital audio data. Each audio channel contributes 13 samples and one control bit during a line period. The function of the control bit alternates from line to line, between step size and de-emphasis control. Two error concealment bits are added for each channel and a parity bit protects the six error concealment bits in each data stream. Two bits remain in each data stream and these are used to provide a utility data channel (RS-232 standard). The audio data may be reassigned to an auxiliary data port.

Teletext data is transmitted during the field (vertical) blanking interval – a period of 25 lines plus 1 line blanking interval in the 625-line standard. This available time is organized into packets, each of which contains 377 symbols and is allocated to a particular function. For example, teletext packets are transmitted on five tv lines (9 to 13) of the field. The other line periods are occupied by packets of data for clock recovery and synchronizing at the receiver, for system control purposes and for individual user data. This last-mentioned is addressed to individual receivers and used for sending them keys for de-scrambling.

Pictures are scrambled by varying the duration of the line blanking interval, in a random sequence of blanking times determined by the programme originator. This causes the tv lines to be randomly displaced in time. As there is no line sync information on a B-MAC tv line, an unauthorized receiver cannot easily restore the original line timing. To achieve maximum privacy the scrambling pattern is varied randomly and non-repetitively. The B-MAC encoder generates a scrambling pattern based on an arbitrary number called a 'seed', and a new, unpredictable seed is produced by a random number generator every 0.25 second. For de-scrambling at the receiver this seed is transmitted to it as data during the field blanking interval as mentioned above.

A similar principle is used for encrypting the digital audio and teletext data. The encryption process is determined by an algorithm, and this algorithm is based on an arbitrary number 'seed' which is changed randomly. Again, the seed, and hence the algorithm, is changed every 0.25 second. And, as with the vision scrambling, the seed is transmitted to the receiver as data during the field blanking interval and used there for decryption to recover the original digital audio and teletext.

Finally, to prevent technically

SATELLITE SYSTEMS

sophisticated pirates from breaking into this whole system, the seed numbers (and the rest of the sytem data) are themselves encrypted. This is done by a process similar to that used in the audio and teletext encryption, with an arbitrary number determining the encryption algorithm. The arbitrary number is known as the key, but is not fixed, as it could be discovered and revealed. So the key is changed by human choice at any time or frequency decided by the programme originator. He enters a new arbitrary number on a keyboard connected to the encoder. The changed key is distributed to all receivers by sending it to their decoders, one at a time, in data packets individually addressed.

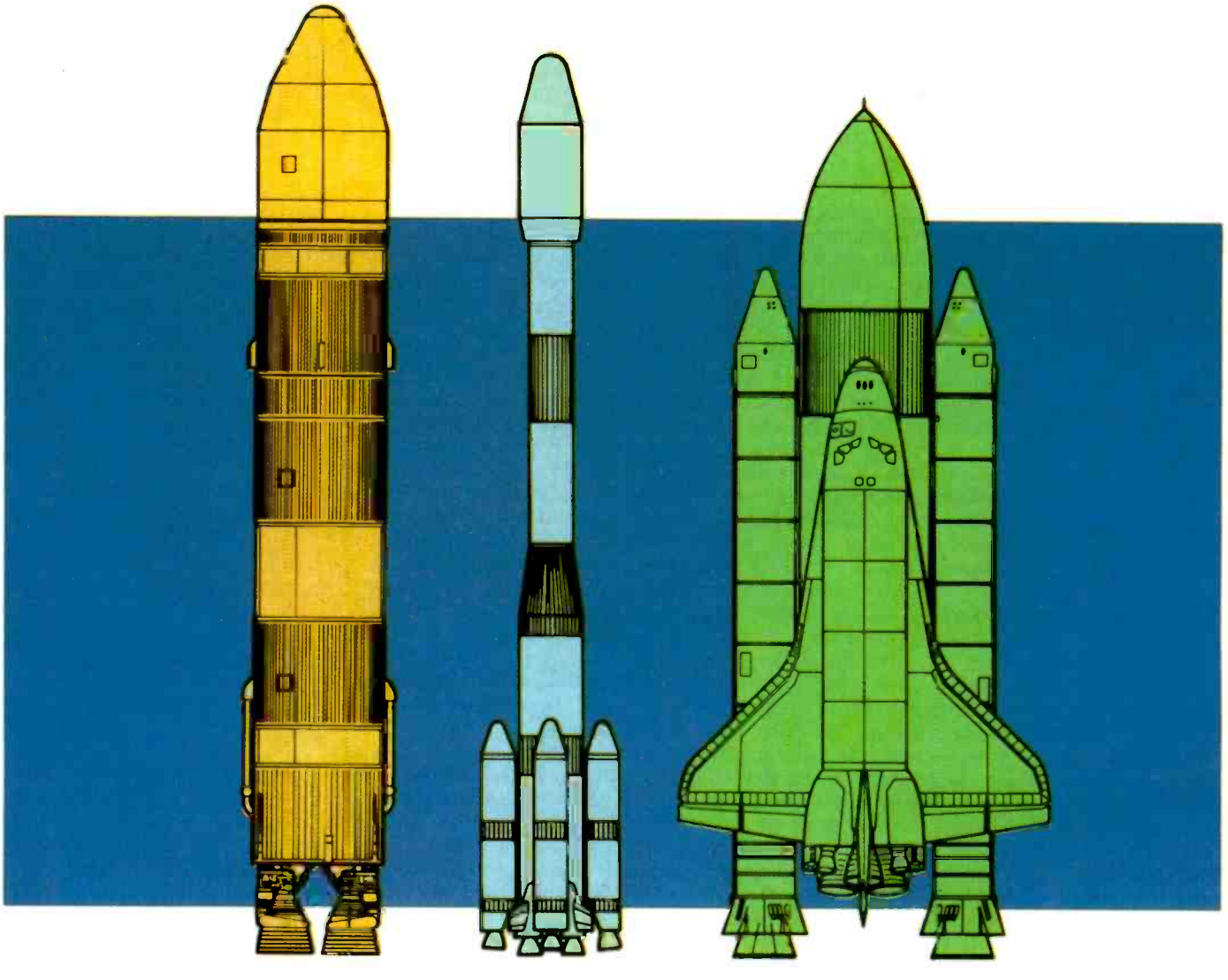
Rescue system consolidated

About 600 people have been rescued from marine, aeronautical and other accidents with the aid of an international satellite system operating since 1982. Called COSPAS-SARSAT, it uses low-altitude polar orbiting satellites to detect and locate transmissions from rescue beacons operating on the 121.5-MHz and 406-MHz allocated frequencies. As the earth revolves on its own axis the spacecraft, moving in their longitudinal paths, progressively scan the whole surface for beacons. A typical spacecraft altitude is 870km.

The system was founded by Canada, France, the USA and the USSR. Other participants are Bulgaria, Finland, Denmark, Norway and the UK. Now its organization is being strengthened by an administrative base and secretariat provided by INMARSAT at its London headquarters. This is making available technical, conference, administrative and international liaison services.

COSPAS is an acronym derived from the Russian words

The proposed satellite launcher on the left, compared in size with two current vehicles, is intended to place satellites in orbit at about half the price per kilogram of payload of that necessary with existing boosters. It would be capable of putting up several spacecraft on the same flight, to go into different orbits. Six Navstar satellites for the Global Positioning System could be launched together. Named after Gregory Jarvis, the Hughes engineer killed in the Challenger shuttle disaster, this expendable vehicle proposal is the subject of a study contract being undertaken jointly by Hughes and Boeing for the US Air Force. If it were constructed, Boeing would build the first two (lower) stages, designed to separate three minutes into the launch, while Hughes would build the third stage carrying the satellites.



SATELLITE SYSTEMS

'search and rescue' and in fact the system includes a Soviet satellite, COSPAS-1, which was launched as Cosmos 1383 in 1982. Similarly, SARSAT is an acronym for 'search and rescue satellite' and refers to a NASA spacecraft of that name launched in 1983. Also contributed by the USA is the services of the NOAA-9 weather satellite (of the National Oceanographic and Atmospheric Administration) launched in 1984. As well as transmitting weather pictures and other meteorological information, this spacecraft carries electronic equipment specifically for the rescue system.

Incidentally, INMARSAT recently increased its membership to 48 countries when Indonesia joined the organization late last year. Sea and air transport are particularly important to this country, geographically a group of islands.

Coming launches

After the loss of NASA's Challenger space shuttle and ESA's Ariane flight V18, both in 1986, there has been something of a hiatus in new satellite launchings while NASA and ESA investigate the causes of these disasters. It seems likely that Arianespace will be the first to start moving again, with a provisional schedule of launches for the ESA. Their flight V19, planned for February 1987, is intended to be an Ariane 3 launcher, carrying an ECS-4 comsat and the third multi-purpose satellite, K3, for the Australian Aussat system described last month. Alternatively Aussat-K3 could go up on flight V22 in July of the same year.

The next Arianespace flight, V20 in April 1987, is intended to launch Europe's first d.b.s. spacecraft, the West German TV-Sat 1. This is similar in technical design to the French TDF-1 d.b. satellite, which is also scheduled for a 1987 launch, in September on V24 or in November on V25, now that the French government has given its official go-ahead. A new meteorological satellite, Meteosat P2, is planned for launch in June by the latest ESA vehicle, Ariane 4.

NASA plans to resume space shuttle launches in February 1988, after a two-year gap. Ths US government has asked them not to take on any more commercial or foreign payloads except those designed specifically for the shuttle or those necessary to US national security or foreign policy.

So for about seven years after the resumption of operations, 41% of shuttle launches will be American military satellites, 47% will be for NASA's own activities – mainly scientific spacecraft – and the small remainder for commercial, foreign government and US government civil space projects. The first mission, flight 26 planned for 18 February 1988, will be to launch a tracking, data and relay satellite that will form part of NASA's own operational system for putting up spacecraft.

But from past experience of delays and accidents, all these dates and arrangements should be taken as highly provisional.

Launching satellites as a business activity is no longer confined to the nations of the West. The USSR with its Proton rocket and China with its Long March vehicle are also competing in this field. For example, there is an agreement for China's Long March 3 rocket to launch a comsat called Pan Am Pacific 1 in 1988. This spacecraft is owned by the American company Pan Am Pacific Satellite Corporation and is actually the Westar 6 satellite which went into a wrong orbit in 1984 and was later brought back by the space shuttle. Direct broadcast satellites owned by the US firm Dominion Satellites are also due to be launched by Long March rockets.

Space pioneer retires

Thirty years after Sputnik-1 startled the world, the space business has now existed long enought for a person to grow old in. One such person is Dr Fred P. Adler, who is on the point of retiring from the Hughes Aircraft Company. Dr Adler was head of a division in this company which developed Syncom, the first synchronous communications satellite. In 1963 Syncom II made history by providing a telephone link between the American president John F. Kennedy and the Nigerian prime minister Abubakar Balewa. This Hughes division also developed Early Bird, also known as Intelsat-1, which was the first commercial comsat. In operational service from the mid-1960s, it had a capacity of 240 telephone circuits or one television channel and kept going for four years.

Dr Adler's division also developed the unmanned Surveyor spacecraft, which performed five successful landings on the moon. In later years he became well known in the UK as president of the consortium which built NADGE, a computerised air defence system for NATO.

Following the death of Howard Hughes, who had become an eccentric recluse, the Hughes Aircraft Company went through a very complicated period as far as its ownership and financial affairs were concerned. But this major satellite manufacturer has now settled down as part of GM Hughes Electronic Corporation, a wholly-owned subsidiary of General Motors. It was bought by GM in December 1985.

Shoe-box size ship terminal

A cheap, compact earth terminal about the size of a shoe-box is likely to be available for use on ships some time during 1988. Intended for low-speed (600 bit/s) satellite data communications, it has been designed by engineers at INMARSAT (the International Maritime Satellite Organization) and will be undergoing trials in 1987. The prototype measures 12 × 8½ × 5 inches, including the antenna, and weighs only 6 kg (13 lb). Production models will be packaged for mounting on a mast or on the superstructure of a vessel.

INMARSAT sees this satellite system as an attractive alternative to standard h.f. marine communications. Estimated to cost the customer about £3500 – a sixth of the price of many existing ship terminals – the equipment is expected to find a market among the world's 175,000 or so commercial vessels and two million pleasure craft. Land mobile communications is another possible application to be tried out in 1987.

Signals are received and transmitted by a quad helix omni-directional, non-stabilized antenna. For reception (at 1.6 GHz) this has a minimum G/T of – 24 dB/K. After amplification and demodulation the signals are decoded to the ASCII data transmission code and sent via a memory to the data terminal. Transmission, at 1.5 GHz, takes place through a 20W r.f. power amplifier.

A synthesizer tunes the transmitter and receiver in 5kHz steps. The equipment is controlled by a microprocessor – Motorola 68000 in the prototype – and integrated front ends and decoder chip sets are said to be under development by component manufcturers. Data transmission rate is actually 1200 bit/s to give bit-for-bit redundancy.

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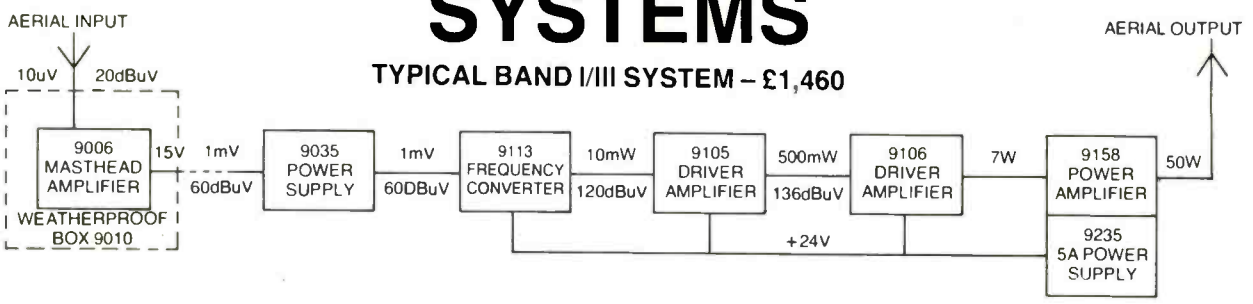

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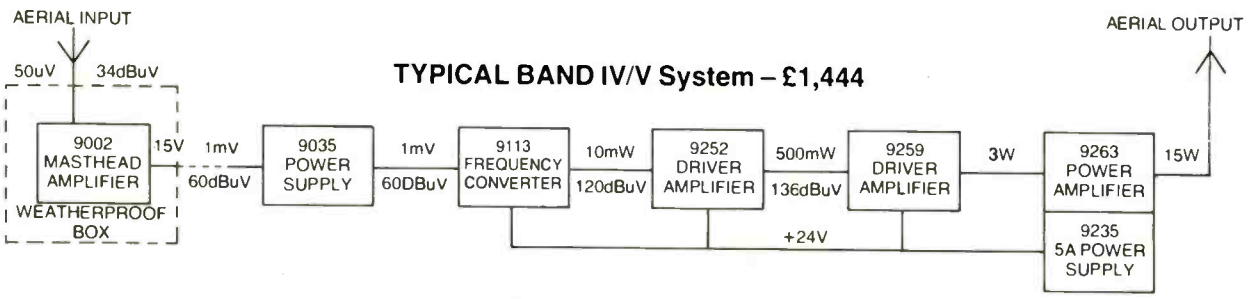

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Mains equipment control

Following the recent article on signalling over over the mains wiring system, the author goes on to show how, with the use of the LM1893 and a microprocessor, remote control is achieved.

NIGEL GARDNER

Signalling over the mains wiring system was discussed in the October 1986 edition of *Electronics and Wireless World*. An enhancement of this concept is to the control of equipment and appliances via the same medium.

The system described uses the LM1893 Biline circuit for modulating and demodulating the mains-borne carrier, and together with a COP402 microprocessor handling all the handshake and signalling protocols, proves to be a very efficient package. The system comprises a master controller with a 1200 baud RS232 interface to the controlling computer and a number of slave modules distributed along the mains under its control (see Fig.1). Each slave unit has four input lines, four output lines and four i/o lines, the latter being programmed from the controlling word sent from the master unit.

MASTER

The master unit (Fig.2) is based around two main circuits. The first of these is the LM1893 carrier current transceiver and the second is a COP402 4-bit microprocessor. The software for the COP μP is implemented in a 27C16 eprom and incorporates all the necessary timing and handshaking functions needed to ensure a very low transmission-error rate. An interface to the host system is provided in the form of a conventional RS232 interface running at 1200 baud. This baud rate can be changed by anyone having a good understanding of COP μP software. A low-cost solution for generating the negative supply needed for the RS232 interface is provided by IC_6, an LM555 timer. The latch, IC_3, is needed to access data from the eprom, as multiplexed address and data lines are used by the COP.

SLAVE

The slave unit (Fig.3) is also based on the LM1893, COP402 and 27C16. Additional components are a +5V regulator and a DS3658 display driver. This latter device is capable of driving loads up to 600mA per output, but other driver circuits could be employed. As previously mentioned, there are 12 interface lines on the slave unit. The four inputs and four outputs are fixed in their function, but the four remaining lines can be defined as either input or output from commands sent from the master unit. Each slave unit has an address capacity from 00 to FF hex, giving a maximum number of 256 possibilities. Modification to the software can result in larger slave address capabilities and even grouping of subsections.

CONSTRUCTION

The construction of these circuits is straight forward enough. However, care must be taken around the mains section of the board and also when handling the c-mos circuits. Tuning up the circuits will require an oscilloscope to ensure correct adjustment and should take place before the COP is inserted into the board and with the two mains isolation capacitors removed.

The easiest way is as follows.

1) With the supply connected, attach a frequency meter or oscilloscope to pin 10 of IC_1 and adjust RO_2 to give a frequency of 125kHz.

2) Connect the oscilloscope to the output side of the isolation transformer – remembering to have removed the two isolation capacitors – and tune the output to a maximum.

3) Connect a jumper between the data-in pin, pin 17 of IC_1, and the 0V supply and observe the level change on the oscilloscope.

4) Adjust the tuning of T_1 to give equal amplitudes when the jumper is connected to 0V or floating.

5) Disconnect the power, remove the oscilloscope and jumpers, insert the COPs and replace the isolation capacitors.

Repeat the procedure for each subsequent slave unit to be aligned ensuring exactly the same trequency is set each time.

SYSTEM OPERATION

The master unit can be connected to either a v.d.u. style terminal or a microcomputer to enable correct operation. On power-up of the master unit, a ">" prompt will be displayed on the screen. This indicates that the master has correctly powered up. The next set of information you have to enter into your terminal is a control sequence of

TABLE 1

COMMAND	BIT/PORT ADDRESS	DESCRIPTION
W	0	1 BIT GO OUTPUT
W	1	1 BIT G1 OUTPUT
W	2	1 BIT G2 OUTPUT
W	3	1 BIT G3 OUTPUT
R	0	1 BIT GO INPUT
R	1	1 BIT G1 INPUT
R	2	1 BIT G2 INPUT
R	3	1 BIT G3 INPUT
W	7	4 BIT D OUTPUT
R	5	4 BIT IN INPUT
W	4	4 BIT G OUTPUT
R	4	4 BIT G INPUT
W	6	8 BIT D, G OUTPUT
R	6	8 BIT IN, G INPUT

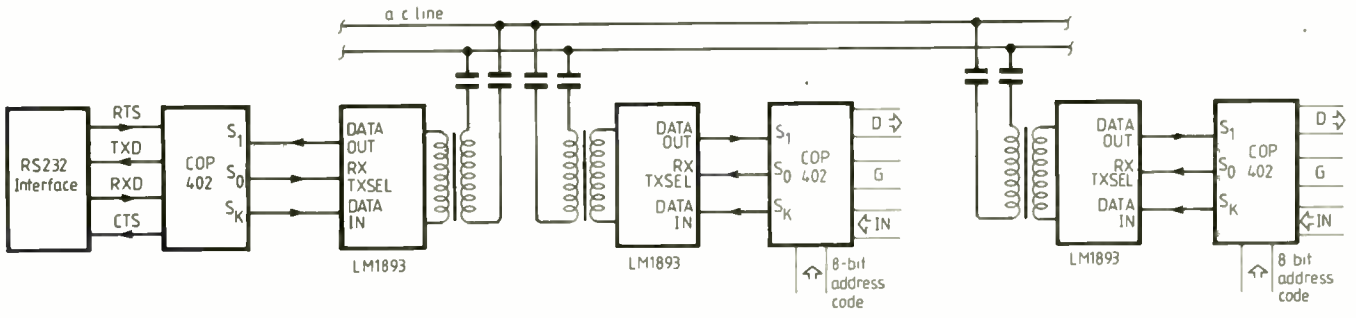


Fig.1 Block diagram of control system, showing master and two slaves.

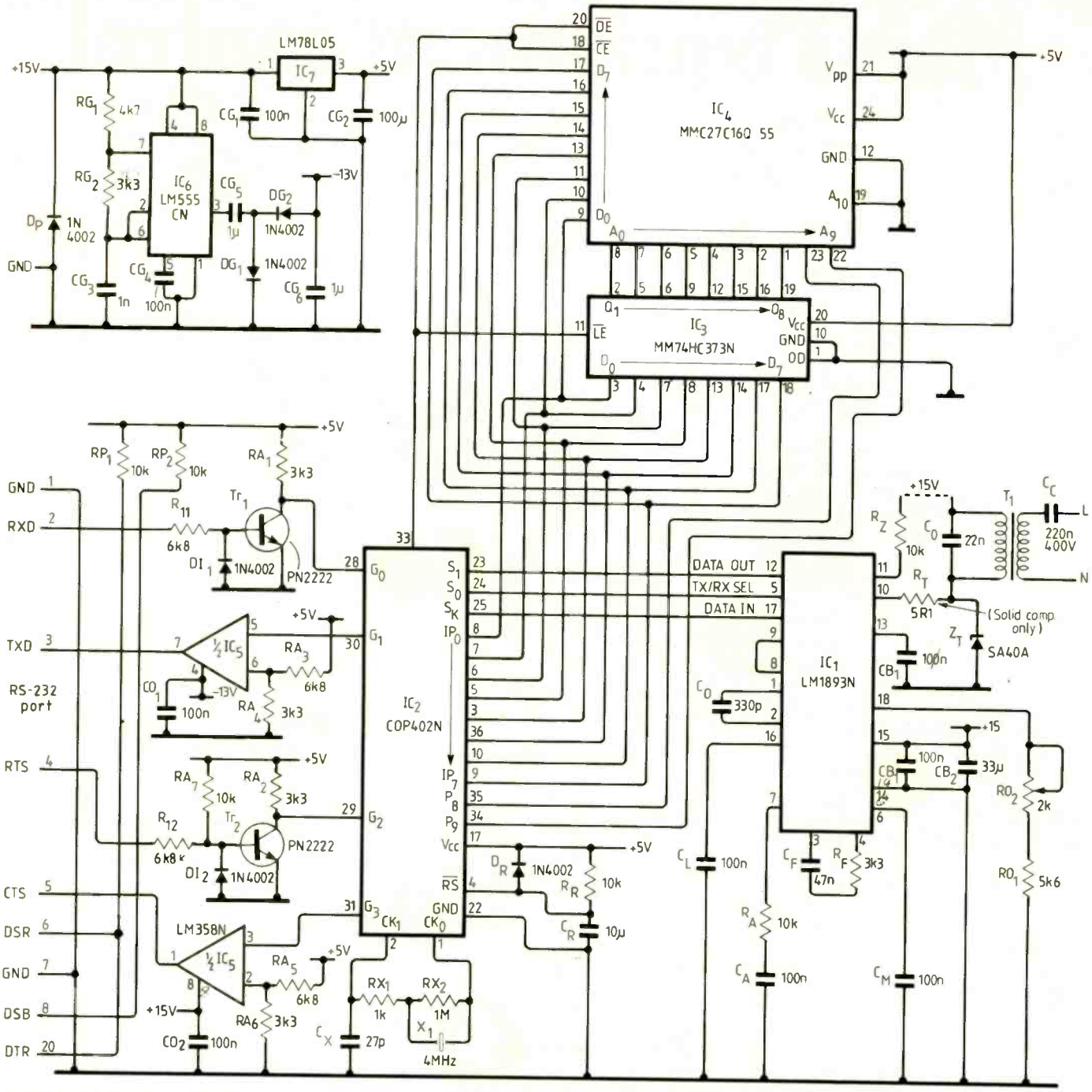


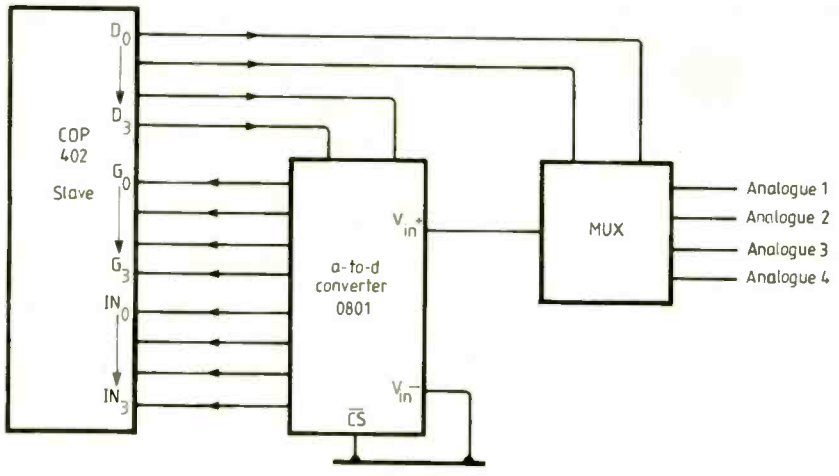
Fig.2. Circuit diagram of master unit.

characters, which is broken down as follows:

> command bit
(R/W)
[1 ASCII]
bit/port address
(0-7)
[1 ASCII]
data
(00-FFH)
[2 ASCII]
slave address
(00-FFH)
[2 ASCII]
CR
[1ASCII]

If, after transmission, a "?" prompt is returned, this indicates that the slave unit has been disconnected, the incorrect slave address was selected or an incorrect sequence was entered into the master unit. If the ">" prompt is returned, this indicates

Fig.5. Slave configured to select one of a number of analogue inputs.



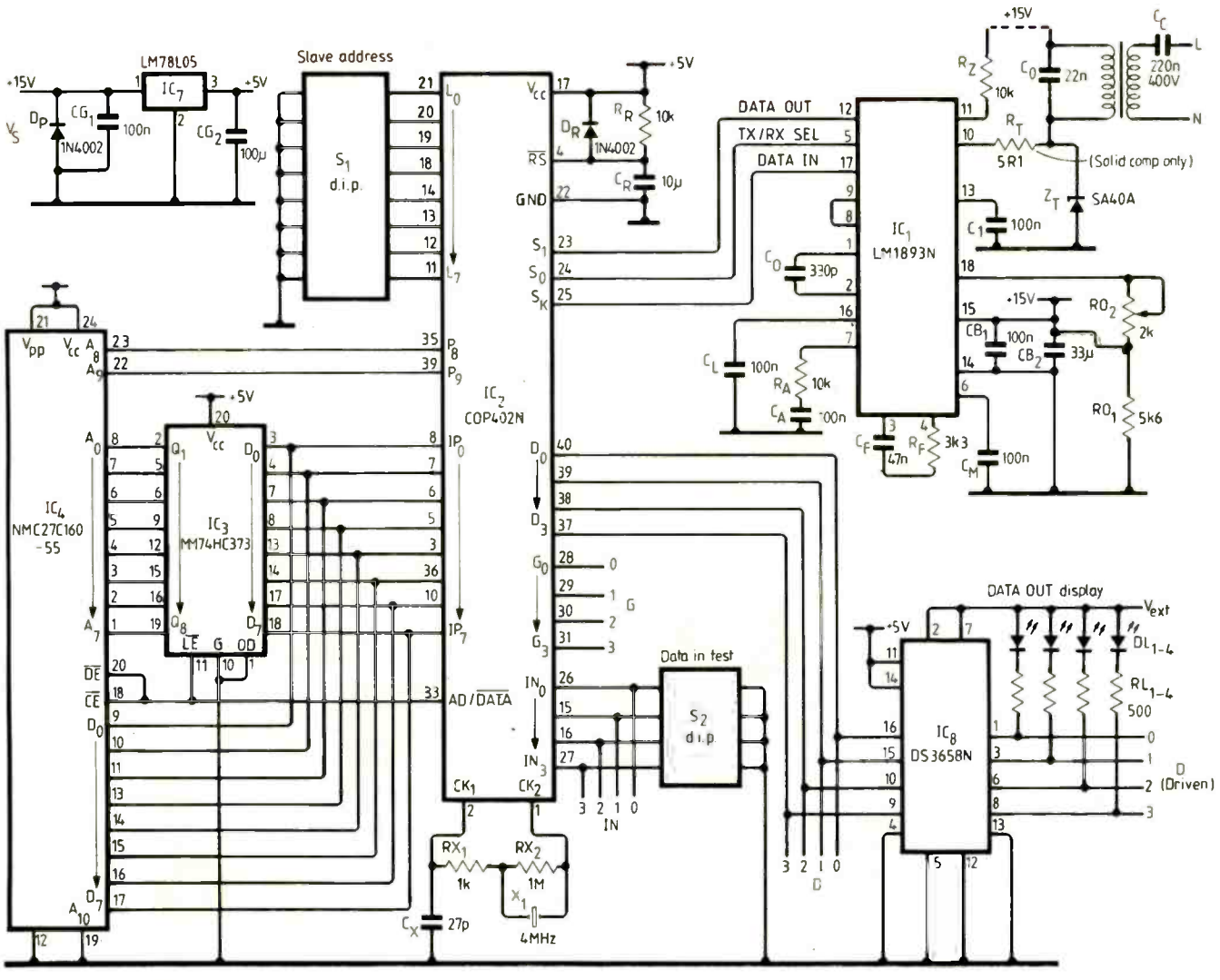


Fig.3. Circuit of a slave unit.

correct reception at the slave unit addressed. Examples of typical messages are shown below:

W 1 01 56 (CR)

This changes the logic level of the G1 line on slave number 56 to logic 1.

R 5 00 E7 (CR)

This will read the logic inputs of the 4-bit input port of the slave addressed E7 and display them on the screen.

A further explanation of these commands and bit/port addresses is shown in Table 1. The command format sent along the mains is shown in Fig.4 and is made up of 3 high-to-low transitions, 3 low and 8 re-sync. bits. This preamble sets up the LM1893 in a mode ready to receive the important data which follows. The remaining data string is made up from 8 address, 8 data, a command byte and finally a checksum to ensure all data is in order. This format for the data-transmission string has proved to be the most efficient and least error free.

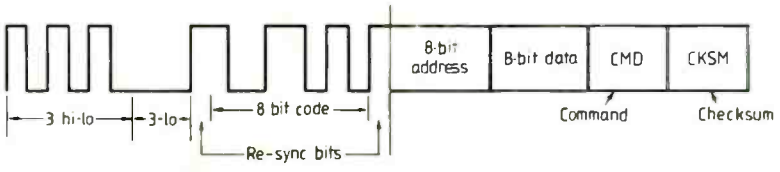


Fig.4. Transmission protocol from master to slaves.

APPLICATIONS

Some suggested examples of the uses for this type of system are in the domestic and industrial lighting control environment where the output lines from the slave COP could drive opto-isolated triacs as outstations of an energy management installation; remote data-acquisition and control within a building for closed-loop heating systems and security monitoring in large premises.

An example of one of these applications is shown in Fig.5. The slave can be used to monitor analogue inputs selected by the multiplexer, fed to the ADC0801 a-to-d converter and then presented to the COP 402 input ports. The four output lines are used to control the multiplexer and hence select the input channel. If a slave module is required to have an analogue output, then Fig.6 might be the simplest solution. The slave provides eight lines to drive the d-to-a converter: the remaining four input lines can be given the function of digital feedback, if the analogue output is used in a servo application, or inputs from the limit switches.

Another application actually using the original version of these modules is for temperature monitoring and control on some sections of pipeline in the frozen north of America, and has been in operation for a number of months.

An important point should be noted about the use of large numbers of units within close proximity. As with all tuned circuits, there is some minor variation in tuning and when all these mistuned networks are lumped in one place the narrow bandwith originally desired becomes almost a broad-band matching. There are ways around this by ensuring the slave units are distributed by about 10 feet of cable – no real problem for 90% of applications, but it could result in excessive loading of the signal if a number of slaves are located adjacent to each other.

An additional device has been introduced to help designers in the other 10% of application areas, where slave units will be in close proximity. An example of this type of

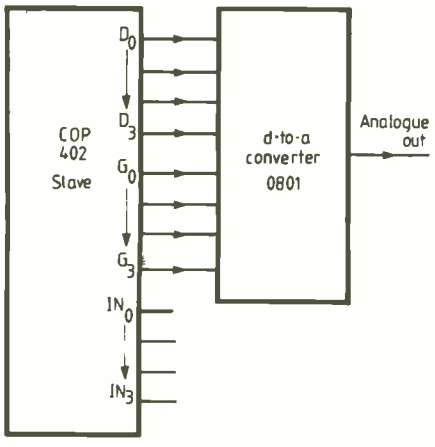
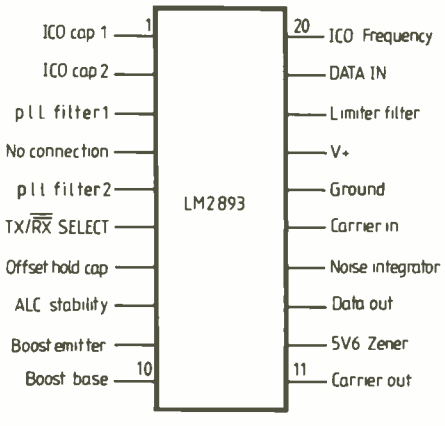


Fig.6. Analogue output from a slave.



Pinout for LM2893

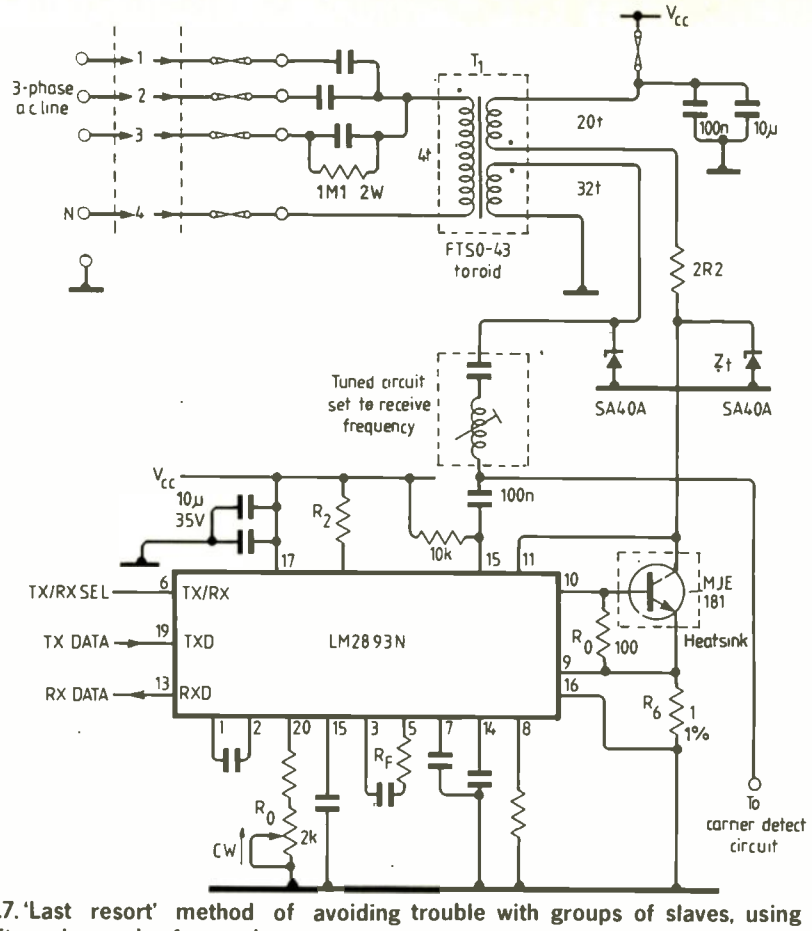


Fig.7. 'Last resort' method of avoiding trouble with groups of slaves, using the split-carrier mode of operation.

A software listing p.c.b. layouts are available, in return for a stamped and addressed envelope, from this office. Please mark your communication 'Mains control'.

One pair of master/slave eproms can be obtained from Nigel Gardner at 4 Magdalen Road, Warnborough, Swindon, Wilts. SN4 0BG. at an inclusive cost of £10.

application area is in electricity meters where a block of flats have all the units in the basement. In these areas, the LM2893 has the advantage over the LM1893 of having separate transmit and receive carrier paths. This enables the designer to use broadband matching for the transmit path and light loading in the receive direction, while still retaining narrow-band matching for greater noise immunity.

An application circuit for this type of interface is shown in Fig.7 and enables the same software to be used with the LM1893. The use of the LM2893 as a replacement to the LM1893 is only recommended as a last resort for those few percent of problem cases. The use of the LM2893 will add slightly to the cost due to the additional components required.

The use of these modules makes life a lot simpler for those who need an off-the-shelf solution for remote data-aquisition systems, where cabling is the major bugbear. This combination of low-cost microcontroller and a high-data-integrity modem working over the mains can form the basis of many remote measuring and control systems. The use within an office environment, for example, can reduce the cabling requirement to interface a selection of printers to a host of PCs. The software is well documented and allows the designer the maximum flexibility.

A software listing and p.c.b. layouts are available, in return for a stamped and addressed envelope, from this office. Please mark your communication 'Mains control'.

FILTERING

As the method of mains communications increases in popularity, there will come a limit to the number of independent systems which can be connected to the same mains feed. The obvious method of r.f. isolation is filtering, but conventional TT or T filtering circuits have to be physically large to cope with the mains currents.

This problem has now been solved by a non-invasive filter developed by Emlux Ltd, which can be clamped around the mains cable at the building entry point, without having to interrupt the supply. This filtering technique can also be used to enable neutral to earth communication to operate in addition to the normal Live-Neutral transmission path. This new filter is tunable to the operating frequency and will be a major advance in simplifying the designer's task, when considering interference from adjacent co-channel systems.

BOOKS

Radio History Comes Alive The technical origins and development of broadcasting in this century make quite a romantic story, but not many of us know more than a few snippets of it. Of course it's all there in the learned society journals, but one would like to have it presented for more relaxing reading.

A new book has just been published which does this and more. "Radio! Radio!" by Jonathan Hill really covers the ground, from pre-Marconi times in the last century to the very end of the valve era in the 1960's. There are separate chapters for each decade from the 1920's onward detailing developments in broadcasting facilities, and in receiver design both technical and aesthetic. The author uses the "radio" of his title in the broader sense of broadcasting transmission and does not neglect television development, starting from Baird's first experiments in 1923.

What makes the book really special is the pictures. There are literally hundreds of photographs: some are of very early pre-broadcast equipment, but the great majority show domestic receivers from the "wirelesses" of the 1920's and on through each decade up to the 1960's.

The book reflects the author's wide range of interests with well-researched technical history and good appreciation of the aesthetic aspects of receiver design. His talents as a photographer are evident; and he even writes well!

Such a well-produced work cannot be cheap, but you won't regret it if you give yourself an £18 treat. The publishers are Sunrise Press, 2-4 Brook Street, Bampton, Devon EX16 9LY. —DPL

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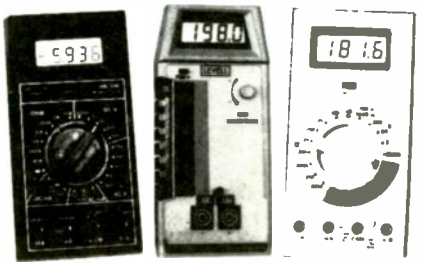

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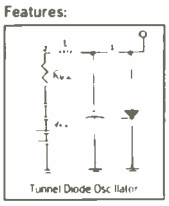

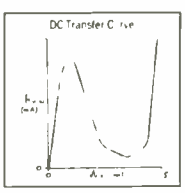

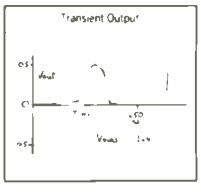

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Automatic testing of surface-mounted components

KLAUS BESELIN and HANS SCHAUFLINGER

This description of the automatic testing of printed-circuit boards with s.m.ds begins with the state-of-the-art in testing methods for p.c. boards with leaded components. Quality assurance starts with the selection of component manufacturers and the incoming inspection of their components, including the board connectors and the circuit boards (Fig.1).

When the individual components have been placed on the p.c. boards, the manufacture of the boards generally ends with soldering: the quality of the p.c. boards is decided during these few stages. The stages which follow — visual inspection, electrical test (possibly in several stages), system test in a set-up with other p.c. boards and firmware and finally power-up at the customer's premises — serve for checking the finished quality and, if necessary, the desired quality improvement. A simple go/no-go result is not sufficient here. As a precondition to economical repair, test methods using specific fault-finding to locate the fault or defective component must be applied (Fig.2).

SIEMENS QUALITY CONCEPT

In assessing the quality of p.c. boards, a quality concept (referred to as a *Q concept*) has been established at Siemens. This allows the effects on quality of the components, component placement method and soldering method to be summarized in one quality coefficient calculated by the formula

$$Q = 100 \cdot (1-\mathrm{dpm_c} \cdot 10^{-6})^{nC} \cdot (1-\mathrm{dpm_p} \cdot 10^{-6})^{nP} \cdot (1-\mathrm{dpm_s} \cdot 10^{-6})^{ns}$$

where dpm = defects per million; n = number per p.c. board; subscript c = components; subscript p = component/placement; subscript s = soldered joints. Q = 100 is achieved with completely fault-free manufacture.

If one considers, for example, a p.c. board with 170 components and a total of 800 soldered joints, the quality coefficient Q = 95 is obtained with the following dpm values:

component defect rate	100 dpm
placement error rate	100 dpm
soldering defect rate	20 dpm

This means that of 100 manufactured p.c. boards, 95 boards have no defects. With the remaining five boards the defects must be specifically eliminated. At the same time, quality-control loops must be used to ensure that series defects and quality impairments of the same type no longer occur, so that the quality of the component delivered and the manufacturing quality can be improved over the long term.

To achieve a component placement error rate of 100 dpm, an average of 10,000 components must be placed without errors. This means that with manual placement at 8s per component, the first error must not occur until 22 hours have elapsed! With automated component placement at a cycle time of 1s per component, an error must not occur on average until after 2 hours and 45 minutes of error-free operation.

These selected numerical examples show the enormous demands made on the manufacture of p.c. boards. An alternative to previous methods is given by the transition from analogue to digital techniques, which require fewer components, as well as the

Fig.1. Printed-circuit board testing as part of the manufacturing and quality system.

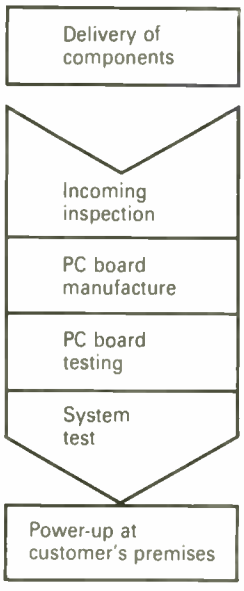


Fig.2. The seven stages in the quality assurance cf p.c. boards.

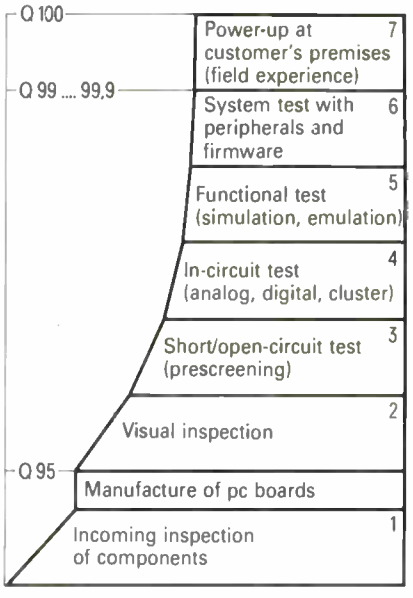
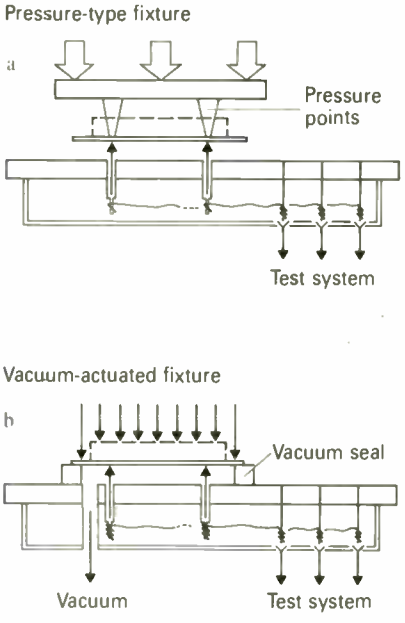

Q 100
Q 99 99,9
Q 95
Power-up at customer's premises (field experience) 7
System test with peripherals and firmware 6
Functional test (simulation, emulation) 5
In-circuit test (analog, digital, cluster) 4
Short/open-circuit test (prescreening) 3
Visual inspection 2
Manufacture of pc boards
Incoming inspection of components 1

Fig.3. Test fixture principles:



ever-increasing integration of the individual components and, finally, the surface-mount technology, which simplifies component placement.

TESTING TECHNIQUE

In the case of p.c. boards with leaded components all the components are inserted into the board from one side – the component side – and connected to the tracks of the board on the other side – the soldering side – by wave soldering.

For the test, which primarily takes place via all connectors, contact is made with additional conductor paths from the soldering side; one contact is usually sufficient for each conductor path which is not connected to a point on the connector. In addition to component terminals, plated-through holes or special test pads which are best arranged in a test-point grid can be used for the purpose. This allows a complete short/open-circuit test of all conductor paths and an in-circuit test of all analogue and digital components. It is followed by a functional test of the p.c. board via the connector.

The first significant step towards surface-mount technology is the replacement of leaded components by surface-mounted devices (s.m.ds). These devices do not have leads which pass through the board and which are available for contacting during testing. Direct contact with the s.m.ds using test probes is not acceptable today for the following reasons:

- s.m.d. cases and packages can be damaged by the spring-loaded test probes, thus impairing the reliability.
- the contact areas on chips are sometimes so small and the positioning tolerances are so great that sufficient contact reliability is not ensured.
- with various designs, the component terminals in the vertical direction are concealed, and special contact elements such as clips for plastic leaded-chip carriers (p.l.c.cs) are not sufficiently reliable for use in bed-of-nails/fixtures.
- unsoldered connections with small-outline (s.o.) or flatpack devices can be closed by the contact force and may appear to be in order in the test.

To test the p.c. boards and for any specific fault locating that may be required, steps should still be taken to ensure that each conductor run is accessible to the automatic tester via the connector or from the soldering side. Test pads on the soldering side and plated-through holes can be provided for probing purposes during layout preparation for the p.c. board, either for a change of level or specifically for test purposes.

FIXTURES

A significant element in the testing is a reliable connection between the test system and the unit under test. Apart from the s.m.t., very high demands are made on the quality and wear resistance of the interface between the test system and the fixture, because the product range and production strategy may require a frequent change of test fixture. However, the resulting problems and the suitability of test-system interfaces to automatic fixture changing will not be discussed in detail here because they are not specific to the s.m.t. We are chiefly concerned with the individual fixture and the demands made on the unit under test, the p.c. board with s.m.ds.

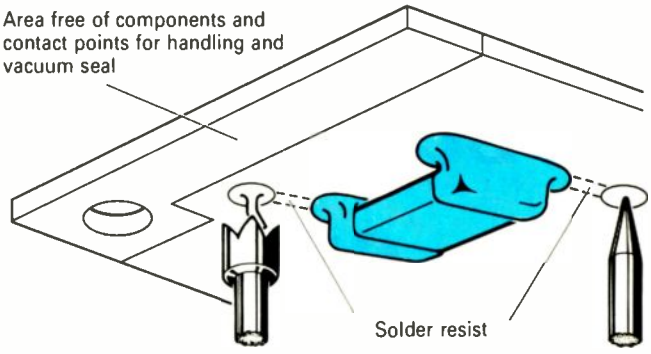


Fig.4. Contact points at an s.m.d. are needed for reliable contact.

TEST FIXTURE

Vacuum fixtures have established themselves in recent years for application to in-circuit testers. The simple principle has a number of advantages compared to manually or pneumatically actuated test fixtures. The vacuum principle is also suitable for p.c. boards with s.m.ds, unless the boards are exclusively reflow soldered and can no longer be held by vacuum because of a large number of open, plated-through holes. In any case, mechanical, pressure-type fixtures may play a greater part in future. These may facilitate the implementation of multistage probing and/or probing on both sides, as well as automatic p.c. board changing (Fig.3).

Probe plate, bed of nails. As already mentioned, it is generally not sufficient to achieve contact only via the connectors for fault locating; a test access point is needed on each conductor path. In the case of boards with leaded components, this test access point is generally the component lead itself, or is provided in the form of a plated-through hole or special test pad on the soldering side of the board.

Easily replaceable spring loaded test probes with a crown or coarse serrated head, or 30-degree tip have proved suitable as contact elements. Each probe should press down on the contact point with a force of at least 1.5N

Conditions and consequences for making contact with p.c. boards with s.m.ds via so-called bed-of-nails test fixtures as a function of size, shape and positions of the contact points.

Test pads ≥ 1.3mm dia. or component terminals (e.g. connector, i.c.) at a pitch of 2·5mm

- Probing on one side:

state-of-the-art for p.c. board and test fixture manufacture.

- Probing on both sides:

increased overheads in fixture manufacture (design, geometry, forces, wiring);
no contact problems should be expected with precise assembly and careful maintenance;
component side no longer accessible for fault-finding.

Test pads ≧ 0.7mm dia. 2·5mm pitch

- Contact on one side:

the use of rugged, standard spring-loaded test probes is possible;
need to improve the precision of the spring-loaded probes;
greater accuracy in position and angularity of the holes in the probe plate;
greater accuracy of locating pins in the fixture;
greater accuracy of the p.c. board relating to the positions of the test pads with respect to the tooling holes, as well as the hole diameters and positions of the tooling holes with respect to each other.

- Contact on both sides:

additional need for maximum precision in mating the lower and upper parts of fixtures.

Test pads ≧ 0.7mm dia. 1mm pitch

Contact can only be made with the p.c. board with miniature test probes;
need for very clean test pad because of the low contact force;
protective measures for the sensitive test probes;
great care in handling and servicing the fixtures;
demands on precision same as for 0.7mm dia. and 2.5mm spacing.

Guide values for p.c. boards and fixtures with very small test pads

Positions of the test pads with respect to holes: ± 0.1mm, on both sides if applicable.
Positioning of p.c. board via the locating pins: ± 0.07mm.
Position of the test probes tips including inclination: ± 0.1mm.

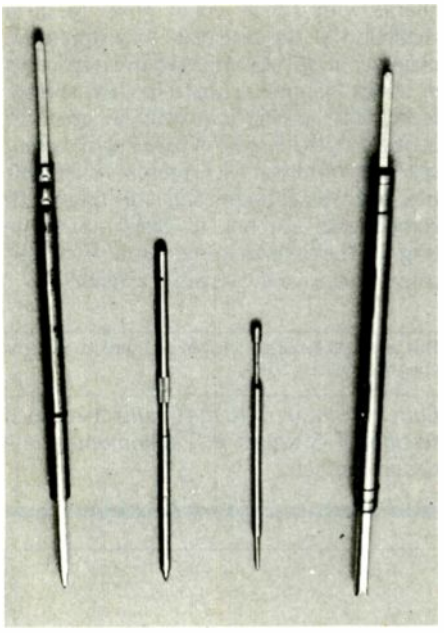


Fig.5. Spring-loaded test probes for 2.5mm and 1mm spacings, 1.5 times enlarged.

to create a reliable contact. This contact force is also necessary for boards with s.m.ds.

Design for probe-ability. In the interest of simple, low-cost and reliable fixtures and of accessibility of the board component side for fault-finding, the following requirements apply to the testing of p.c. boards with s.m.ds:

- separate contact points should be provided for s.m.ds;
- contact points must be located on the soldering side;
- contact points should be arranged in 0·1 in. spacing.

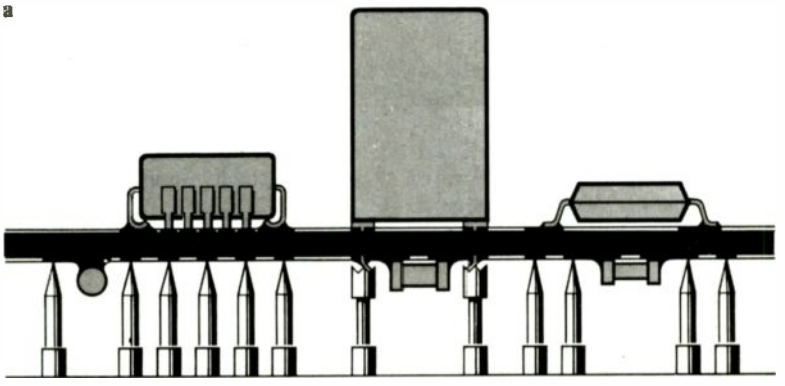
Contact points can be soldering eyelets of plated-through holes ≧ 1.3 mm in diameter or, with mixed assembly, component leads (primarily connector terminals). The contact points must be free from solder resist or insulting coating; at a sufficient distance from the s.m.d terminal pads and edges; and at a sufficient distance from outer edges, tooling holes and other openings to allow contacting, handling and sealing in vacuum-activated fixtures where applicable (Fig.4).

Admittedly, some of these are severe requirements. For many products, they may create considerable friction between the development, production and quality assurance departments, because it appears that space saving – one of the significant advantages of s.m.ds. – is being defeated. This means that even with the application of s.m.ds, the manufacturing and process techniques must not be considered in isolation. The p.c. board as a product must obviously encompass the costs of fault locating and elimination from the overall economical aspects, and must therefore be of a design which lends itself to convenient testing. Fault elimination on p.c. boards with s.m.ds is still relatively difficult and the methods applied require optimization.

Although the decreasing size of components and higher levels of integration allow a further reduction in size of boards, the smaller lead or pad spacings (0.050 in, 0.040 in, etc.) create even greater problems for test

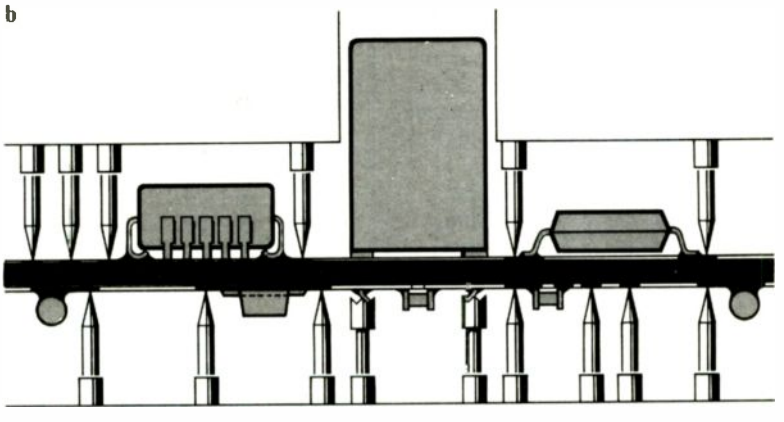
Fig.6. Principles of contacting for a.t.e.

a



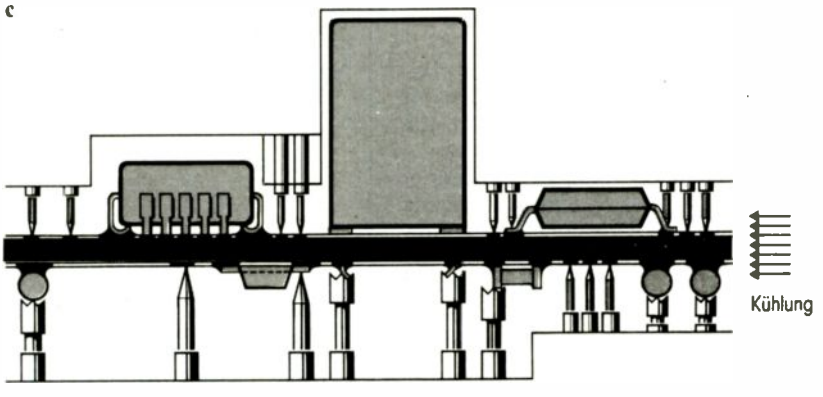
Probing on one side at a spacing of 0.100" on contact points which are as large as possible must continue to be the objective

b



Probing on both sides at a spacing of approximately 0.100" should be limited to exceptional cases

c



Direct probing for SMDs
Probing at 0.050" spacing and possibly on both sides and in two stages, is not acceptable at present

fixtures. The spring-loaded test probes commercially available today are not yet suitable for extensive application. Their lack of stability, low contact force, short spring excursion, difficult and expensive assembly and wiring are only acceptable in exceptional cases. Fig. 5 shows a size comparison between the spring-loaded test probes for 0.100 in and 0.050 in spacings.

The second step towards surface-mount technology leads to an increasing number of components per board, because of the smaller size of the components. This means a correspondingly high number of conductor paths and, because of the grouping of functions on a board, a considerably higher degree of complexity. As long as the test-access points per conductor run are ensured and it is possible at the design stage to provide the required contacts, the test philosophy applied so far should be retained using test systems with higher numbers of pins. If restrictions are made, particularly at the design stage or in the p.c. board layout and design, a transition to the cluster test must take place for some areas, i.e. two or more components are tested as a single unit. This cluster test, as well as the functional test of the p.c. boards, requires a high programming overhead on account of the increased circuit complexity. Locating faults becomes increasingly difficult, as can be seen by taking the following example. Referring to the first example, assume that the components are doubled to 340 and the soldered joints to 1600. In the case of p.c. boards with s.m.ds, a quality coefficient of approximately Q = 97 can be achieved with the following dpm values:

component defect rate	50 dpm
placement defect rate	20 dpm
soldering defect rate	5 dpm

Having achieved this objective, the overhead involved in the testing technique must be reconsidered. There is clearly a need to promote the built-in test technique for p.c. boards and components of growing complexity.

In the transition period, in the case of boards fitted exclusively with s.m.ds and with a large number of conductor paths, a changeover to probing on both sides can be made. Fewer problems are expected here than with mixed construction of boards with leaded components and s.m.ds, for which fixtures can only be created at very high costs because of the widely differing component heights.

Increased integration and miniaturization through the use of s.m.ds must, of course, also be taken into account in the manufacture of the boards and in component placement. Of the large numbers of problem areas, however, only those points which are relevant to the use of test fixtures need be considered here: small track clearances and soldering pads; small plated-through holes; and high tolerance requirements. The intention is to meet these requirements, for example during component placement, by using special position-detection systems and appropriate control of the head of the automatic component-placement system.

In the case of bed-of-nails fixtures, with which several hundred spring-loaded test probes are to be applied simultaneously to the contact points, such position correction is not possible. With a board which is positioned in the fixture via two locating holes, it is necessary for all test probes to be applied reliably to the contact points, whilst no short-circuits with adjacent tracks are created. If there is therefore a desire or need to make contact with small contact points with a close pitch and/or on both sides, the relevant conditions must be provided for during p.c. board manufacture and in the construction of test fixtures.

With the present state of the art, contacts with a spacing of 0.05 in and possibly on both sides or in two stages should be rejected.

An acceptable solution for testing p.c. boards with s.m.ds could prove to be a reduction of the test pads to a minimum diameter of 0.7mm while retaining spacings of about 2·5mm, so that standard spring-loaded test probes can still be used. In contrast to the testing of bare boards where spacings of 1mm and less present no difficulties, the contact points of boards fitted with components are not so easily accessible (Fig.6). The fixtures would result in complicated, expensive and sensitive assemblies.

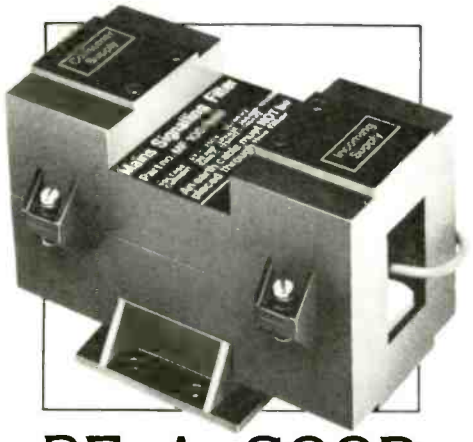
This article is based on a paper published in *Siemens Components* No. 6 (1986).

Both authors are with the Quality Assurance section of Siemens AG, Communications Group, Munich.

```
                                    ; assume addr of data array in BX
                                    ; and binary bit reversed
1$:     AX, II          MOV         ; get current loop counter
        DX, NU          MOV         ; and compare against the terminal
        AX, DX          CMP         ; value of the number of passes
7$                      JA          ; exit if all done
        AX, # 1         MOV         ;
        JJ , AX         MOV         ; take opportunity to zero counter
        CX, II          MOV         ; use the fact that II has the right
2$:     AX, 1           SHL         ; number of shifts in it to
2$                      LOOP        .; calculate 2**II
        IA, AX          MOV         ; and store result temporarily
        AX, 1           SHR         ; calculate half of IA and also
        IB , AX         MOV         ; store result temporarily
3$:     AX, JJ          MOV         ; compare current loop contents
        DX, IB          MOV         ; against terminal value of J loop
        AX, DX          CMP         ; if IB iterations have been done,
6$                      JA          ; go on to next outer loop
        KK , AX         MOV         ; if not, put current J into K
4$:     AX, KK          MOV         ;
        DX, N           MOV         ; and compare it to the loops'
        AX, DX          CMP         ; terminal value of N
5$                      JA          ;
        SI, KK          MOV         ; calculate the two pointers
        DI, IB          MOV         ; into the data array
        DI, SI          ADD         ;
        SI              DEC         ; adjust to point to zeroth element
        SI, 1           SHL         ; and for word addressing
        DI              DEC         ;
        DI, 1           SHL         ;
        AX, [BX+SI]     MOV         ; get the corresponding array words
        DX, [BX+DI]     MOV         ; from the matrix
        AX, DX          ADD         ; first operation is addition...
        [BX+SI], AX     MOV         ; restoring result in memory
        AX, DX          SUB         ;
        AX, DX          SUB         ; whilst the second is subtraction..
        [BX+DI], AX     MOV         ; replacing each pair as we go
        DX, IA          MOV         ; increment the inner loop K counter
        AX, KK          MOV         ; by the step value in IA
        AX, DX          ADD         ;
        KK , AX         MOV         ; and replace the loop counter
4$                      JMP
5$:     JJ              INC
3$                      JMP
6$:     II              INC
1$                      JMP
7$:                                 ; Gray code reordering goes next
```

Fast Hadamard transform

This assembly language version of the fast Hadamard transform was omitted from Mark Varney's article "Hadamard versus Fourier transformation" January issue, page 16. The listing uses FIG-Forth assembler, with Forth variables to hold current values of the loop counters. The binary-bit reverse routine is the same as for the fast Fourier transform, and Gray code re-ordering is not included for brevisty. The 16bit data is assumed to be in two's complement.



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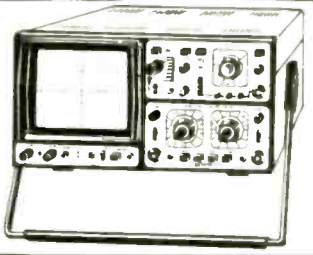

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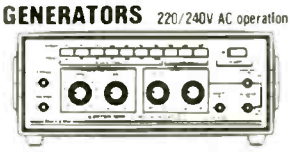

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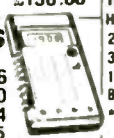

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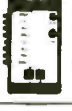

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Broadband voltmeters can be distinguished into the following groups:

- meters with rectifier input
- meters with amplifier input
- meters employing sampling techniques

As for all voltage measuring meters, a high input impedance is essential.

METERS WITH RECTIFIER INPUT

In the simplest case, a detector probe is used in conjunction with a high-impedance electronic d.c. voltmeter. A high-frequency detector located in the probe head is employed to obtain a d.c. output proportional to the measured r.f. Detectors use high-frequency diodes in half-wave (Fig.1(a), (b)) or voltage/doubler (Fig.1(c)) rectifier circuits. For voltages over 1V, diode rectifiers function as peak amplitude detectors in which d.c. output is equal to the peak value minus the forward bias voltage of the diode. For voltages higher than 3V, diode bias can be neglected and the d.c. output of the detector, scaled down by 1.41 or 2.82, serves as a measure of the r.m.s. value of a pure sinusoid. Readings can be taken on the linear scale of the d.c. meter. For voltages lower than 3V, diode bias becomes significant and special non-linear scales are needed. Calibration is accurate for sinusoids.

The voltage range of this combination is from 0.1-0.3V to 10-20V. The upper limit is imposed by the reverse voltage rating of the diode and can it be extended with a divider. The lower limit is imposed by measurement accuracy. Below 100mV, accuracy degradation renders the meter useless. Accuracy is affected by compression of the non-linear scale at low voltages; different volt-ampere characteristics among diodes, requiring a one to one meter-probe calibration; and temperature dependence of the diode volt/ampere characteristic.

Millivolt ranges are characterized by the change in detector behaviour. Peak response gradually gives way to 'square-law' detection, which results in a non-linear detector response. To overcome this problem, along with the temperature dependence, some instruments use thermostatically controlled probes and linearizing electronic circuits.

Another approach, used in the present meter, is through a comparison detector. In this, two similar detector circuits are used: one rectifies the unknown r.f., while the other, a known, instrument-generated, comparison a.f. The amplitude of the comparison a.f. voltage is automatically controlled to give the same d.c. output as the r.f. one. It is evident that if the responses of the two detectors are close enough, so will be the magnitudes of the two signals. It is assumed here that the two detectors have a flat frequency response from a.f. to r.f. and are under the same thermal conditions: error can be introduced by diode mismatch and detector frequency response.

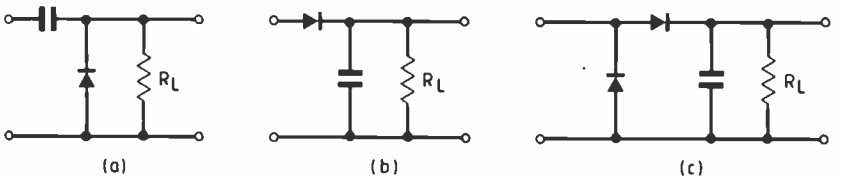


Fig.1. Forms of diode detector in half-wave (a,b) or full-wave (c) circuits.

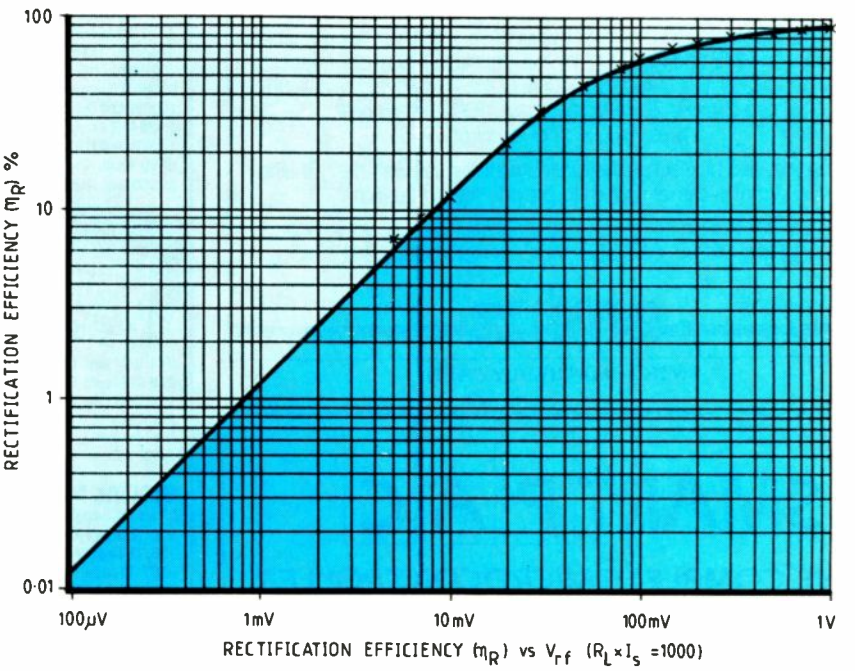


Fig.2. Computer plot of rectification efficiency of a half-wave circuit.

R.F. DETECTION

In a detection circuit (Fig.1) we can define for symmetrical waveforms the efficiency of rectification $n_R(v)$ as

$$\eta_R(v) = k\frac{V_{dc}}{V_{rfp}} \qquad (1)$$

where V_{dc} is the d.c. output of the detector,
V_{rfp} is the peak value of the detected r.f.,
k is a circuit factor, 1.0 for half-wave circuits and 0.5 for voltage-doubler circuits.

For sine waves $\eta_R(v)$ becomes

$$\eta_R(v) = k\frac{V_{dc}}{\sqrt{2}\,V_{rf}} \qquad (2)$$

where V_{rf} is the r.m.s. value of the detected r.f.

The output of a half-wave circuit (Fig.1(a), (b)) employing a diode that has a purely exponential characterisic defined by

$$I = I_S\left[\exp\left(\frac{V}{V_\tau}\right) - 1\right] \qquad (3)$$

and provided that the internal resistance of the signal source is negligible, can be calculated[2] for the whole millivolt range from

$$I_0\left(\frac{\sqrt{2}\,V_{rf}}{V_r}\right) = \exp\left(\frac{V_{dc}}{V_\tau}\right)\left[1 + \frac{V_{dc}}{R_L I_S}\right] \qquad (4)$$

where $I_0(x)$ is a modified Bessel function of the first kind and zero order,
V_T is the volt equivalent of temperature: for room temperature V_T=25.5mV,
I_s is the reverse saturation current of the diode,
R_L is the load resistance.

A computer plot of eq.(4) in the form $\eta_R(V_{rf})$ vs. V_{rf} is depicted in Fig.2. The Bessel integral.

$$I_0(x)=\frac{1}{2\pi}\int_0^{2\pi}\exp(x\cos\omega t)d\omega t \qquad (5)$$

was evaluated first using Simpson's rule for numeric integration. Then the two parts of eq.(4) were made equal by successive approximations. As can be seen, experimental results from a germanium diode detector are in excellent agreement with those theoretically predicted.

For small values of V_{rf}, the Bessel integral can be approximated by a series expansion

$$I_0(x)=1+\frac{x^2}{2^2}+\frac{x^4}{2^2 4^2}+\frac{x^6}{2^2 4^2 6^2}+\ldots \qquad (6)$$

Also exp(x) can be expanded

$$\exp(x)=1+x+\frac{x^2}{2!}+\frac{x^3}{3!}+\ldots \qquad (7)$$

Substituting the first two terms of the series expansions (6) and (7) and dropping V^2_{dc}, equation (4) becomes

$$V_{dc}=\frac{V^2_{rf}}{2V_\tau}\left[\frac{R_L I_S}{R_L I_S+V_\tau}\right] \qquad (8)$$

and for $R_L I_s \gg V_T$

$$V_{dc}=\frac{V^2_{rf}}{2V_\tau} \qquad (9)$$

V_{dc} calculated with eq. (8) has the following error over the exact solution of eq.(4):

+6.9% for V_{rf}=20mV
dropping to +2.0% for V_{rf}=10mV

Eq.(8), illustrated by the straight-line portion of the curve in Fig.2, indicates that for small V_{rf}, V_{dc} is proportional to the square of V_{rf}. For signals of V_{rf} = 30mV or less, the detector practically behaves as 'a square law' device. However, this square-law response offers both advantages and disadvantages.

The main advantage is that the detector has almost r.m.s. response regardless of the waveform of the measured signal. This is true only to a certain extent, depending on the voltage and the crest factor (ratio of peak to r.m.s.) of the waveform. Permissible crest factors for 2% and 5% error are given below:

V_{rf}	Crest factor
3mV	10/13
10mV	3/4
30mV	1.7/2

(source Rohde & Schwarz)

This property can be very useful in noise measurements. By insertion of a suitable attenuator or capacitive divider, higher voltages can be reduced to this level, thus extending upwards the almost true r.m.s. range.

On the other hand, the d.c. output of the detector falls/off rapidly with decreasing V_{rf}, becoming, below a certain value, unusable for further processing. For this reason, even the best professional meters have as their most sensitive subrange 3mV or 1mV f.s. To demonstrate this, a voltage-doubler detector will yield only 1.5 microvolt d.c. for 0.3mV r.f. In this low d.c. environment, the problems encountered are: noise (inherent and pick-up); thermocouple effects; short and long term thermal stability; electrostatically induced voltages; amplifier offsets; and printed-circuit leakage currents. The practice is to use a chopper-stabilized d.c. amplifier in the input stage. As an alternative a high-precision op-amp with comparable performance can be used.

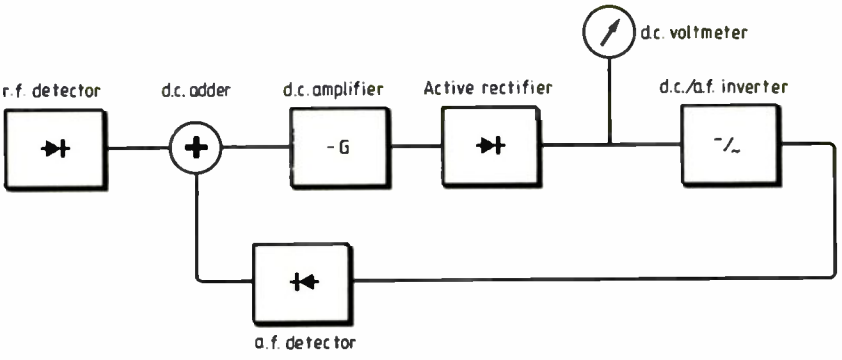


Fig.3. Block diagram of the millivoltmeter.

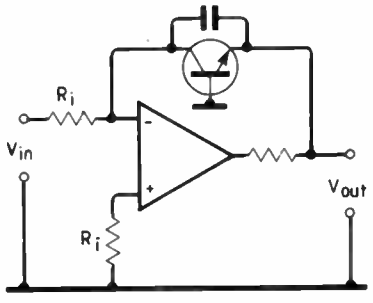


Fig.4. The use of log. amplifier obviates the need for elaborate switching. This is a basic circuit.

PRINCIPLE OF OPERATION

The principle of operation was used in a series of professional instruments manufactured by Rohde & Schwarz. During an attempt to re-design one such instrument[1], new circuits evolved that simplified circuitry astonishingly.

The r.f. to be measured and the comparison a.f. are rectified in the two similarly designed detector circuits (Fig.3, detectors 1,2) located in the probe head. The d.c. voltage difference of the two detector outputs is designated the error voltage

$$V_{err}=\frac{\sqrt{2}}{k}\left[\eta_R(V_{rf})V_{rf}-\eta_R(V_{af})V_{af}\right] \qquad (10)$$

The error voltage is then amplified by $-G$ and is ideally rectified, the rectified voltage being used for meter indication.

$$V_{ind}=G\,V_{err} \text{ for } V_{err}>0 \qquad (11)$$
$$V_{ind}=0 \quad \text{for } V_{err}\leq 0 \qquad (12)$$

The feedback loop closes with a d.c./a.f. inverter that generates a pure 5 kHz sine wave of V_{af} r.m.s. value, directly proportional to the absolute value of V_{ind}. Ratio of proportionally S is

$$S=\frac{V_{af}}{|V_{ind}|} \qquad (13)$$

Combining (10), (11), (13)

$$V_{ind}=\frac{G\eta_R(V_{rf})}{\frac{k}{\sqrt{2}}+GS\eta_R(S/V_{ind})}V_{rf} \qquad (14)$$

Examining eq.(14) one can see that it is a typical closed-loop transfer function of a control system. If $\eta_R(v)$ were constant, then the value of the fraction would have been constant and the relation between V_{ind} and V_{rf} linear throughout the meter range. The fraction contains $\eta_R(V_{rf})$ and $\eta_R(S|V_{ind}|)$ that are in general different by a small amount. In order to bring eq.(14) into common terms regarding $\eta_R(v)$ the following assumption is made:

$$\frac{\Delta\eta_R}{\Delta V}\approx\frac{d\eta_R(V_{rf})}{dV_{rf}} \qquad (15)$$

Eq.(15) is unconditionally true for V_{rf} <20mV (because $d\eta_R(V_{rf})/dV_{rf}$ is constant). For V_{rf}>20mV it is true for small ΔV. ΔV diminishes as G increases. Eq.(15) yields

$$\eta_R(S|V_{ind}|)=\eta_R(V_{rf})+a-a\frac{V_{rf}}{S|V_{ind}|} \qquad (16)$$

where

$$a=\frac{d\eta_R(V_{rf})}{dV_{rf}}S|V_{ind}| \qquad (17)$$

Rearranging, eq.(14) transforms to

$$V_{ind}=\frac{G[\eta_R(V_{rf})+a]}{\frac{k}{\sqrt{2}}+GS\left[\eta_R(V_{rf})+a\right]}V_{rf} \qquad (18)$$

The derivative $d\eta_R(V_{rf})/dV_{rf}$ in eq.(16) can be calculated analytically from

$$\frac{d\eta_R(V_{rf})}{dV_{rf}}=\frac{1}{\sqrt{2}\,V_{rf}}\left[\frac{dV_{dc}}{dV_{rf}}-\frac{V_{dc}}{V_{rf}}\right] \qquad (19)$$

$$\frac{dV_{dc}}{dV_{rf}}=\frac{\sqrt{2}\,I_1\left(\frac{\sqrt{2}\,V_{rf}}{V_\tau}\right)\left[R_L I_S+V_{dc}\right]}{I_0\left(\frac{\sqrt{2}\,V_{rf}}{V_\tau}\right)\left[R_L I_S+V_{dc}+V_\tau\right]} \qquad (20)$$

($I_1(x)$): is modified Bessel function of the first kind of first order.

A high degree of linearity can be achieved if $GS[\eta_R(V_{rf})+a] \gg k/\sqrt{2}$ throughout the

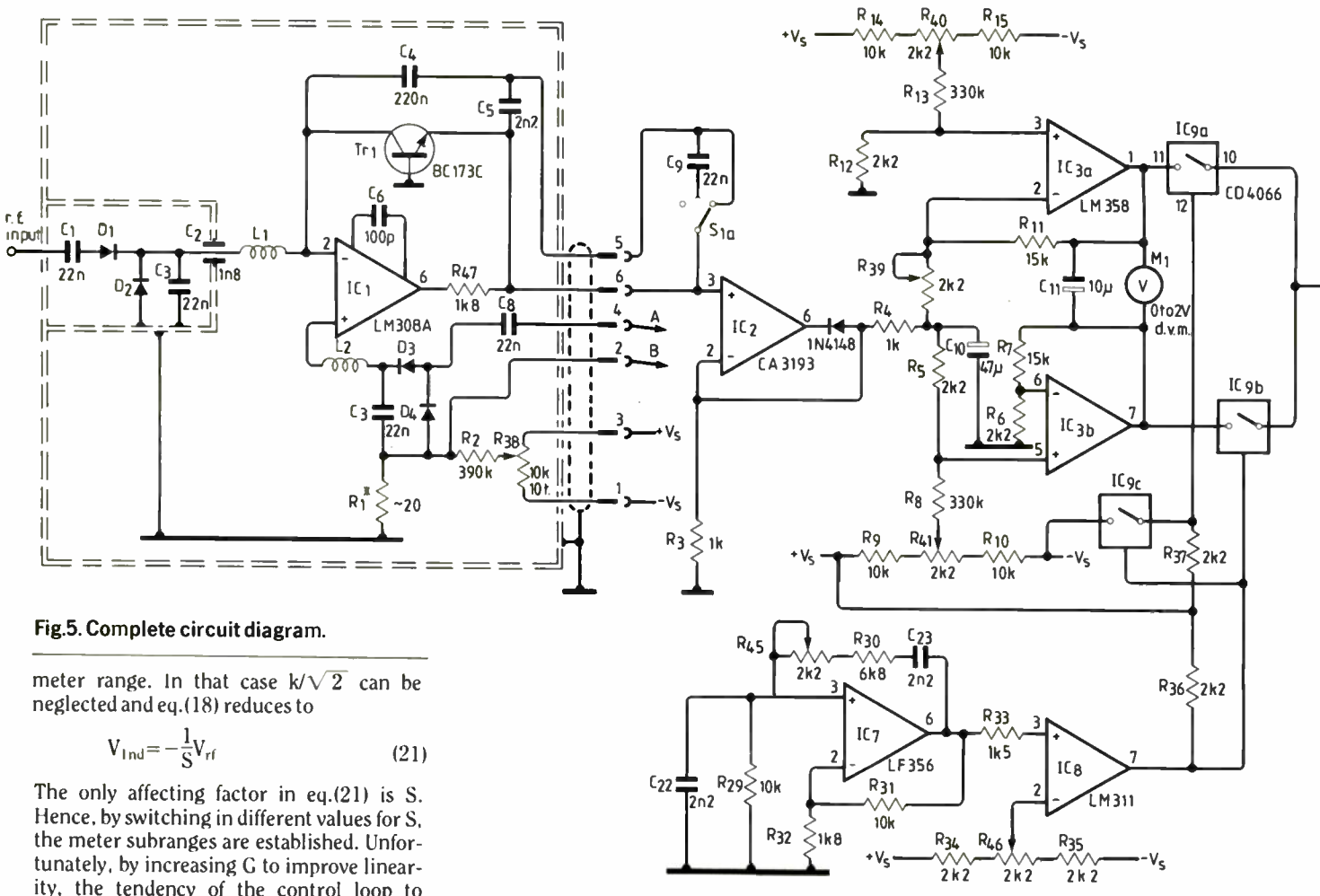


Fig.5. Complete circuit diagram.

meter range. In that case $k/\sqrt{2}$ can be neglected and eq.(18) reduces to

$$V_{Ind} = -\frac{1}{S}V_{rf} \qquad (21)$$

The only affecting factor in eq.(21) is S. Hence, by switching in different values for S, the meter subranges are established. Unfortunately, by increasing G to improve linearity, the tendency of the control loop to oscillate is increased. Stability can be maintained by giving G a frequency roll-off characteristic. For this reason, integrator capacitors are included in the d.c. path, but this prolongs response time.

A compromise between linearity, stability and response time can be reached. For this, in each subrange a suitable value for G is selected, such as to maintain $GS[\eta_R(V_e d2f)+a]$ within limits. The limits are determined by linearity imposing the lower limit ($GS[\eta_R(V_{rf})+a] \gg k/\sqrt{2}$), and control-loop stability imposing the upper limit. Hence, maximum G should be used for the minimum V_{rf} to be measured (where $[\eta_R(V_{rf})+a]$ and S are minimum) and vice-versas).

THE LOG. AMPLIFIER

The innovation of this design lies in the fact that the above goals are attained without the need for an elaborate switchable amplifier. Here a log. amplifier followed by a small-gain linear 'post amplifier' greatly simplify matters.

By this combination, as it will be shown, the dependence of V_{ind} on $[\eta_R(V_{rf})+a]$ is loosened and any desired degree of linearity between V_{ind} and V_{rf} can be achieved. A design requirement for 1:10 ratio subranges can thus be easily realised, in contrast with the switchable amplifier alternative.

Error voltage, eq.(10) modified by (15), (16), (17) becomes

$$V_{err} = \frac{\sqrt{2}}{k}\left[\eta_R(V_{rf})+a\right]\left[V_{rf} - S|V_{ind}|\right] \qquad (22)$$

Error voltage is applied in the input of the log. amplifier, (Fig.4) and its output, after being rectified (gain=1) and amplified by g is used as V_{ind}

$$V_{ind} = -gV_\tau \ln\left(\frac{V_{err}}{R_i I_t}+1\right) \qquad (23)$$

where R_i is the series input resistor,
I_t is the reverse saturation current of the transistor p-n junction,
g is the gain of the post amplifier.

Using the approximation $\exp(x) \sim (1+x)$, for small x and after some algebraic manipulation, V_{ind} takes the form

$$V_{ind} = -\frac{V_{rf}}{S} + \frac{\exp\left(\frac{V_{rf}}{SgV_\tau}\right)-1}{\exp\left(\frac{V_{rf}}{SgV_\tau}\right)\frac{1}{gV_\tau} + \frac{\sqrt{2}\,S[\eta_R(V_{rf})+a]}{kR_iI_t}} \qquad (24)$$

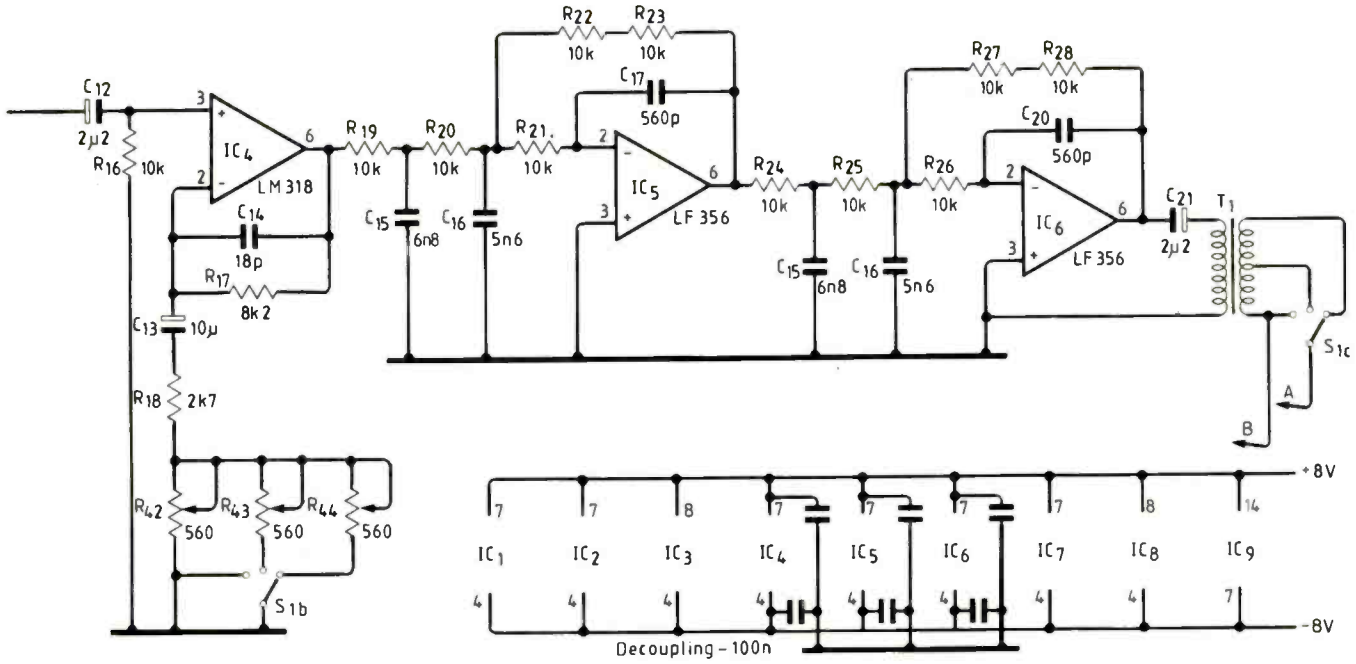
Eq.(24) is an improvement over eq.(18), since the expression containing $[\eta_R(V_{rf})+a]$ is no longer a multiplier of V_{rf} but a quantity to be subtracted, i.e. eq.(24) comprises two parts, a linear and a non-linear one. Also, $[\eta_R(V_{rf})+a]$ in the non-linear part is divided by an extremely small quanity, R_iI_t, the orders of magnitude of I_t and R_i are 10^{-14} A and 10^{+5} 214 respectively.

The magnitude of the non-linear factor, obviously affecting linearity, can be calculated by entering design values in eq.(24). The meter has three subranges: a) 2V-200mV, b) 200-20mV and c) 20-2mV. For compatibility with common d.v.ms, full-scale indication is 1.999V. Therefore, S values are: $S_a=1$, $S_b=0.1$ and $S_c=0.01$. In each subrange, the non-linear part has its highest value for the full-scale r.f. input. Among the three subranges maximum occurs for the 20mV r.f. input, where $[\eta_R(20mV)+a] = 0.47$ is lowest. It is interesting to note that the non-linear factor can be trimmed to any desired value, by proper g selection. A design value of g=10 was chosen, resulting in a linearity error of the order of 10^{-4}. Thus, method error is far below the one anticipated from other sources. For the accuracy sought, the non-linear factor can be neglected and eq.(24) reduces once again to eq.(21). On the other hand, excessive g is not favourable, as it affects stability by increasing the overall open-loop gain.

Summarizing, the log. amplifier functions as a self-controlled, high-gain amplifier. It exhibits the highest gain in the vicinity of zero input. Maximum gain is only limited by the open-loop gain of the op. amp. (typ. 300 000).This property of the log. amplifier is particularly useful in the microvolt range, where ample gain is essential. However, this is not the only affecting factor. Lowest-value measuring capability (sensitivity) is determined by zero stability and noise.

ZERO STABILITY

Zero offset (meter indication with no input) is unilateral, i.e. it is displayed only on the side of zero where the control loop is effective. In the other side, '00.00' is displayed regardless of offset. This, a consequence



of the control function, can lead to erroneous measurements. It is therefore advisable to introduce deliberately an offset in the displayed side in order to avoid uncertainty.

If a zero offset exists (whether displayed or not), an input V_{rf} will be indicated as

$$V_{ind} = -\frac{1}{S}\sqrt{{v_{rf}}^2 \pm {V_{offs}}^2} \qquad (25)$$

where V_{offs} is the displayed zero or an equivalent amount on the other side. The positive sign refers to the displayed offset while the negative to the non-displayed.

In the case of the displayed offset, eq.(25) indicates that: for V_{rf} much greater than V_{offs}, the error introduced by V_{offs} is negligible; for V_{rf} of the same order as V_{offs} the value of V_{rf} must be calculated from eq.(25); and for V_{rf} much smaller than V_{offs} the contribution of V_{rf} to V_{ind} is too small for accurate interpretation.

It is evidence that V_{offs} must be kept as low as possible. Moreover, the stability of V_{offs} with temperature and time is of great importance. The origin of V_{offs} lies in the existence of parasitic microvolt voltages within the sensitive part of the meter, namely the two detectors and the log. amplifier inputs – the most critical part of the meter. The rest is more or less straightforward.

CIRCUIT DESCRIPTION

The circuit diagram of the meter is depicted in Fig.5. Error voltage is first log. amplified (IC_1 in the probe), then rectified (IC_2) and low-pass filtered (R_4, C_{10}). The following stage ($IC_{3a,3b}$) is a balanced-to-ground, low output-impedance d.c. amplifier. Potentiometers R_{40}, R_{41} null offsets (zero meter indication with zero rectifier input). Balance is adjusted by means of R_{39} although metering is balance-error free. The output of the

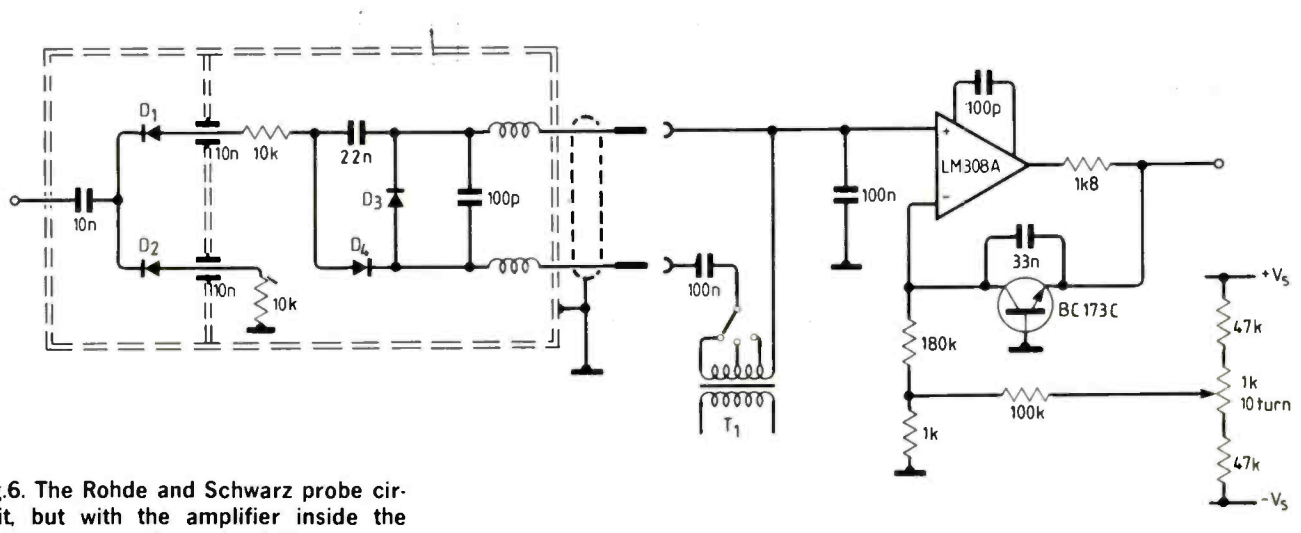


Fig.6. The Rohde and Schwarz probe circuit, but with the amplifier inside the meter, gave poor thermal stability.

succeeding chopper, a 5 kHz squarewave, is then amplified in meter calibration amplifier IC_4. This stage functions also as an impedance buffer between chopper and the first active filter. Separate calibration potentiometer, R_{42}, R_{43}, R_{44}, are provided for each subrange to compensate for minor discrepancies in transformer T_1 ratios. Two cascaded low-pass Butterworth active filters[3] (IC_5, IC_6) that follow, concert the squarewave into a sinusoidal voltage which, suitably divided by switchable ratio transformer T_1 (1:1, 10:1, 100:1), is finally delivered to the comparison detector in the probe. Auxiliary circuits to switch the chopper comprise, 5 kHz frequency oscillator IC_7, shaper-driver IC_8 and inverter-driver IC_{9c}. R_{45} and R_{46}.

THE PROBE CIRCUIT

The incorporation of the log. amplifier (IC_1, Tr_1) in the probe was dictated by the need to suppress thermocouple voltages. As a first approach the Rohde & Schwarz probe circuit was tried with the log. amplifier located in the meter body (Fig.6). The former, implemented with ordinary components, gave the meter poor zero thermal stability.

Zero thermal stability was enhanced by increasing circuit compactness by placing IC_1 close to the two detectors ($D_{1,2,3,4}$), and limiting the number of components to an absolute minimum. The use of carbon or metal-film resistors should be avoided (R_1 inevitably used in the nulling circuit is an r.f. choke functioning as a copper wire-wound resistor.) R_2 is not a part of the sensitive circuitry. The input circuit is guarded and IC_1, selected for low input-voltage offset, low offset drift and low noise.

The need for a screen between the two detectors degrades the probe stability during thermal transients, but elimination of the screen affects the frequency response of the probe. Diodes are socket-mounted (not soldered). Gold plated i.c. pin sockets are recommended. Integration capacitors $C_{4,5,9}$ are subrange selectable for best stability/response time performance. The zeroing adjustment R_{38} is incorporated in the probe to facilitate switching between probes without need for re-zeroing. The value of R_2 indicated in Fig.5 may need adjustment depending on R_1, IC_1 offset and diode matching.

DETECTOR DIODES

As already mentioned, meter accuracy and thermal stability greatly depend on the degree to which the four diodes ($D_{1,2,3,4}$) are matched. Ideally, all diodes should have identical volt/ampere characteristics within a wide temperature range.

Among many types of r.f. germanium diodes that were tested, in lots of 10 to 40 per type, no pair of exactly identical diodes was found. Discrepancies were noticed in their characteristics in both the forward and reverse bias state, as well as in their temperature dependence. Fortunately, a reasonable amount of mismatch can be tolerated before its effects become noticeable. Also, instead of four matched units, two matched pairs, each split between the two detectors, suffice.

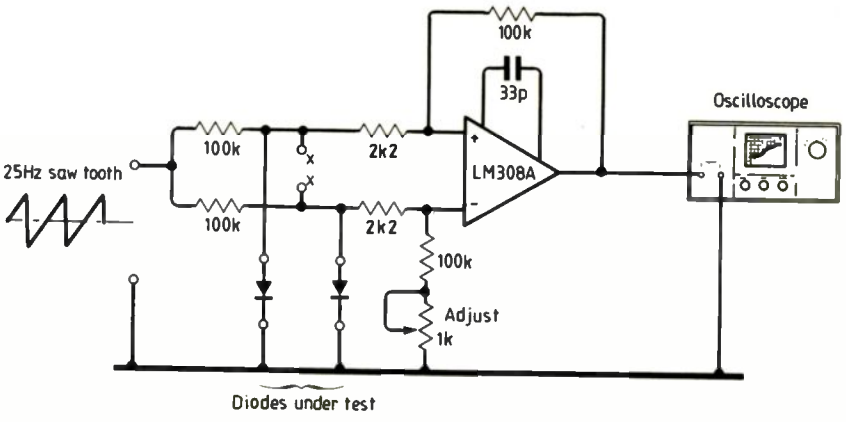
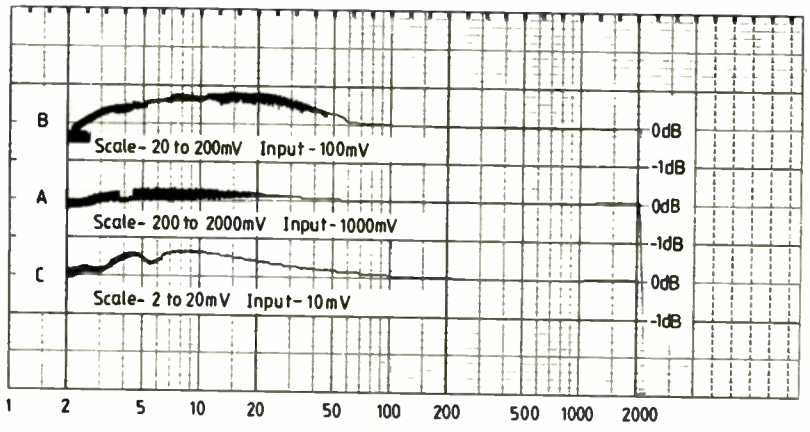


Fig.7. Circuit to assist diode pairing.



Audio frequency response of the instrument.

Inevitable residual mismatch also affects meter zero. Nulling is easily accomplished, along with op-amp offset, via R_{38}. Furthermore, the drift of the meter zero caused by temperature change of this residual mismatch can be benefically used to compensate inverse drift of the op-amp offset. Thus, by proper arrangement of the four diodes in the two detector circuits, thermal stability can be improved.

For diodes available in quantities, a preselection was made by sorting them according to their forward-bias voltage measured with a multimeter. Various groups having closely similar values were formed, and preference was given to the most numerous group having the lowest voltage value. Although the latter is not of much importance, it is nevertheless a welcome property, because it indicates a high I_s (refer to eq.(8)). Pairing was assisted by the tester of Fig.7, in which the degree of match between two diodes is better as the oscilloscope travel approaches a straight line. Best matches were tested (in the same way) in other ambient temperatures.

This procedure, being almost a d.c. test, is a measure of the performance of the diodes in the l.f. and h.f. spectrum. V.h.f. and u.h.f. efficiency evaluation was performed in the actual meter. Here again, discrepancies were noticed among previously matched diodes. Thermal stability and frequency response optimization was the criterion for the final selection and positioning of the pairs.

Diodes of the types AA112, AA113 and AA119 were tested in quantities, with satisfactory results. Any of these types can be used. A smaller number of OA90, OA95 and AAY11 was also tested. A limited number of 'gold-bonded' type (1N270 and 1N995) showed better temperature behaviour. Unfortunately, this cannot take the form of a general rule due to the small number tested and the unavailability of other types of this variety for cross-checking. Manufacturer's pairs were also not available for testing.

OTHER CONSIDERATIONS

Other factors that, in a lesser degree, affect meter accuracy are, firstly, error due to temperature de-calibration. Meter calibration assumes a stable relation between oscillator frequency and filter frequency response. Uneven temperature drift in the corresponding circuits (IC_5, IC_6, IC_7) alters this relation and thus, meter calibration becomes temperature sensitive. By using temperature-stable capacitors, properly matched, the 5 kHz frequency oscillator and the two active filters can be made to drift evenly. In this way, de-calibration error on the prototype was limited to <0.2% from 0 to 50°C.

Secondly, there is a linearity error due to distortion of the 5 kHz sinewave. The meter measures the d.c. value of the a.f. envelope. Detection of low voltages is r.m.s. sensitive. Any change in the relation between the envelope d.c. and the a.f. r.m.s. affects

linearity, mostly in the most sensitive subrange. Distortion, harmonics, line hum and modulation must be avoided. Measurements on the prototype showed <1% t.h.d. and harmonic levels <−47dB for the 2nd and <−50dB for the 3rd respectively. The transformer used is purpose wound on a ferrite core for low distortion. Primary and secondary have 2,000 turns, while the taps are on 200 and 20 turns. Transistor-radio audio transformers of suitable turns ratio can be used as an alternative. Modulation can occur from control-loop instability, but the specified capacitors $C_{4,5,9,10}$ ensure stable operation. The a.f. comparison voltage must be free from a.m. modulation with c.w. r.f. meter input. The r.m.s. value of a modulated signal is

$$V_{rms}=V_c\sqrt{1+\frac{m^2}{2}}$$

where V_c is the r.m.s. value of the carrier,
V_m is the r.m.s. value of the modulating signal
m is the degree of modulation V_m/V_c

PERFORMANCE

The meter was tested in conjunction with Rohde and Schwarz SMS-type r.f. signal generator. Linearity was tested throughout the meter range, at 10 MHz, with the generator terminated into 50 ohms. Composite generator-meter error (expressed as a percentage of the reading, not of the f.s. value) is

subrange	error
2V -200mV	+/−1.5%
200mV- 20mV	+/−0.7%
20mV - 2mV	+/−0.5%

The same results were obtained with the generator unterminated. For the frequency-response test, the generator was left unterminated in order to reduce to the minimum the components between generator and probe. One N-type-to-BNC adaptor was inevitably used. The frequency response of the generator-adaptor-probe combination for one of the prototype probes is given above. Among the above voltage levels a frequency-response disagreement can be noticed. It can be readily explained by the variance of the input impedance with V_{rf}. Indicated approximate impedance values are for <10 MHz. Input capacitance given includes shunt capacitance of the diodes, stray capacitance and that of the BNC connector. More moderate values of the order 3 to 4pF are possible.

Sensitivity at 10 MHz is 0.2mV for one stable digit, 0.5mV for two stable digits and 1.0mV for three stable digits.

subrange	V_{rf}	input impedance	frequency 10mHz	100MHz	250MHz	500MHz	1GHz
2V −200mV	1V	100kΩ/4pF	0dB	−0.4dB	−0.5dB	+0.5dB	+1.7dB
200mV− 20mV	100mV	45kΩ/5pF	0dB	−0.3dB	−0.6dB	−0.7dB	−2.1dB
20mV - 2mV	10mV	20k6pF	0dB	−0.3dB	−0.7dB	−1.0dB	−3.1dB

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2. R. W. Landee, D. C. Davis, A. P. Albrecht, L. J. Giacolleto: 'Electronics designers' handbook', McGraw-Hill, New York, 1977, pp. 24-138, 24-142.
3. National Semiconductor: 'Audio handbook', 1976, pp. 5-3, 5-5.

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Orthogonal functions — an introduction to Walsh functions

The use of Walsh functions reduces transforms to simple arithmetic, mainly addition, well suited to present-day processing methods. Only one set of functions – and therefore coefficients – is involved, instead of the two of Fourier analysis.

CHARLES H. LANGTON

Orthogonal functions are not new nor are they a special set of functions as trigonometrical or exponential functions are. Rather it is the property of orthogonality possessed by certain groups of already existing functions that is important.

The application of this mathematical concept has become more widespread in electronics in recent years because it has been found useful in analysing problems in communications such as modulation and multiplexing, computer switching networks, and pattern recognition systems. Perhaps the best known application of orthogonal functions is in the Fourier analysis of complex waveforms, although many textbooks on Fourier tend to play down, or not even mention orthogonality. Walsh functions are another group owing their prominence to orthogonality.

When testing a particular function for orthogonality it must be compared with another function having the same variable. The two functions are then orthogonal, or not orthogonal, according to the result of the test. For example, although the functions sin(at) and sin(bt) may be orthogonal, sin(at) and sin(bx) are certainly not because by definition any two functions are said to be orthogonal over a certain specified interval. Sin(at) is a function of time, and as such the interval must have the dimension of time. Sin(bx), on the other hand, is a function of some variable x and as such the interval must be of an appropriate dimension.

To determine whether two signals, f(i,t) and f(j,t) say, are orthogonal over a given interval T, it is necessary to integrate the product of the two signals with respect to t, over the range $0 \leq t \leq T$. Then, to be orthogonal, the result of this integration must be non-zero when the two functions are identical (that is when i equals j). Additionally, the result must be zero for all other cases (when the functions are not identical). This is written as

$$\int_0^T f(i,t).f(j,t)\,dt = \begin{cases} o \text{ for } i=j \\ k \text{ for } i=j \end{cases}$$

If the non-zero value k when the functions are equal is unity, the functions are described as orthonormal.

CIRCULAR FUNCTIONS

Since and cosine functions form the most widespread orthogonal system in use. The set of such functions is complete. Consider the set of circular functions in Fig. 1, harmonically related to the fundamental frequency ω radians per second, and observed over the interval $T=1/f=2\pi/\omega$ seconds.

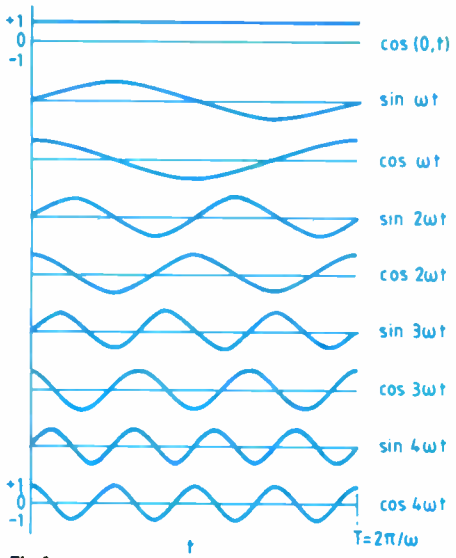


Fig.1.

We know, thanks to Fourier, that by choosing appropriate combinations of the eight waves shown, plus the d.c. level, at the correct amplitudes and then adding them together, an approximation to any single valued, repetitive complex waveform with period T may be synthesized. For a closer approximation extend the set to include higher order harmonics, 5ωt, 6ωt, etc.

The reverse process to synthesis, waveform analysis, is not easy, until we recall that harmonically related sine/cosine waves are orthogonal. Then it becomes a simple matter in principle to multiply each cosine wave (cosωt, cos2ωt, etc.) in turn with the function f(t) of the unknown waveform, and integrate this product for $0 \leqslant t \leqslant T$ where T is the period of f(t). Just like trying different keys in the lock.

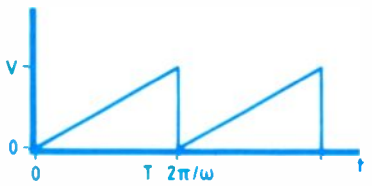
When the right key is chosen, the integral will not only have a non-zero value, but this value can be the coefficient of cos(rωt), telling not only that cos(rωt) is present in f(t), but also how much of it is present.

Unlike a good lock, it is possible that several cosine terms may 'click' when analysing a complex wave. The cosine coefficient are usually denoted by a_1, a_2, etc., and so a complete set of cosine terms will be $a_1\cos\omega t$, $a_2\cos 2\omega t$, $a_3\cos 3\omega t$, . . . $a_n\cos n\omega t$. Of course, if any of the terms do not exist in f(t), the corresponding coefficients will be zero.

Having tested each cosine wave against f(t) and obtained a set of cosine coefficients, the same procedure is repeated, this time testing the set of sine waves against f(t). Those that click will produce a non-zero integral, and this is given the letter b and appropriate suffix. The set of sine terms in f(t) will then be $b_1\sin\omega t$, $b_2\sin 2\omega t$, etc., up to $b_n\sin n\omega t$. Again, some of the coefficients will very likely be zero.

A partial example will exemplify this. Consider a repetitive sawtooth wave that rises linearly from zero to a maximum value

Fig.2.



V in a period T seconds, and having zero flyback time (Fig.2). The equation of this function is f(t) = Vt/T. To determine the amplitude of any term, say cos3ωt, that may possibly be present in this sawtooth, follow the directions indicated above. Thus:

$$a_3 = \frac{2}{T}\int_0^T f(t)\cos(3\omega t)dt$$

$$= \frac{\omega}{\pi}\int_0^T \frac{V}{T}.t\cos(3\omega t)dt$$

$$= \frac{\omega V}{\pi T}\int_0^{2\pi/\omega} t\cos(3\omega t)dt = 0.$$

Thus there is no component cos3ωt present in the given waveform. If the calculation is performed with cosωt, cos2ωt, or any harmonic of this cosine series the result will be zero, i.e. $a_1 = a_2 = a_3 = \ldots = a_n = 0$.

Turn now to an investigation of the sine terms. Starting with sinωt:

$$b_1 = \frac{2}{T}\int_0^T f(t).\sin\omega t\,dt$$

$$= \frac{\omega}{\pi}\int_0^T \frac{V}{T}\,t.\sin\omega t\,dt$$

$$= \frac{\omega V}{\pi T}\int_0^{2\pi/\omega} f(t).\sin\omega t\,dt$$

$$= \frac{\omega V}{\pi T}\left[-\frac{2\pi}{\omega^2}\right] = -\frac{V}{\pi}\text{, as } T=2\pi/\omega.$$

In a similar way the coefficient of the second harmonic b_2 is $-V/2\pi$, that of the third harmonic $b_3 = -V/3\pi$, and the nth coefficient $b_n = -V/n\pi$. Hence all the sine terms are present in the ideal repetitive waveform represented by the equation f(t) = Vt/T.

WALSH FUNCTIONS

Sine/cosine waves are not the only ones that lend themselves in this way as building bricks in the assembly of complex waves. Sets of rectangular pulses may also be employed, and in digital processing this type of pulse is more readily available in a system than are sinusoidal waves.

Walsh functions are two-valued pulses, nominally +1 and −1. These can easily be translated into 0 and 1 levels, suitable for use in any normal two-state system. The mark-space ratio in general is non-uniform, which makes it difficult to define the frequency of such waves. Instead, the term sequency is used, defined as

$$\text{sequency} = \tfrac{1}{2}\left[\text{average number of zero crossings per second}\right]$$

the unit being zs^{-1}.

Walsh waveforms are defined over an interval $0 \leqslant t \leqslant T$ and the wave will be sampled N times during this interval, where N is a binary number; less than 16 sample points would normally be too crude. When sampled it is usual to define the sample points as $\theta_0, \theta_1, \theta_2, \ldots \theta_{N-1}$.

Walsh functions must be used in sets. The number of functions in a set must be a binary number having the same value as N. The position of a particular function in the set is the order number n = 0, 1, 2, . . . N−1. It is usual for sets of Walsh functions to be drawn as a series of rectangular waves although as such waves are sampled it is more correct to indicate the sampled points θ_i only. Fig.3 shows a set of eight Walsh waves.

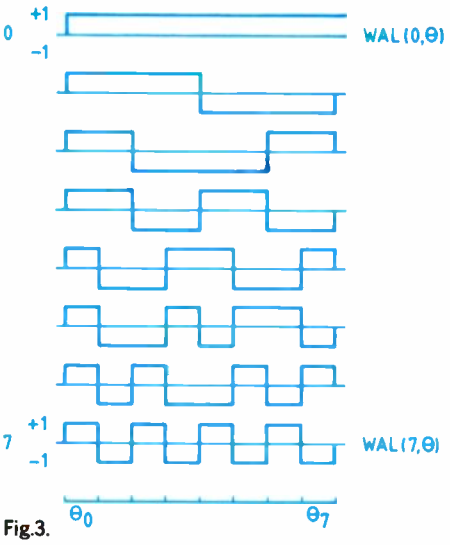
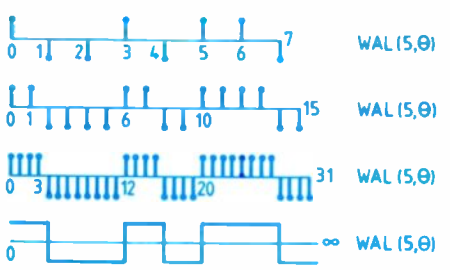


Fig.3.

The diagram above represents a complete set of WAL(n,θ) waveforms for N = 8.

It is interesting to note that for any value of N, the form of the i^{th} (permissible) order is the same. To illustrate this point, WAL(5,θ) is drawn below for N = 8, 16, 32, and infinity.



As N increases, the definition improves. As $N \rightarrow 8$, the waveform approaches a rectangular shape.

The instantaneous value of any Walsh function can only be +1 or −1. To specify an instantaneous value, two arguments are required. First it is necessary to specify the order of the function, and secondly the instant θ at which the function is sampled. So the general way of expressing a Walsh function is WAL(n,θ). In Fig.3, for example, WAL(5,3) = −1.

DERIVATION

But how are these functions derived in the first place? There are two main routes. The first is to use a rather bulky formula which is tedious to perform. An oversimplified version of the formula could be:

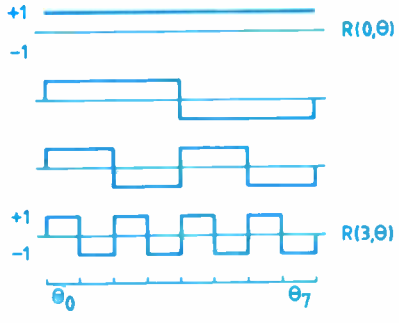
$$\text{WAL}(n,\theta) = \prod_{r=0}^{p-1}(-1)^{k_r}$$

where $2^p = n$. k_r always works out as an integer, therefore $(-1)^{k_r}$ will be (−1) or (+1) according to whether k_r is odd or even. The sign π stands for repetitive multiplication of $(-1)^{k_r}$ for all values of r from zero to p−1.

Hence, the final value of WAL(n,θ) can only be the result of several values of +1 or −1 multiplied together, hence WAL(n,θ) = ± 1 as stated. The table gives all the values of WAL(n,θ) for N = 8, n = 0 to 7, θ = 0 to 7.

Order n	Normalized discrete instant θ								
	0	1	2	3	4	5	6	7	
0	1	1	1	1	1	1	1	1	WAL(0,θ)
1	1	1	1	1	−1	−1	−1	−1	WAL(1,θ)
2	1	1	−1	−1	−1	−1	1	1	WAL(2,θ)
3	1	1	−1	−1	1	1	−1	−1	WAL(3,θ)
4	1	−1	−1	1	1	−1	−1	1	WAL(4,θ)
5	1	−1	−1	1	−1	1	1	−1	WAL(5,θ)
6	1	−1	1	−1	1	−1	1	−1	WAL(6,θ)
7	1	−1	1	−1	1	−1	1	−1	WAL(7,θ)

The second method is simpler and based on a set of harmonically related square waves described by Rademacher in 1922.* Walsh defined his own functions in 1923. Rademacher functions were later found to be a subset of Walsh functions. Here are the first four Rademacher functions:



Although Rademacher functions do not form a complete set, they are easy to remember and are useful because they enable Walsh functions to be determined more easily. The relation between the two is:

$$R(k,\theta) = WAL(2^k - 1,\theta) \qquad (1)$$
$$k = 0,1,2,\ldots$$

Hence

$$R(0,\theta) = WAL(0,\theta)$$
$$R(1,\theta) = WAL(1,\theta)$$
$$R(2,\theta) = WAL(3,\theta)$$
$$R(3,\theta) = WAL(7,\theta)$$
etc.

The remaining Walsh functions that are Rademacher functions may be developed from the known Rademacher functions by the relationship:

$$WAL(i,\theta).WAL(j,\theta) = WAL(i \oplus j,\theta) \qquad (2)$$

where ⊕ is modulo-2 addition (exclusive-OR), and the dot signifies multiplication.

EXAMPLE

An example may be useful. Look for the Rademacher equivalent of WAL(2,θ). First express the order of WAL(2,θ) in binary. Thus

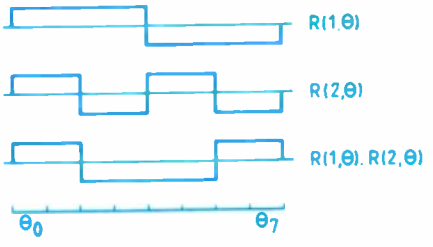
$$WAL(2,) = WAL(010_2,\theta).$$

Then find two binary numbers which, in modulo-2 addition add up to 010_2. Recall that in modulo-2 addition you never carry one.

$$010_2 = 001_2 \oplus 011_2$$

$$\therefore WAL(2,\theta) = WAL(001_2 \oplus 011_2,\theta)$$
$$= WAL(1 \oplus 3,\theta) \quad \text{reverting to decimal}$$
$$= WAL(1,\theta).WAL(3.\theta) \quad \text{from equation 2}$$
$$= R(1,\theta).R(2,\theta) \quad \text{from equation 1}$$

The next diagram shows R(1,θ) and R(2,θ), and also their product. You can see this is identical to WAL(2,θ).



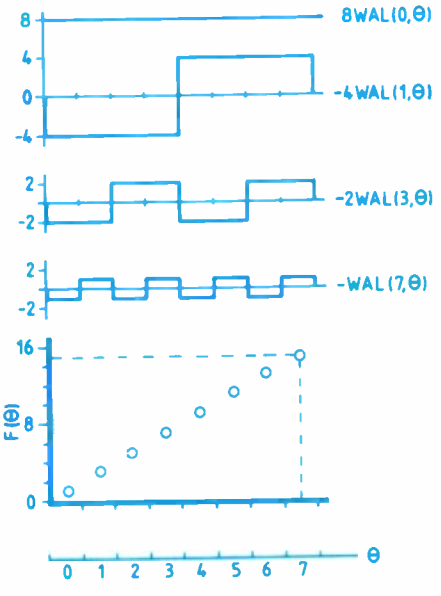
You may like to show that WAL(4,θ) = R(2,θ).R(3,θ).

WAVEFORM SYNTHESIS

Now take a number of Walsh waves and add them together algebraically so as to produce a complex waveform. As an example, let

$$F(\theta) = 8WAL(0,\theta) - 4WAL(1,\theta) - 2WAL(3,\theta) - WAL(7,\theta).$$

The four Walsh terms drawn to the same timebase, N being 8, are:



The sum of the samples taken at each instant is plotted on the lower axes. This shows a set of points F(θ). If the Walsh timebase was repetitive, F(θ) would have the form of a sawtooth, not unlike that shown in Fig.2.

WAVEFORM ANALYSIS

For a complex waveform f(t) it is possible to perform the reverse process of synthesis, that is analysis, and determine the content of the wave in terms of a series of Walsh functions:

$$a_0WAL(0,\theta), a_1WAL(1,\theta), \ldots a_rWAL(n,\theta).$$

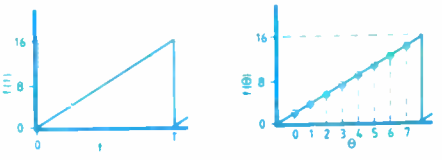
This is because Walsh functions form a complete orthogonal set, and a complex wave can be tested against each function in turn to see if the function is embedded in the wave. Very similar to Fourier analysis in fact.

The test for the magnitude of the nth Walsh coefficient a_n is to apply the following equation:

$$a_n = \frac{1}{N} \sum_{\theta=0}^{N-1} F(\theta).WAL(n,\theta) \qquad (3)$$

F(θ) is the original complex waveform sampled N times, N being as defined previously. Note that as f(θ) is a sampled function, i.e. not continuous, summation may be used instead of integration, as employed in the Fourier example.

Again, a worked example may be useful. Consider once again the sawtooth function f(t) shown and its sampled counterpart:



In this example we determine the value of the coefficient of the third order, $a_3WAL(3,\theta)$, assuming that N = 8.

When	θ = 0	1	2	3	4	5	6	7	
F(θ) =	1	3	5	7	9	11	13	15	from Fig.9
WAL(3.θ) =	1	1	−1	−1	1	1	−1	−1	from Table 1

Then, from equation 3,

$$a_3 = \tfrac{1}{8}[1(+1)+3(+1)+5(-1)+7(-1)+9(+1)+11(+1)+13(-1)+15(-1)]$$
$$= \tfrac{1}{8}[1+3-5-7+9+11-13-15]$$
$$= \tfrac{1}{8}[-16] = -2.$$
$$\therefore \text{third-order term} = -2WAL(3,\theta).$$

The other terms in the series may be obtained in the same way, so that if

$$f(t) = \frac{V}{T}.t = \frac{16}{8}.t = 2t,$$

the Walsh transform of f(t) is

$$F(\theta) = 8WAL(0,\theta) - 4WAL(1,\theta) - 2WAL(3,\theta) - WAL(7,\theta).$$

The rest of the terms are zero. Note that a_0 (eight in this case) represents the mean value of the wave.

This, of course, is the function we first thought of when considering the synthesis of a complex wave, so we have completed the cycle of transformation.

Many calculations using Walsh functions reduce to the product of (±1) and (±1). It is possible to replace the arithmetical operation of multiplication by the logical operation of exclusive-OR thereby speeding up the process. This is done by storing all data levels of +1 as zero, and all data levels of −1 as one as summarized in the table.

Arithmetical multiplication	Logical exclusive-OR
+1 . +1 = +1	0 ⊕ 0 = 0 converts to 1
+1 . −1 = −1	0 ⊕ 0 = 1 converts to −1
−1 . −1 = +1	1 ⊕ 1 = 0 converts to 1

Further reading

Beauchamp, K. G., Walsh functions and their applications Academic Press, New York (1975).

Harmuth, H. F., Transmission of information by orthogonal functions. Springer-Veriag (1972).

Roddam, T., Walsh functions, *Wireless World* Dec. 1981, Jan. 1982.

Charles Langton, M.Sc., M.I.E.R.E., is a chartered engineer operating as a freelance development engineer and technical author based at York.

*The same year in which the BBC was established – irrelevant, but it forges a link between Walsh functions and *Wireless World* readers.

Economical digital audio for broadcasting

Delta modulation codec provides an efficient and cost-effective means to convey a multiplicity of high quality audio and audio-with-video programming via satellite, cable tv and terrestrial broadcasting

STEVEN E. FORSHAY

Digital systems are attractive for signal delivery to consumers in that excellent audio performance is achieved in poor reception conditions, and effective scrambling algorithms are easily implemented without compromise in signal integrity. Digital broadcast systems proposals to date have employed both linear and companded forms of p.c.m.

Linear p.c.m. systems with the necessary dynamic range for high quality audio (e.g. 80 dB) offer excellent performance but are less desirable in consumer broadcast applications due to the high rate (500 to 700 kb/s per channel). The cost and implied manufacturing precision in d.a.cs (0.01% for 80dB dynamic range) and anti-aliasing filters is also of concern.

Good performance and a bit-rate reduction to perhaps 330kb/s can be achieved using fixed emphasis, simplified error protection, and a suitable digital companding algorithm around a p.c.m. codec of at least 10-bit full-scale resolution. The Nicam codec system[1] developed by the British Broadcasting Corporation is an example of such a system. Companded p.c.m. systems offer a reduction in transmission bandwidth requirements, but the cost of precision converters and anti-aliasing filters necessarily remains.

Efficient digital transmission implies a low sampling rate while high quality implies audio bandwidth to at least 15kHz; these conflicting goals place tighter restrictions on anti-aliasing filter design and ultimately limit the extent to which filter cost can be reduced. In addition, viable p.c.m. systems require error concealment or correction which adds significantly to consumer hardware complexity and cost.

There is no doubt that well designed p.c.m. systems can satisfy consumer demand for quality service but the inherent cost of precision d.a.cs, multi-pole anti-aliasing filters, and error-correction circuitry may continue to be a deterrent to large-scale consumer acceptance.

DESIGN OBJECTIVES

In development of an efficient and cost-effective transmission coding scheme several important goals must be achieved in a balanced approach:

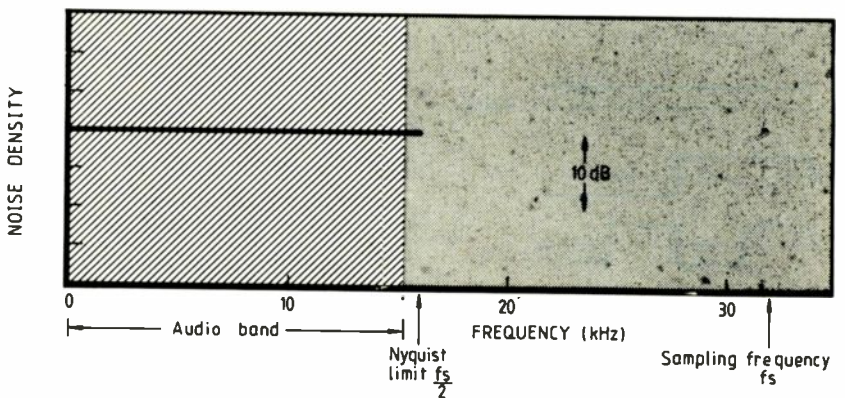


Fig.1. In a pulse-code modulation codec quantization noise is uniformly distributed over the Nyquist bandwidth.

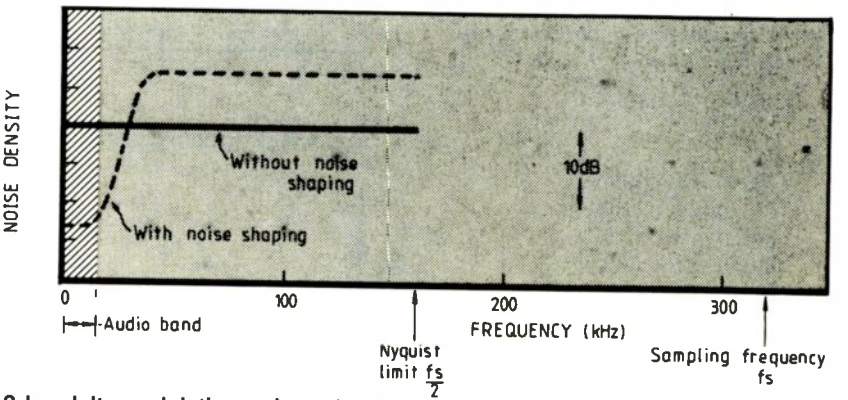


Fig.2. In a delta modulation codec noise shaping can be used to reduce the amount of in-band quantization noise.

- The codec system must be faithful in reproduction of high quality source program and be fully complementary in its signal processing. The system should provide high quality transmission means consistent with present day technology and likely future expectations.
- The system must be economically viable and practical in operation in order to ensure long-term acceptance. Hardware costs, especially consumer hardware, and transmission efficiency must be well-balanced with performance and future flexibility.
- The system should offer robust performance and minimal audible degradation in the presence of worst-case errors.

DELTA MODULATION

Prior research work in development of a digital audio recording system based on delta modulation[2] led us to consider a variation on the technique for consumer broadcast media, where delta modulation has the following significant advantages over p.c.m:

- The delta demodulator (a passive integrator) is inherently simple and can be manufactured with much less precision than a multi-level p.c.m. d-to-a converter.
- The audio signal is highly oversampled, thereby eliminating the need for complex anti-aliasing filters.

● All bits are of equal weight so that isolated bit errors have minimal audible effect (in contrast to m.s.b. errors in p.c.m.). Good error performance can be achieved with a low overhead error concealment technique (discussed later).

ADAPTIVE DETLA MODULATION

Delta modulation can be used to achieve significant advantages in bit-rate and cost. However, linear delta modulation at the rates under consideration (200 to 300 kb/s) has insufficient dynamic range for high quality reproduction. An adaptive delta modulation scheme therefore seemed desirable.

Quantization noise in any properly dithered, sampled and quantized codec system appears approximately uniformly distributed over the Nyquist bandwidth. In any companded codec, noise modulation (program modulated quantization noise) must be considered.

In digitally companded p.c.m. systems quantizing accuracy (number of bits per sample) decreases according to the companding law with increasing signal level. Noise modulation is produced as a result of quantization accuracy varying from fine at low signal levels, to coarse with signals approaching full scale. High-level l.f. signals can produce audible h.f. noise modulation as only minimal noise masking is provided by the signal spectrum. Fixed high frequency pre-emphasis can reduce noise audibility with a commensurate compromise in high frequency headroom. Such a system may work well if low frequency noise is sufficiently low and the compromise in high frequency headroom, is acceptable. In contrast to straight p.c.m., noise modulation in a differential system such as a.d.m. is produced by high slope signals (i.e. signals of high frequency and level). This charateristic is quite acceptable and possibly preferable to that of companded p.c.m. in that high frequency signals are more effective at masking high frequency noise.

Conventional a.d.m. schemes code the audio signal and step-size scaling information into a single bit-stream. Step-size adaptation information is derived by monitoring the output data stream; such systems are referred to as being output-controlled, with the significance of individual output bits varying according to the particular adaption algorithm chosen. These are inherently sensitive to errors occuring in critical gain control bits which produce annoying impulsive changes in signal amplitude. The magnitude of this effect can be reduced by slowing the adaption rate but transient response is then degraded. System design becomes a compromise between acceptable error performance which requires a slowly varying scaling signal, and good transient performance which requires fast adaption.

Use of fixed pre-emphasis can yield a good compromise between audible noise modulation and low to middle frequency headroom. However, even if the reduction in high frequency headroom is acceptable, the de-emphasis characteristic only serves to increase the audibility of low frequency noise components which may not be masked by signal.

Output-controlled a.d.m. is therefore unlikely to meet the requirements of a high quality codec system.

Quantization noise in a properly designed codec is approximately uniformly distributed over the Nyquist bandwidth. In over-sampled systems such as delta modulation much of the noise power is outside the audio band and noise shaping[3] can be employed to redistribute the quantization error energy so as to reduce the in-band noise level at the expense of out-of-band noise. Figure 1 illustrates the noise distribution for a 32kHz sampled p.c.m. system. Figure 2 illustrates the benefit of noise shaping in an over-sampled system. The delta modulation system under consideration here operates at a minimum of six times over-sampling; noise shaping is employed to reduce the otherwise flat in-band noise spectrum at a 6dB/octave rate with decreasing frequency below about 15 kHz. Noise in the region of 1kHz is suppressed approximately 15dB.

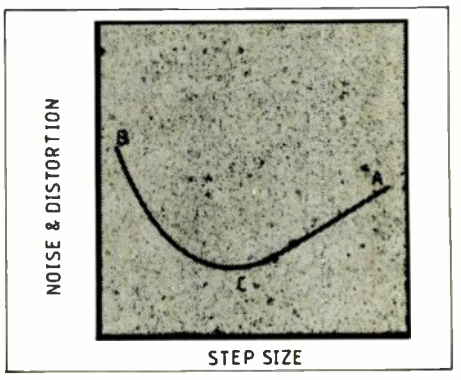


Fig.3. Small step size B introduces distortion due to slope overload, large step size A introduces excessive quantization noise. Optimum step size C minimizes noise and distortion.

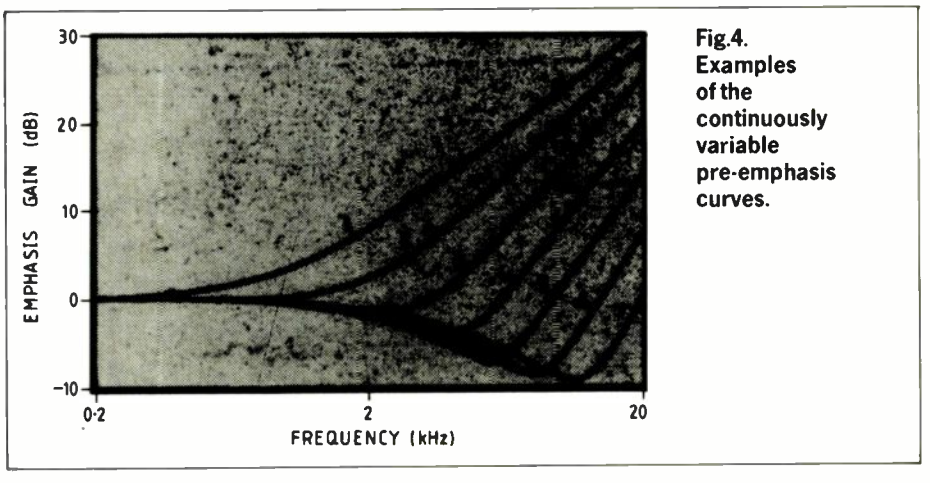


Fig.4. Examples of the continuously variable pre-emphasis curves.

OPTIMIZED DELTA MODULATION

Quantization error and distortion products at an a.d.m. codec output are functions of two varying parameters; step size and the audio signal itself. Consider the codec operating on a single sine wave. Output noise and distortion will vary qualitatively with step size as shown in Fig.3. In region A the step size is too large resulting in excess quantization noise. In region B step size is too small resulting in excess distortion and noise due to delta modulator slew-rate limiting. In region C step size is optimized so as to fully load the delta modulator and produce minimum quantization noise. Delta modulator performance can be optimized in a similar way for each short segment of audio if the step-size control signal is continuously variable.

Conventional output-controlled a.d.m. codecs operate in region A most of the time and move into region B on signal transients; codec performance is never quite optimized. In our system, delta modulator step size is input-controlled thereby achieving continuous optimum operation in region C.

VARIABLE EMPHASIS

Noise in an optimized a.d.m. system depends on the program signal. In the presence of a fixed-amplitude sine wave for example, codec noise is directly proportional to signal frequency. Conventional fixed emphasis can be effective in reducing quantization noise if the spectral components of the desired signal are predominantly low frequency in nature. However, h.f. signals boosted by the emphasis require greater step size and therefore produce more broadband quantization noise.

De-emphasis will restore the overall frequency balance and maintain the noise level in the region of the signal components at its original level before emphasis, but will increase the audibility of low-frequency noise components not masked by the program signal. For this reason conventional fixed emphasis alone is insufficient to provide high quality performance with a.d.m.

In studying the resultant quantization noise spectra in an optimized a.d.m. codec operating at 200 to 300kb/s there are three distinct operating modes to be considered if noise suppression via emphasis is to be optimized. The conclusions listed over are drawn on the basis that the predominant signal spectral components are confined to a narrow region of the audio band on a short-term basis. In fact, such a signal (sine wave) is the most critical case.

● When the dominant spectral components

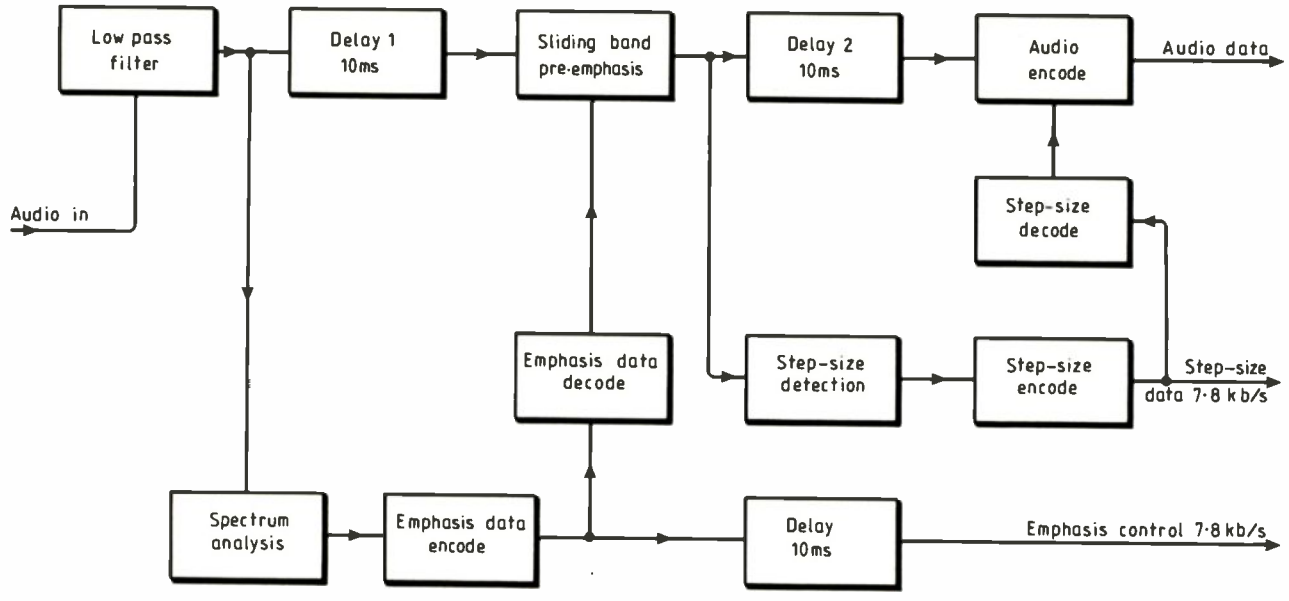


Fig.5. Simplified schematic of the adaptive delta modulation digital to analogue converter.

of the signal lie below 500 Hz substantial high frequency pre- and de-emphasis is desirable to eliminate noise audibility. Low frequency noise is masked by the signal.

- Dominant middle frequency signal components (i.e. 1 to 3kHz) still provide some low-frequency noise masking, but it is desirable to slide the emphasis up in frequency so as to maintain the benefit of high-frequency noise suppression without increasing the quantization noise by boosting the audio signal.
- Dominant signals above 3kHz can effectively mask noise in the region of the signal spectrum but noise at either end of the audible spectrum (particularly l.f. noise) will not be as well masked. A pre-emphasis curve with a notch in the region of the dominant signal spectrum will decrease the required step size and result in reduced quantization noise. Complementary de-emphasis will then pick out the signal components while suppressing both low and high-frequency noise not masked by the signal spectrum.

A family of pre-emphasis curves achieving the desired variable response is shown in Fig.4. Curve 1 is most appropriate for signals of predominant low frequency content. Response curve 3 is more desirable for signals with predominant middle frequency components. If the predominant signal components are in the 6kHz region, curve 5 would be the desired emphasis response.

NARROW BANDWIDTH CONTROL SIGNALS

The adaptive operation of system frequency response or gain control via a control signal is a waveform multiplication process whereby control signal sidebands are superimposed on the spectral components of the audio signal. Depending on the extent of sideband spread and the nature of the program signal the audibility of such sidebands may become objectional.*

In the input-controlled codec system

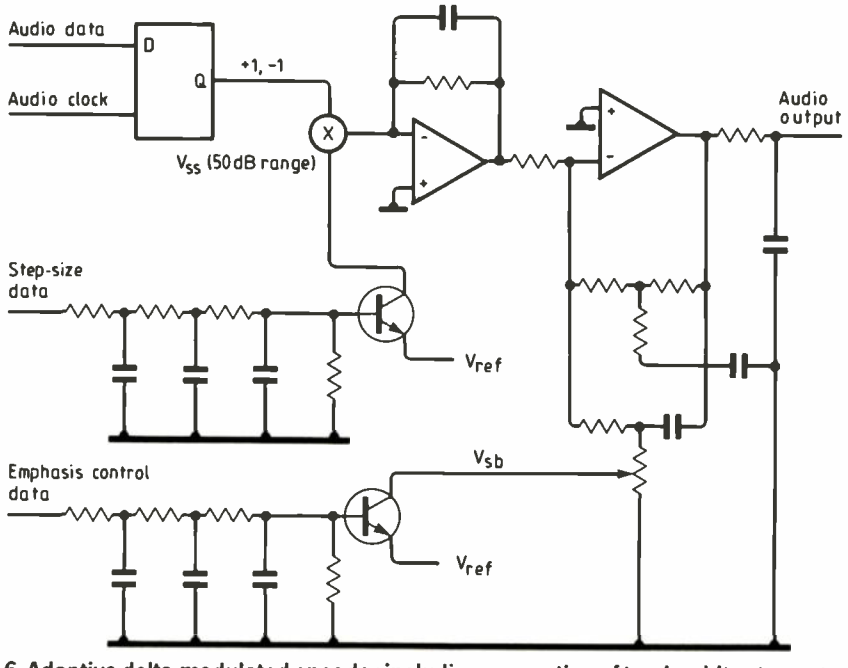


Fig.6. Adaptive delta-modulated encoder including generation of two low bit-rate control signals.

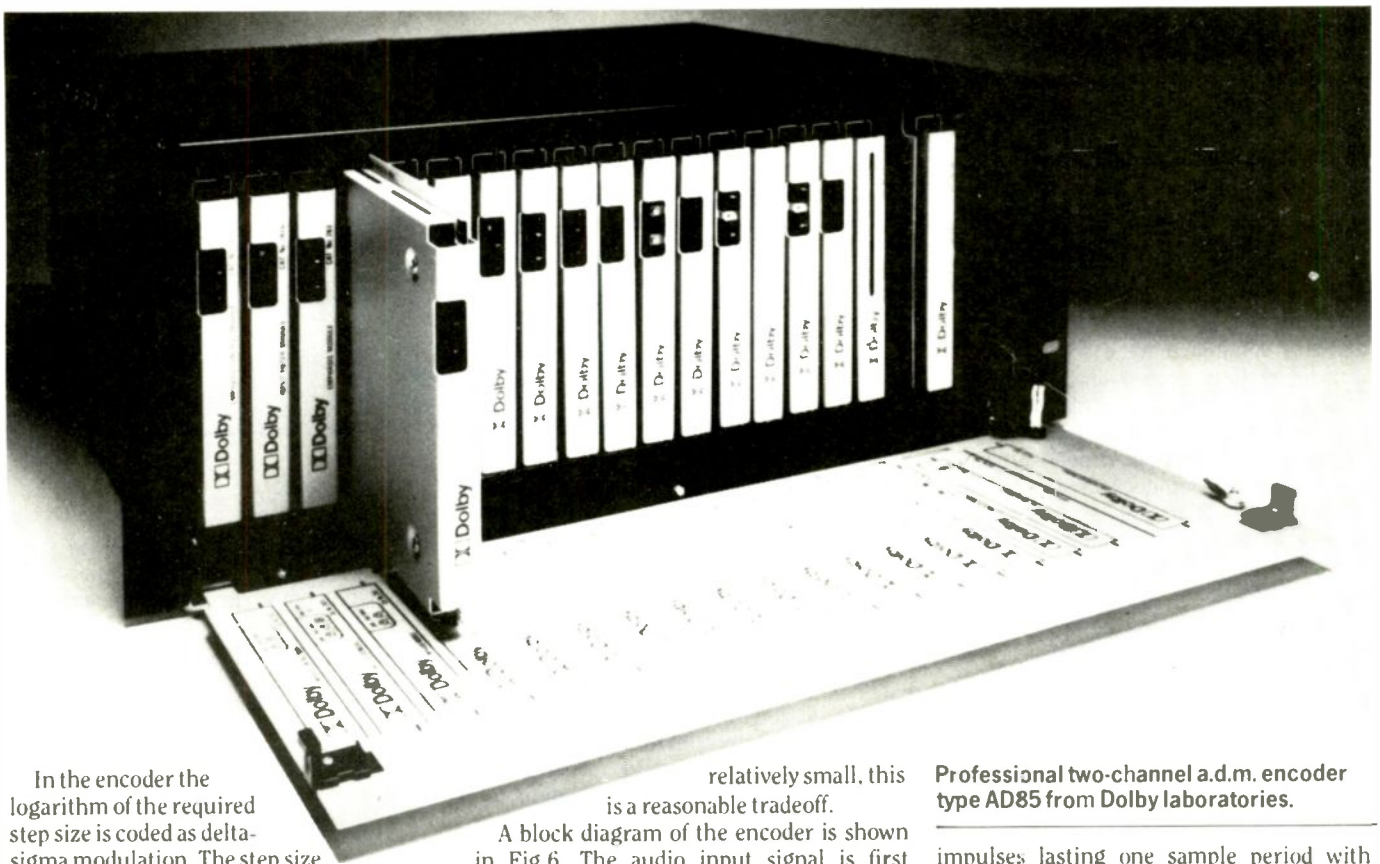
under consideration, control signals are generated in the encoder and conveyed to the decoder in separate 8kb/s data streams. The audio signal is analysed in advance through use of delay lines in the encoder; thus narrow band (50Hz) control signals can be used with no compromise in transient performance. The fact that the control signal is of narrow bandwidth and is greatly oversampled, leads to a robustness in performance in the presence of transmission bit errors, and with decoder parts of wide tolerance.

CONSUMER DECODER

In developing this new codec we realised that the complexity of the decoder circuitry appearing in the consumer's home could be greatly reduced at the expense of some additional complexity in the encoder; this is a fundamental aspect of our approach.

A conceptual diagram of the consumer decoder is shown in Fig.5. Each single channel decoder receives three data streams; audio data, step-size control data, and emphasis control data. The audio data-rate is typically in the range of 200 to 350kb/s, while the step size and emphasis control data are at much lower data rates, typically around 8kb/s each. In audio-with-video applications a convenient data rate is half the horizontal line rate.

*In theory, complementary noise reduction systems introduce antiphase control sidebands during expansion. However, due to parts tolerances in practical circuit implementations, control sidebands may not be completely cancelled. To minimize the audibility of those components that remain taking advantage of the masking effects of large signals on closely spaced sidebands) it is desirable to reduce control-signal bandwidth as much as possible while maintaining system dynamic performance. The slower the control signal can be made, the more tolerant the system will be to control signal mistracking.



Professional two-channel a.d.m. encoder type AD85 from Dolby laboratories.

In the encoder the logarithm of the required step size is coded as delta-sigma modulation. The step size control signal is decoded by low-pass filtering the step-size data stream to recover its average value and exponentiating the resultant voltage. The emphasis control signal is decoded in an identical fashion, and is used to control the variation in pole frequency in the adaptive de-emphasis network.

Wide dynamic range audio is recovered by multiplying the incoming audio data by the decoded step-size control signal, and integrating the resultant signal in a leaky integrator. The recovered audio is then dynamically de-emphasized; a final single de-emphasis pole provides sufficient attenuation of spurious out-of-band components that no further output filtering is required.

The decoder is relatively simple and does not require components of extraordinary precision. Performance degradations produced as a result of component tolerances produce errors in gain and frequency response similar to analogue systems; no mechanism exists for creation of high-order non-linearities such as those produced by non-monotonic p.c.m. converters.

Decoder cost is low. The original decoder design using available discrete components – at a cost of about £4 for a two-channel circuit – has been superseded by a specially designed integrated circuit from Signetics. This 28-pin two-channel device requires only a few external components to achieve the low-pass filtering of the two control signals and a simple low-pass audio output filter.

PROFESSIONAL ENCODER

The fundamental requirement of the system encoder is to produce the necessary bit streams for high quality reproduction via the cost-effective decoder. The encoder is significantly more complex than the decoder, but as the number of encoders required is relatively small, this is a reasonable tradeoff.

A block diagram of the encoder is shown in Fig.6. The audio input signal is first low-pass filtered to remove any spurious components. The spectrum of the band-limited audio is then analysed to determine the optimum emphasis characteristic to minimize the audibility of quantization noise in the presence of that particular signal spectrum. An emphasis control signal is generated and encoded in a low data-rate bit-stream, and locally decoded for control of the encode variable emphasis. By definition, the control signal decode operation limits the recovered control signal bandwidth to approximately 50 Hz, corresponding to a rise time of about 10ms. The audio is therefore delayed by 10ms before pre-emphasis.

Emphasized audio is examined in the step-size detection circuitry to determine the signal slope and corresponding optimum step size. The logarithm of this value is coded into the step-size data stream and decoded with an identical bandwidth limitation as that of the emphasis control signal. The emphasized audio is delayed a further 10 ms before being applied to the delta modulator.

Since the emphasized audio is delayed relative to the emphasis control signal, an equal delay of 10ms is required in the emphasis control data path so that all data arrive properly timed at the decoder.

ERROR CONTROL

Step-size and emphasis control signals are of narrow bandwidth and are greatly oversampled in the encoding process. As a result, errors occuring in the control bit streams produce only minor audible effects; error rates approaching 10^{-2} can be tolerated without correction.

The effects of reproduced errors in p.c.m. and delta modulation systems are quite different. Those in p.c.m. introduce audible impulses lasting one sample period with amplitudes corresponding to the significance of the bit in error. Errors in the most significant bit will produce impulses of half full-scale. In contrast, an error in a delta modulation system will produce a small exponentially decaying step extending over the decay time period of the decode integrator. Experimental results indicate that reproduced p.c.m. errors are virtually always audible, while the reproduced errors in practical delta modulation systems are more likely masked by the program spectrum. Therefore even at low error rates (i.e. 10^{-5}) a viable p.c.m. system necessarily requires some form of error protection, while a delta modulation system may perform quite well without error concealment or correction.

Error concealment in p.c.m. systems works by reducing the amplitude of error impulses through data interpolation or substitution. Concealment can give good results as long as errors are reliably detected, but at higher error rates, where concealment may break down, detection can become unreliable resulting in reproduction of many unconcealed errors. Signal muting is often employed under these conditions, but erratic muting is likely to be equally annoying.

In delta modulation, error concealment works by reducing the duration of the error step. In our scheme, encoded audio data is blocked into N-bit blocks and a modulo-4 summation is performed based on the number of data ones in the block. The two-bit result is transmitted along with the data block as redundant information. In the presence of single-bit errors, an identical modulo-4 summation performed in the decoder will yield information on the presence and polarity of the transmission errors.

On detection, the error is concealed by intentionally introducing an error of opposite polarity into the respective data block.

Since it is desirable to introduce the artificial error as close to the original error as possible, it is inserted in the middle of the concealment block. The time difference between real and artificially induced errors range between zero and N/2 bits; the average difference being N/4 bit periods. The step introduced into the audio will consequently average N/4 bit periods in length.

Note that 2/(N+2) errors will hit a concealment bit causing misconcealment. The probability of misconcealment can be substantially eliminated by carrying a third parity bit on the two concealment bits.

Pulse-code and delta modulation system operating with proper concealment will produce small error steps of similar duration. However, errors reproduced in a delta modulation system operating above the error rate where the concealment begins to break down are likely to be tolerable, thereby reducing the need to mute. A delta modulation system is likely to be usuable at an order of magnitude greater error rate than a p.c.m. system.

CODEC PERFORMANCE

In theory, the codec dynamic range can be greater than 100dB and is largely independent of audio data rate. Practical hardware implementations readily achieve 90dB.

Non-linear distortions are not inherent in delta modulation, but practical considerations in first generation analogue circuitry yield performance near 0.15%. In contrast to p.c.m. however, distortion decreases at lower operating levels.

At audio data rates near 300kb/s/channel subjective performance closely rivals 16-bit linear p.c.m. The minimum recommended audio data rate for high-quality audio applications is about 200kb/s/channel. At this data-rate codec performance is still quite good; noise modulation becomes audible only under critical listening conditions with high quality program material. Future improvements in encode technology may yield a further reduction in the recommended minimum bit-rate.

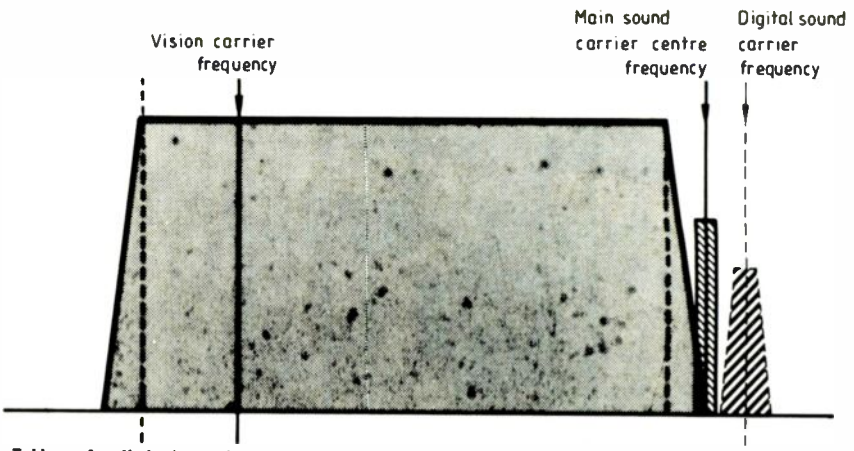


Fig.7. Use of a digital carrier within an existing television channel.

BROADCAST DATA FORMATS

Use of any digital codec system in broadcasting requires some form of data formatting to permit subsequent synchronization and recovery at the receiver. Audio data is typically transmitted accompanied by recognizable synchronization patterns as a continuous stream, or in packets or bursts. Transmission of audio data in the horizontal blanking interval of an NTSC or PAL television signal or via data packets in MAC systems are examples of burst formats.

The data rate of our system is quite flexible. However, in audio-with-video applicants it is desirable to operate at a data rate coveniently related to video frequencies. Present systems use one-half horizontal line rate for control data and a multiple of the line rate for audio data. In one application 13 bits of audio data for each of 2 audio channels is transmitted on each line of an NTSC video format. A second system, using a B-MAC* format conveys six audio channels with 13 bits per line at an audio data rate of 204 kb/s.

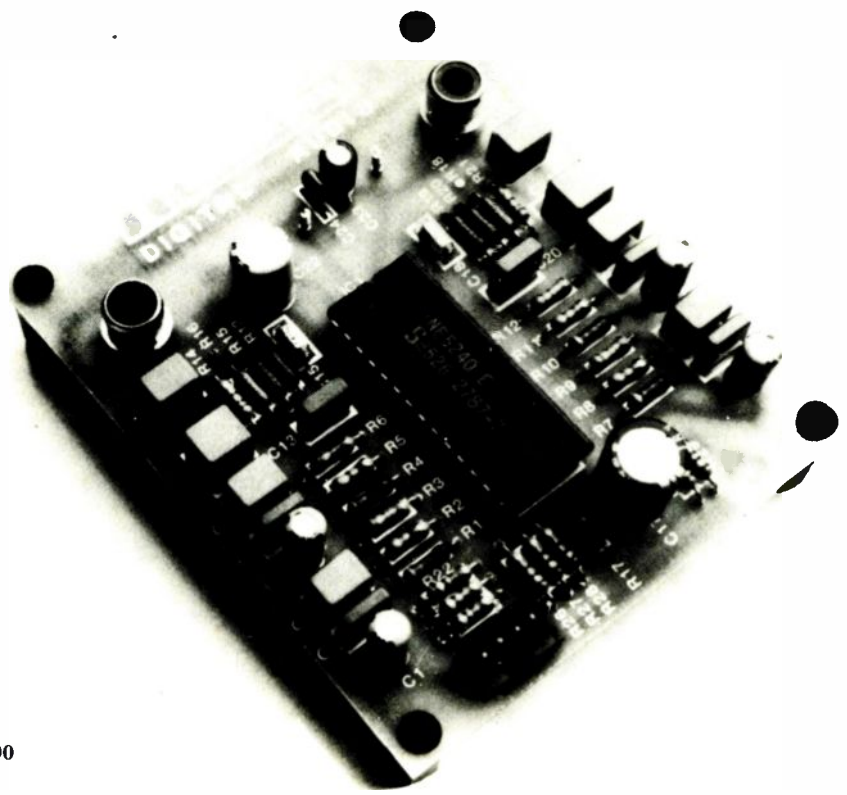
An alternative method employing continuous transmission of digital signals is to use a digital subcarrier using a modulation scheme such as quaternary phase-shift keying to transmit the digital data. This technique is particularly suited to the transmission of high quality stereo sound on the existing terrestial television network where the new subcarrier can be placed above the existing analogue sound subcarrier Fig.7.

In addition to the B-MAC satellite broadcast system adopted in Australia, the independent television stations have recently decided on a professional distribution system for picture and up to four high quality audio channels. This uses a.d.m. on a single subcarrier above the vision signal (page 335-ed.).

CONCLUSION

The delta modulation codec described provides an efficient, cost effective, and practical means to convey a multiplicity of audio and audio-with-video programming of excellent quality to consumers via d.b.s., cable tv and terrestial broadcasting systems. Further improvements in encoder technology should allow cost reductions leading to more widespread use. Future development will provide the means to further control the dynamic range of the received programme in the home without some of the limitations of existing compression and limiting systems.

Implementation of consumer decoder using Signetics NE5240 integrated circuit.



*Adopted by the Australian Department of Communications for HACBSS satellite broadcasting.

Steven E. Forshay is with Dolby Laboratories, Inc. at San Francisco, California, and acknowledges original codec development by colleagues K.G. Gundry and C.C. Todd.

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Pioneers

3. André Marie Ampère (1775-1836), father of electrodynamics.

W.A. ATHERTON

Execution by guillotine is a grisly death. For an 18-year old to have his dear father die in that way is a profound psychological shock. Ampère's father died on the guillotine during the French Revolution on 22nd November 1793. Grief stricken, the man whose name we now remember on every plug, socket and fuse became a recluse for almost a year.

Though his life continued to be marred by tragedy and unhappiness, Ampère grew to become a respected mathematician and chemist, and an electrical physicist of world renown. Maxwell dubbed him the Newton of electricity.

Even if Ampère had died in the early months of 1820 he would still be honoured for his contributions to mathematics and chemistry. In France, for example, Avogadro's law is known as the Avogadro-Ampère law, Ampère having arrived at it just three years later. He was also narrowly beaten to the recognition of chlorine (1810) and iodine (1813) as elements, this time by Humphrey Davy. However it was his monumental work on electrodynamics from 1820 to 1827 which earned him a place in history. It was he who founded the subject and he who crafted the beauty of its structure.

André Marie Ampère was born in Lyons on 22nd January 1775. His father was a well-to-do merchant who moved his family out of the city soon after his son was born. Ampère grew up in a village. His home was his school. His education, provided and supervised by his father, seems to have consisted mainly of studying a large variety of books. In this way he discovered and developed interests in science, metaphysics, mathematics and poetry, even teaching himself Latin in order to read the mathematical works of Euler and Bernoulli.

Besides science and mathematics (and Latin!) Ampère also received a thorough grounding in the Roman Catholic faith. As with Oersted, his Christianity helped determine his view of nature. Throughout his life the teachings of Christianity and those of the 18th century philosophers, though not always mutually supportive, had great influence on him.

After the tragedy of his father's grotesque death, a year passed before Ampère began to rejoin the world, partly through a study of botany and by writing poetry. By the time he was 22 he had met the girl he was to marry, but had no skilled trade and only a small inheritance. Financial worries were never to be far away.

The couple married in 1799 and settled in Lyons, with Ampère earning a modest living by teaching mathematics. His first published paper, on the theory of games of chance, earned him a step up the academic ladder to the post of professor of physics and chemistry at Bourg, near Lyons, and more money.

Tragedy however struck again. After less than four years of very happy marriage his wife died. Plunged into grief and despair, Ampère left Lyons, with all its memories, for Paris.

There he made an unfortunate second marriage and was swindled by his new father-in-law. After a divorce he set up a new home in Paris with his mother and an aunt, where his two children, a son by his first wife and a daughter by his second, were raised. And there he conducted his immortal work.

Paris was now his home. He obtained a position at the renowned École Polytechnique, became a member of the Institut Imperial (1814), and in 1819 began teaching at the University of Paris. His contemporaries included Arago, Biot, Savart, Laplace and Poisson. It is said that he was the epitome of the absent-minded professor – even forgetting a dinner date with the Emperor Napoleon, so the story goes.

For the first time, an experiment on magnetism had been performed without a magnet...

For much of his life Ampère seems never to have been far from unhappiness, a stunning contrast to the public service he rendered. Late in life, it is said, he confessed that only a few years had brought him real happiness. He died alone in Marseilles on 10th June 1836, aged 61.

Oersted's epic discovery that an electric current produces magnetic effects was published on 21st July 1820 and it created a sensation. For many years there had been speculation that electricity and magnetism were somehow connected, yet Charles Coulomb some 30 years before had apparently proved beyond doubt that they were not. Oersted's news was conveyed by François Arago to an astonished meeting of the French Académie des Sciences on 4th September.

In the days and weeks that followed, Paris witnessed the spectacular birth of a new branch of science as Ampère dissected Oersted's discoveries and extended them. Others joined in, notably Arago, Jean Biot and Félix Savart in Paris, Johann Schweigger in Germany, and Davy in London.

But it was Ampère who became the major contributor to the new science of *electrodynamics*, the name which he gave to the newly-discovered phenomena. "It expresses their true character, that of being produced by electricity in motion", he wrote. The older type of electricity he named *electrostatics* – "phenomena produced by the unequal distribution of electricity at rest in the bodies in which they are observed" (1822).

As well as giving us these new words, Ampère carefully defined the terms electric current and electric tension (voltage), phrases which had until then been used very loosely. Ohm's Law, however, was still a few years away (1826-27).

After verifying Oersted's work Ampère expressed it as a law: if an observer had a current flowing from his feet to his head then a needle placed in front of him would have its north-seeking pole deflected to his left. Later this was re-expressed as the right-hand screw rule.

With electricity and magnetism now known to be related the question arose as to which of the two was the fundamental phenomenon.

To Ampère, electrical 'fluids' seemed more likely than magnetic 'fluids' as the fundamental cause, and so he formed an hypothesis that electric currents were the cause of magnetism. If that were true, then when an electric current caused a magnetic compass needle to move it could only be the

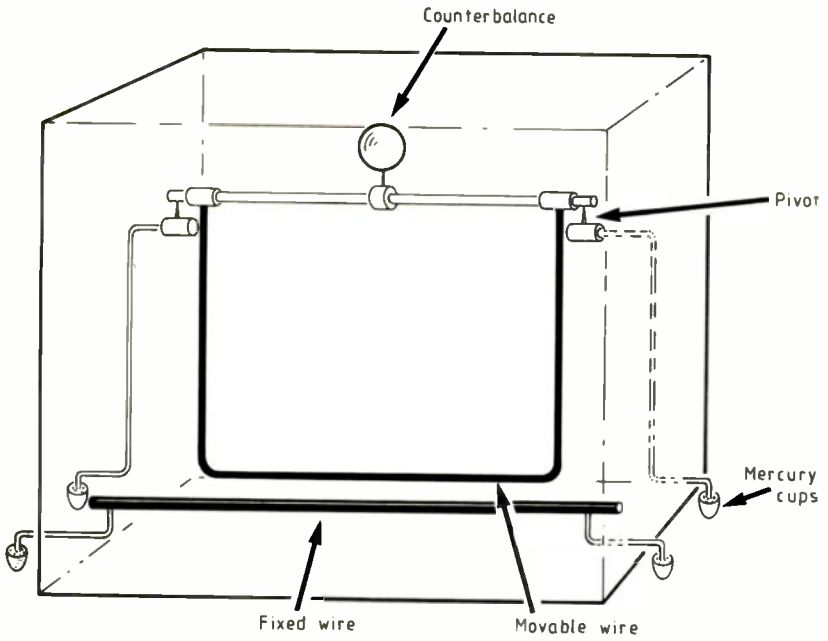

Fig 1: Ampère's experiment to show the magnetic effects of one (direct) electric current on another. The movable wire, hanging on pivots, would swing towards or away from the fixed wire when currents flowed. Connections to the wires were via cups of mercury.

result of electricity acting upon electricity. Therefore, he reasoned, two electric currents should interact via their magnetic effects. In a very clever experiment, suggested by Laplace, two parallel wires each carrying a current were shown to attract one another magnetically when the currents were in the same direction and repel when the currents were in opposite directions (Fig.1).

For the first time an experiment on magnetism had been performed without a magnet.

Ampère proved that these attractions and repulsions were not due to electrostatic phenomena. He described them as *voltaic* so as to emphasise the point. The name of Volta, the Italian inventor of the electric battery, was passing into electrical terminology.

Ampère also gave us the word *galvanometer*, an instrument he described as *"similar to a compass needle, which, in fact, differs from it only in the use that is made of it."*

He simply placed the conductor horizontally above or below the magnetic compass needle. The direction of movement of the needle indicated the direction of flow of the current through the conductor and the angle indicated the magnitude. He named the instrument after Galvani, the Italian usually credited with the discovery of the electric current. It was left to someone else to name its unit of measurement after Ampère himself.

With a little of what now might be called lateral thinking Ampère suggested another use for the galvanometer.

"By employing as many conducting wires and magnetized needles as there are letters, by fixing each letter on a different magnet ... we may form a sort of telegraph, by which we can write all the matters we may wish to transmit."

He also suggested a way of using a keyboard at the transmitter to make transmission easier.

Ampère's telegraph was never built and it is doubtful that his suggestion contributed to the electrical telegraphs which appeared a decade and a half later. But the galvanometer certainly did. When telegraphs did appear, codes were used to minimize the number of wires and needles needed.

Meanwhile in Germany, Schweigger had made a more sensitive galvanometer using a coil of wire of about 100 turns with a compass needle pivoted within it. Announced on 13th September 1820, it was this *multiplier* that became important in the early telegraphs.

Ampère also used coils of wire which he named *solenoids*. He and others showed that such coils could imitate all the effects of a magnet. These solenoids were developed into powerful electromagnets, notably by William Sturgeon in England and especially by Joseph Henry in America. By 1831 Henry had constructed a giant which could lift a ton.

Ampère now made a great conceptual leap.

A bar magnet itself could be explained, he suggested, by assuming the presence of circular electrical currents within it, running concentric to its axis. Such concentric currents might, he speculated, originate from contact between the molecules of the material. It was now only a short step for him to explain the Earth's magnetism as being caused by electric currents running from east to west.

However, Ampère's friend Augustin Fresnel, of wave theory of light fame, pointed out a serious flaw. Iron was not a very good electrical conductor and any such currents would produce a very noticeable heating effect in bar magnets. If Ampère was right, bar magnets should be warm, perhaps hot. And they are not.

Fresnel himself suggested a way out of the dilemma. Since nothing was known of the internal properties of molecules, why not assume circular currents within the molecules themselves? Ampère did. And he used this electrodynamic molecule in developing his mathematical theory of electrodynamics. The magnetism of a bar magnet was simply the sum of the magnetic effects of the molecular currents. Further, in materials such as iron, nickel and cobalt, the randomly-oriented molecular currents, which summed to give a zero effect, could be realigned by the action of other currents so as to produce a permanent magnet. In other non-magnetizable materials this realignment did not take place.

This theory was later considerably advanced by Wilhelm Weber and became the basis of his theory of electromagnetism. It is still basic to our understanding of magnetism.

Ampère pushed his electrodynamic molecule even further, suggesting that it was not only the source of electromagnetism but also of chemical combinations. He was suggesting a whole new theory of matter, but it had relatively few adherents.

It is of interest to note a comment of nearly a century and a half later. R.P. Feynmann, a renowned physicist discussing modern quantum electrodynamics in 1965 wrote: "In this one theory we have the basic rules of all ordinary phenomena except for gravitation and nuclear processes. For example, out of quantum electrodynamics come all known electrical, mechanical, and chemical laws."

Though he could not have foreseen quantum electrodynamics Ampère would have found the theory to his liking.

Ampère was without doubt the leading light of the period, performing many beautiful experiments and consolidating electrodynamics into a mathematical subject. In 1827 he published a synthesis of his work which became famous and is still the foundation of the mathematical theory of electrodynamics.

Later, his interests returned to a subject he had long held dear; the classification of the sciences. His last major publication was his own classification published in 1834, which he is said to have regarded as the capstone of his career. Today it is largely forgotten. The capstones that history awarded him are his work on electrodynamics and the naming after him of the unit of electric current. And, of course, the words he coined: electrodynamics, electrostatics, galvanometer, voltaic and solenoid.

Though his epitaph is "Happy, at last", a tribute by his son is more fitting: "He was never content with probabilities but always sought Truth."

Tony Atherton works at the Independent Broadcasting Authority's engineering training college in Devon. His book, From Compass to Computer, A History of Electrical and Electronics Engineering, was published by Macmillan in 1984.

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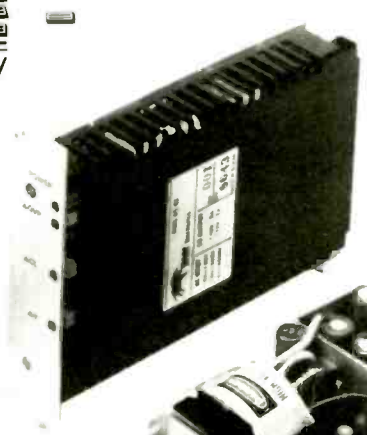

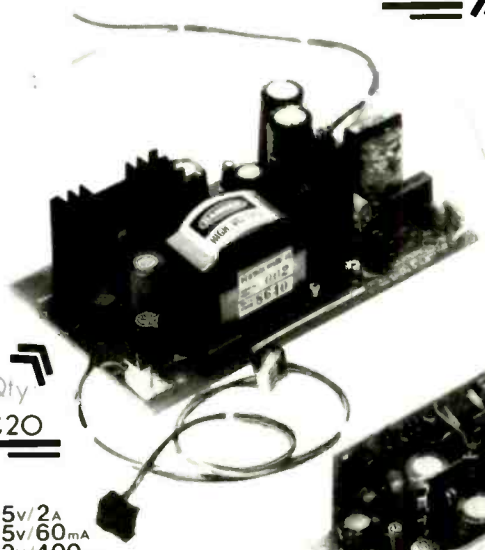

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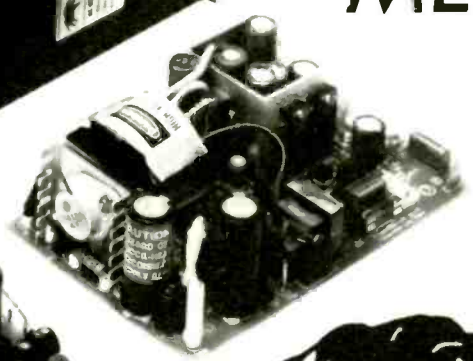

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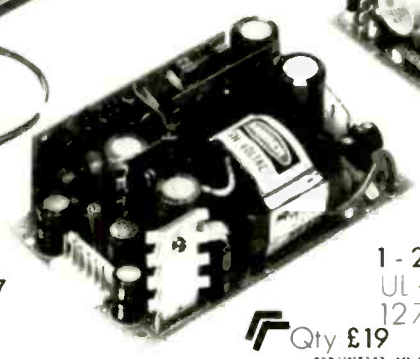

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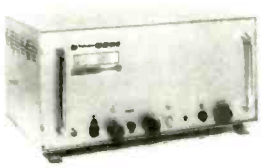

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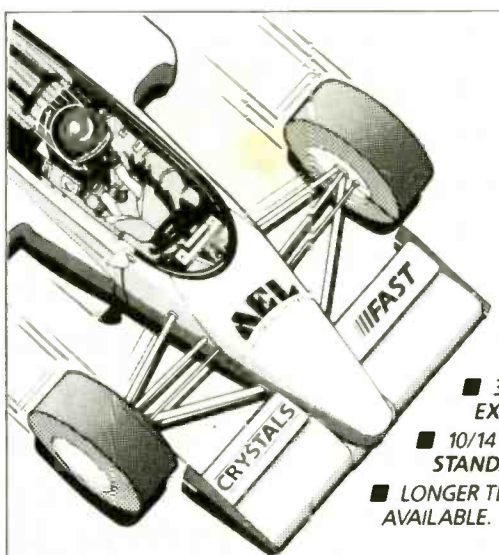

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CIRCUIT IDEAS

SIBILANCE DISTORTION SUPPRESSOR

Distortion in a communications channel or recording process, for example due to over-modulation, is noticeable on sibilants. This distortion takes the form of added lower-frequency signals, causing a rushing-wind sound during words containing the letter s. Suppressing low-frequency signals when high-frequency signals are present makes this effect less irritating.

Input feeds a high-pass filter which drives an electronic switch via a rectifier with adjustable bias. The input signal also feeds a second high-pass filter and a low-pass filter. Output from these filters is normally mixed but when there is a large burst of high frequency, the electronic switch stops the low-pass signal.

Since filter requirements for detecting sibilance and passing frequencies to the output are different, two independent filters are used. Two Sallen and Key high-pass filters are cascaded to detect sibilance and their output is passed to the active rectifier.

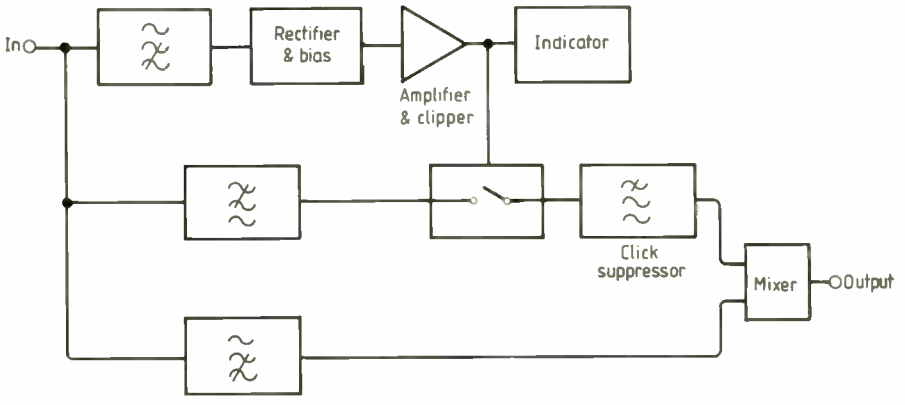
Filters in the signal path were chosen by trial and error to give best results with the material that prompted this design – a bad cassette recording of a lecture. Capacitors and resistors in these circuits could be made variable. Simple filtering removes switching clicks.

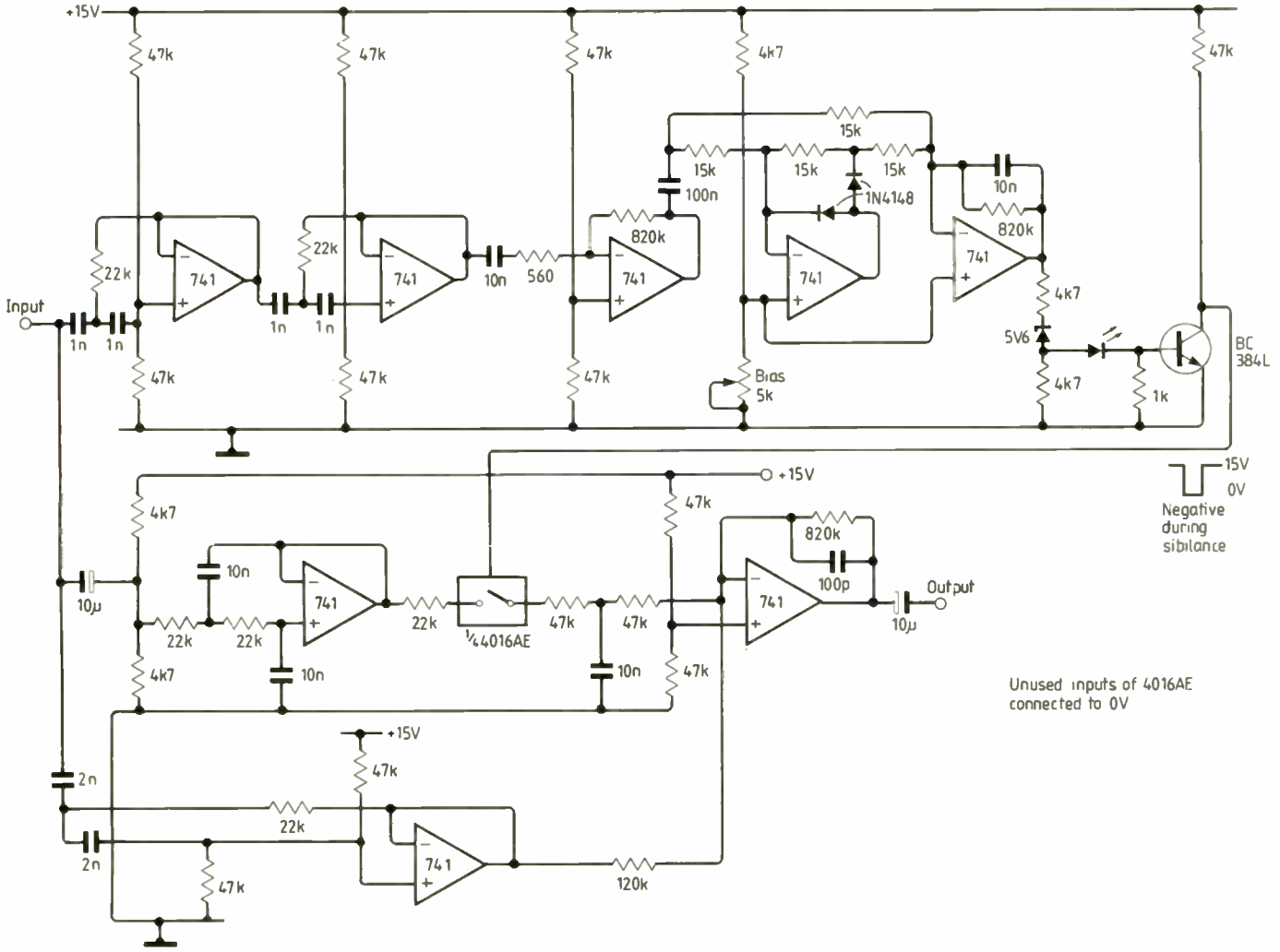
With the aid of a led which lights during sibilance, switching level is set using a bias control on the active rectifier. This control is advanced until the led lights on sibilant peaks. The prototype was connected between output of a cassette recorder and input of a loudspeaker amplifier.

John de Rivaz
Truro
Cornwall

Reading

Wireless World Circards 1.4 and 1.5 for low and high-pass Sallen and Key filters respectively and 1.3 for absolute value circuits.





CIRCUIT IDEAS

VOLTAGE-CONTROLLED STATE-VARIABLE FILTER

The well known Kerwin-Huelsman-Newcomb state-variable filter using a summer and two phase-inverting integrators suffers from two disadvantages – low gain and dependency between resonance frequency and Q. These disadvantages are removed when integrators without phase inversion are used as shown in the first diagram.

A positive integrator consists of a differential amplifier, IC_2, and non-inverting amplifier, IC_1, the feedback resistor of which is replaced by capacitor C. The XR2208 comprises a multiplier and a high-gain op-amp which forms the differential amplifier.

Output voltage of the integrator is

$$\frac{V_{in}}{sR'C}$$

where R' is AR/V_c. Multiplier constant A is given by the device manufacturer and V_c is control voltage. Integrator time constant is set to the desired value using V_c.

The second diagram shows a state-variable filter with two positive integrators whose transfer functions are

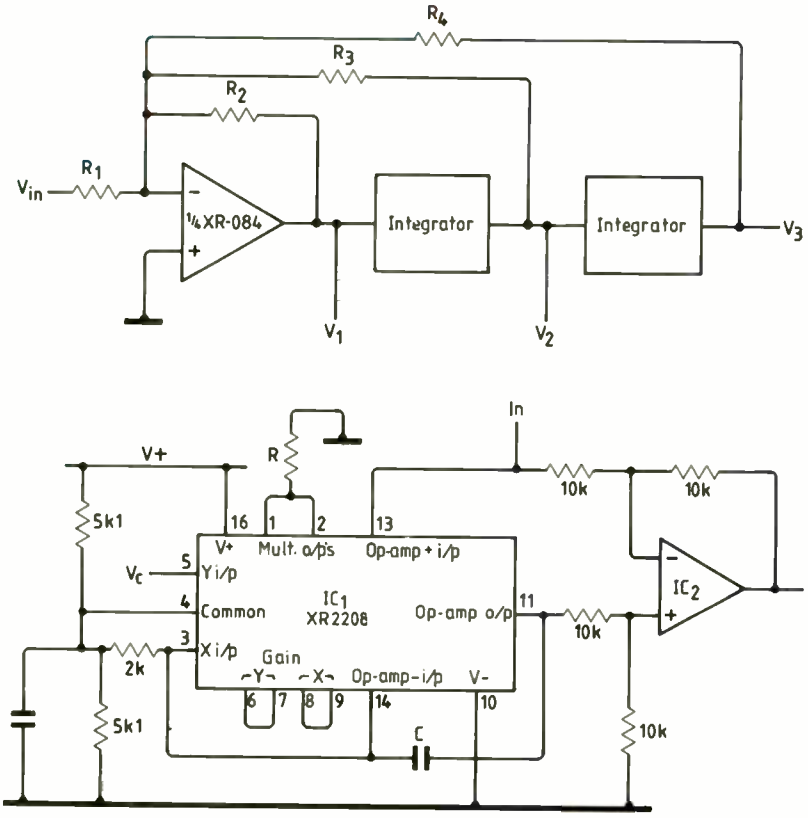
$$V_1/V_{in} = -G.s^2\tau^2/K$$
$$V_2/V_{in} = -G.s\tau/K$$
$$V_3/V_{in} = -G/K$$

where K is $1+R_2/R_3.s+s^2\tau^2$ and τ is the integrator time constant.
From this,

$$\omega_0 = 1/R'C$$
$$Q = R_3/R_2$$
$$G = R_2/R_1$$

Hence resonance frequency ω_0 is controlled by V_c. Q can be set to a desired value independently of ω_0 by varying R_3 and gain G can be varied using R_1. Values of Q and G are thus set to a value of the order of 10^2.

Kamil Kraus
Rokycany
Czechoslovakia

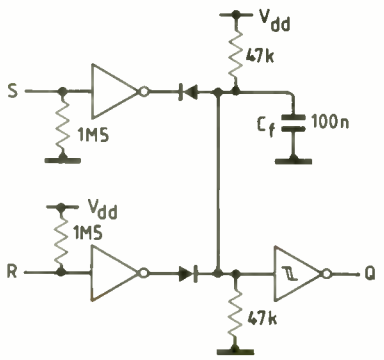


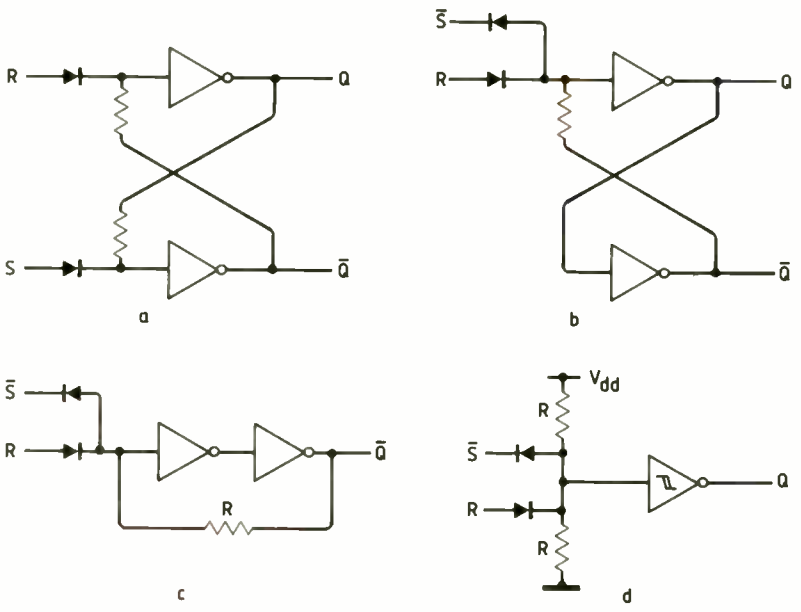
RS LATCHES USING INVERTERS

Three ways of producing an RS latch using two c-mos inverters and diodes are shown here, and an even simpler circuit using just one c-mos Schmitt inverter with diodes.

A touch-controlled latch is also shown (a.c. power supply is required). Touching S sets the Q output and touching R resets it. Optional capacitor C_f provides high-frequency noise immunity.

Hernán Tacca
Buenos Aires
Argentina



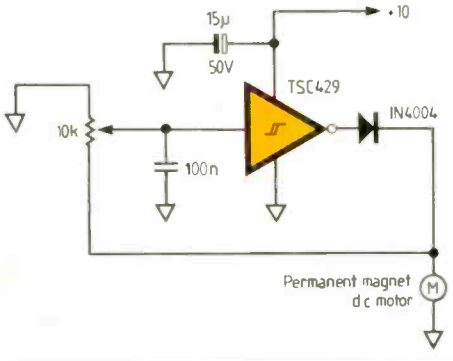


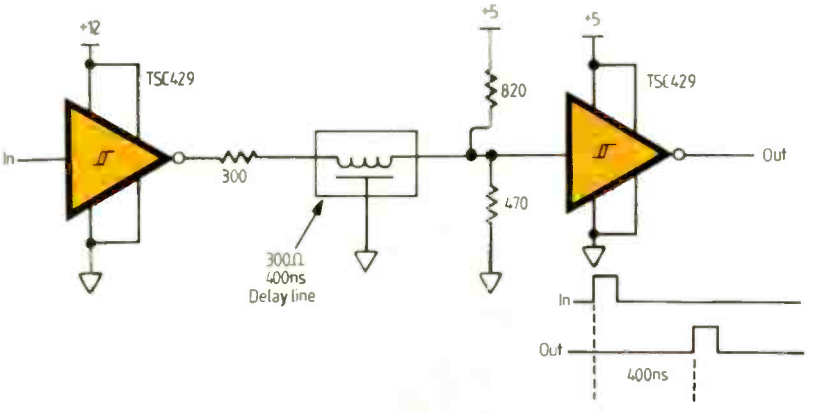
APPLICATIONS SUMMARY

HIGH-SPEED MOSFET INTERFACE I.C. IN POWER DRIVING

Back e.m.f. is used as feedback representing motor speed in this controller for small motors (left). Being capable of 6A peak output current, the TSC429 mosfet power interface i.c. can form both the comparator and driver so the circuit is simple. With rise and fall times of 25ns, the device is also fast enough to drive short-time delay lines (right).

These two circuits are from Teledyne application note 28, which covers operation and uses of the 429. There are a further seven circuits in the note, including voltage converters and a pulse-transformer driver.



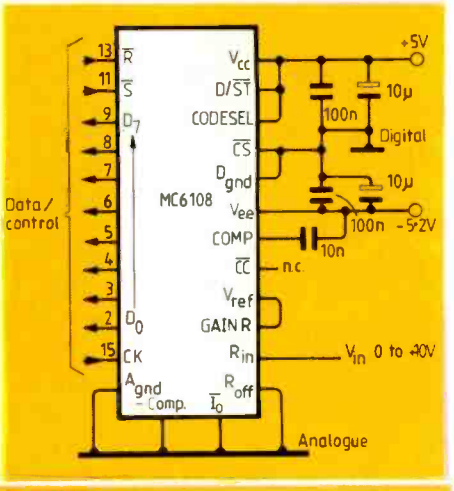


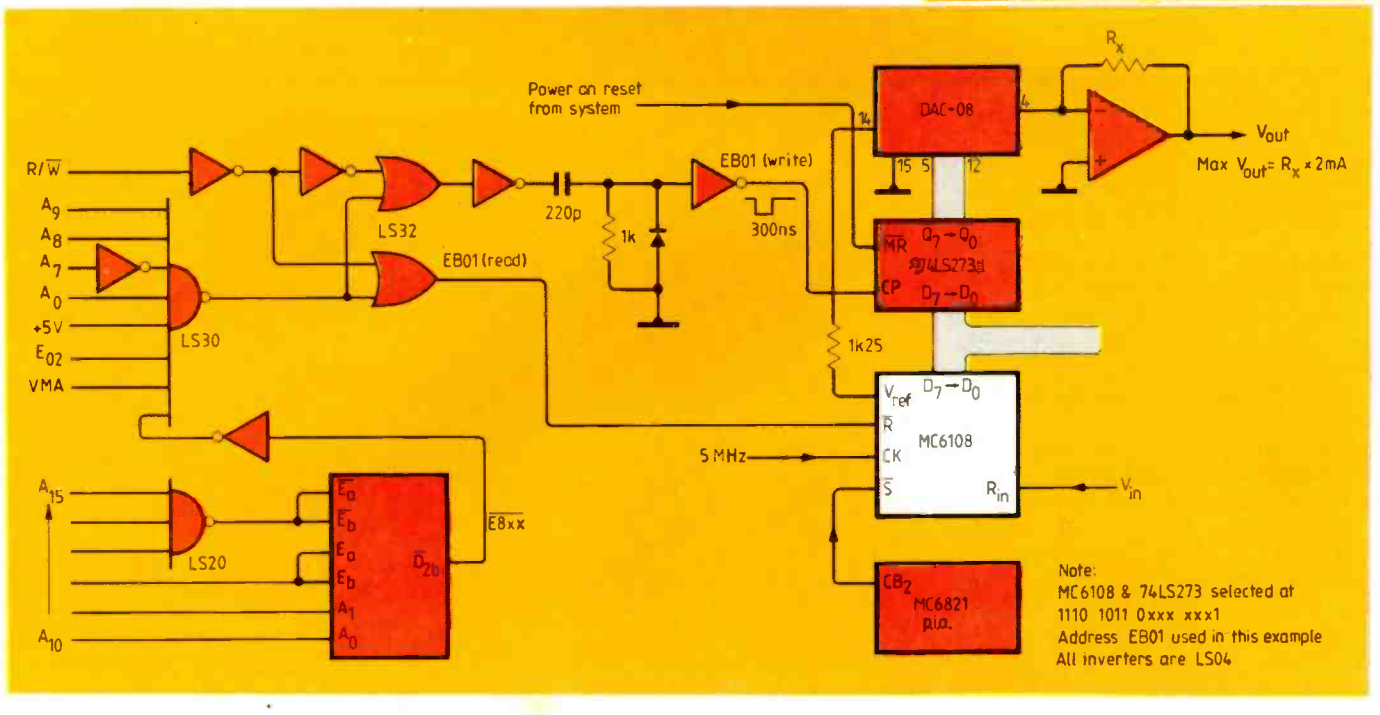
MICROPROCESSOR INTERFACING FOR A HIGH-SPEED A-TO-D CONVERTER

Assembly-language software is included in application note AN963 which describes how MC6108 eight-bit analogue-to-digital converters are interfaced to a 6802 processor system. Within the a-to-d converter is a d-to-a converter with comparator, successive approximation registers, a reference, control logic and three-state buffers.

Although operation of the converter is only guaranteed with a clock signal of up to 5MHz, the Motorola note says that clocks of up to 10MHz can be used when 'careful attention to detail' is paid. After describing how the converter operates, address decoding is discussed. Most of the examples given require a 6821 peripheral interface adaptor either to provide control signals or to control the converter and read its data but there is one example of interfacing the device directly using only address decoding.

In the example shown, analogue information can be read, processed, and then sent out as an analogue signal again through the DAC08 digital-to-analogue converter. Digital data to be converted to analogue form is held in a 273 octal latch when an active-low latching pulse is sent from the address decoder. On the rising edge of the pulse, data is transferred to the d-to-a converter. Output of the converter is a current proportional to the reference current and the digital data presented to it.





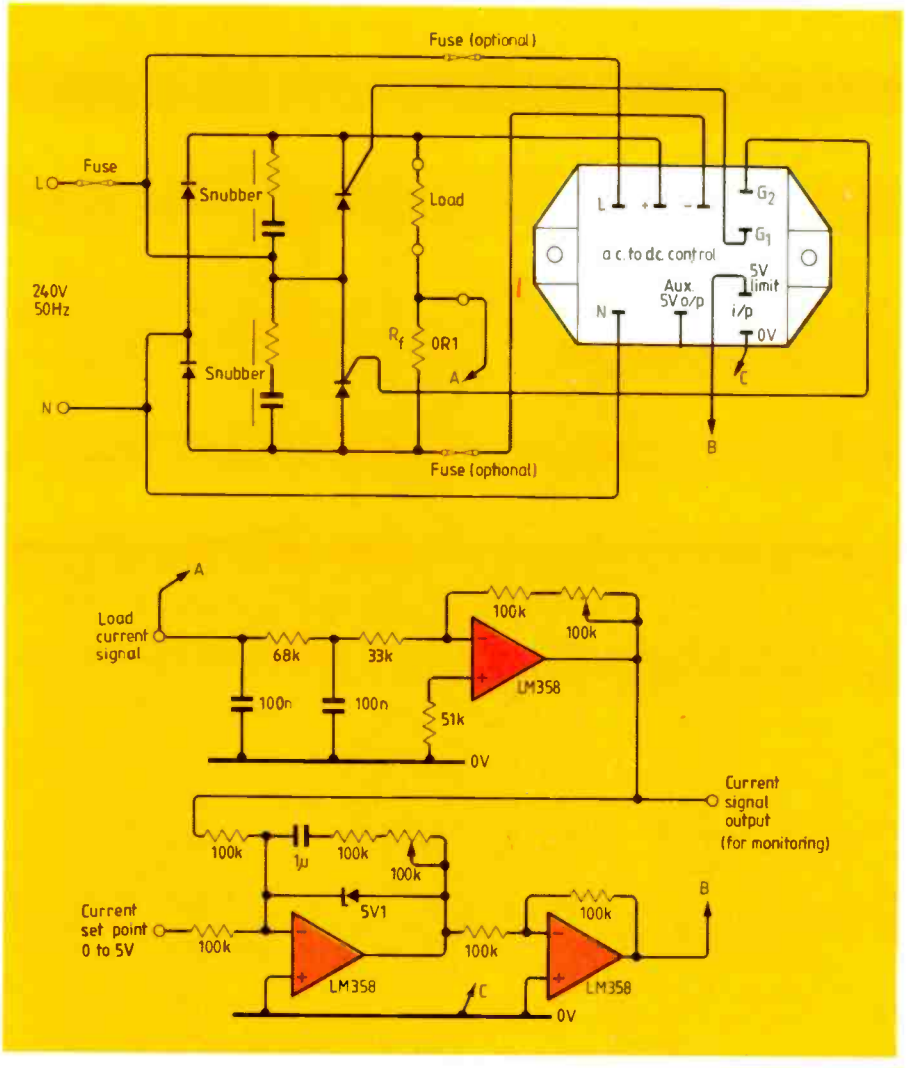
APPLICATIONS SUMMARY

10A DC CONTROLLER

Heavy-duty supply circuits arsimplified by phase-angle trigger modules as this constant-current closed-loop controller circuit from Semikron's M2DrHL/SP application note shows. The modules provide isolated trigger signals for thyristors.

In the low-power circuit, bottom the first amplifier senses current over the shunt resistor and feeds the second amplifier which is configured as a comparator and pi controller. Current feedback is compared with the input signal; difference between the two signals determines the trigger-module firing angle.

Used to drive a d.c. motor armature winding, the circuit becomes a constant-torque controller. The high-power section of this circuit is for a.c. to d.c. control but there is also a configuration for a.c. to d.c. control in the note. Remaining circuits are op-amp circuits for trigger-module driving, one for speed control of a d.c. motor and the other a power controller.



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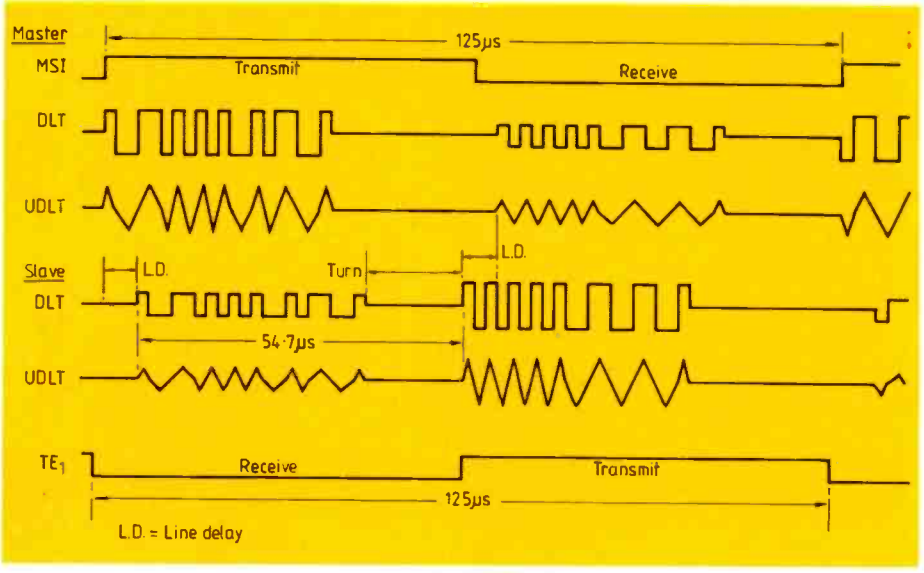
OFFICE COMMUNICATIONS

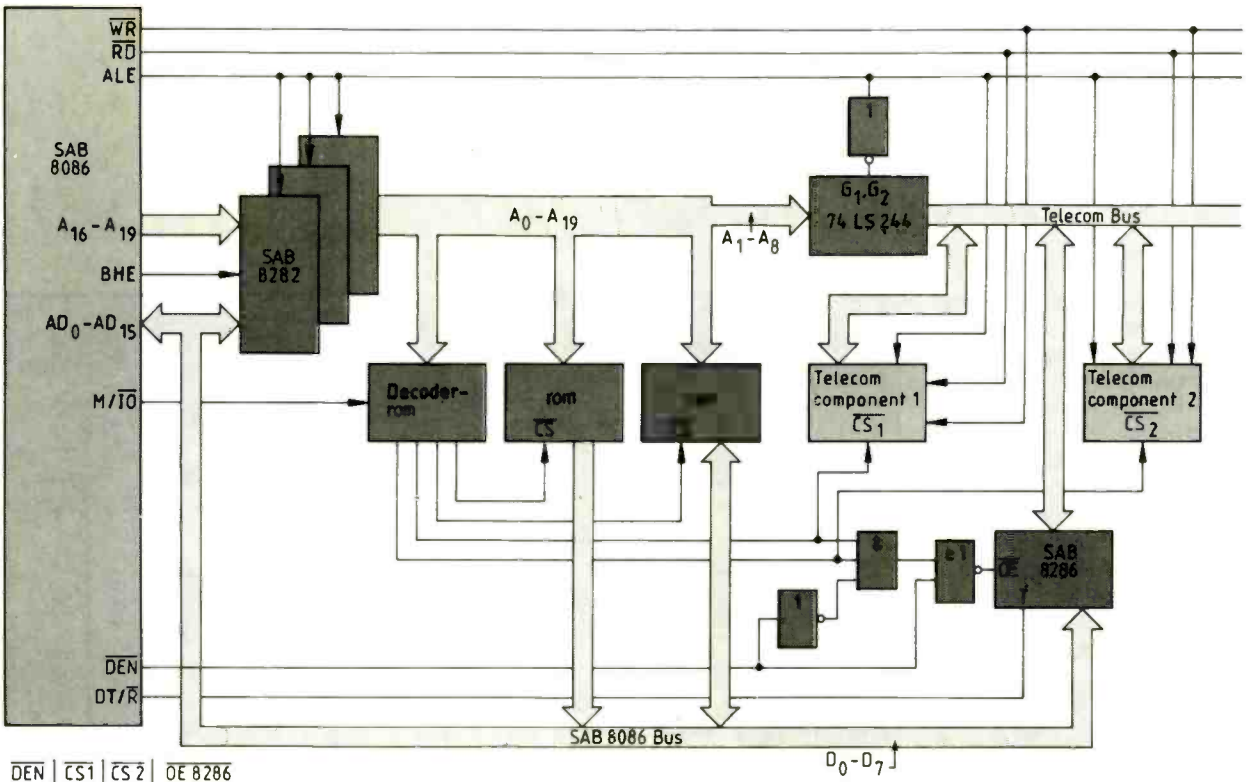
Leaflet AR239, called 'Implementing integrated office communications,' is produced by Motorola to give an overview of their various i.cs related to office communications.

The leaflet briefly covers some questions and answers relating to integrated services digital networking and places the various i.cs in block diagrams like the one shown to complement descriptions of what they do.

The universal digital-loop transceiver chip set, u.d.l.t., is an 80kbit/s synchronous transceiver capable of transmitting data over up to 2km using a twisted pair. Over shorter distances, of up to 1km, the similar d.l.t. set can be used.



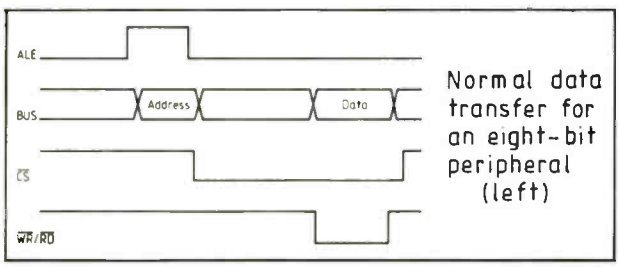
APPLICATIONS SUMMARY

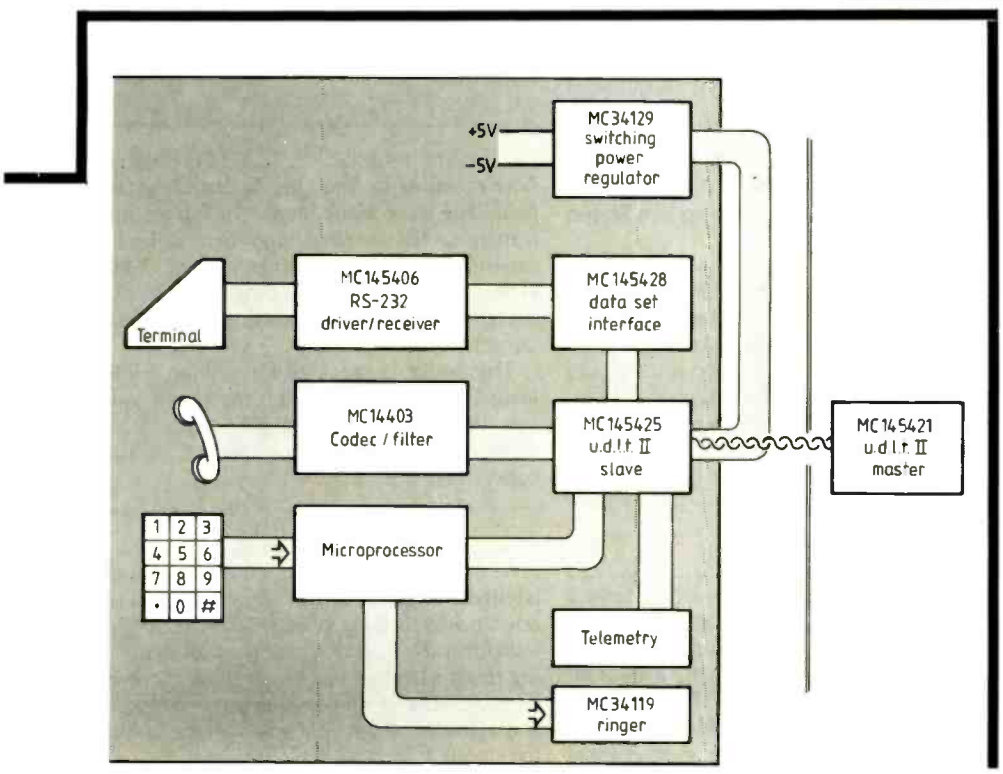


DEN	CS1	CS 2	OE 8286
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

$\overline{OE\ 8286} = \overline{DEN} + \overline{\overline{DEN} \cdot \overline{CS1} \cdot \overline{CS2}}$

To address a register use IN AL, ɔc.2
where ɔc is the telecommunication, i.e. register address





CONNECTING 8-BIT TELECOMMUNICATIONS I.CS TO 8081/80186 PROCESSORS

With 8086/186 processors, data and addresses are 16 bits and problems can arise when eight-bit PEB20 telecommunications i.cs are interfaced.

Processor i/o addressing consists of two 32Kbyte areas – one with odd bytes and one with even bits. Even-address i/o data bytes pass on D_{0-7} and odd bytes on D_{8-15} so components with even addresses connect to the lower part of the bus while components with odd addresses connect to the upper part.

Eight-bit peripherals using both even and odd addresses cause problems. Application note i.s.d.n.(3) from Siemens describes how to overcome this problem using hardware to form a separate bus for the telecommunications i.cs. To ensure that only even addresses are used, the real address is multiplied by two in software and only address lines A_{1-8} pass to the telecommunications bus.

Video frame store

To conclude this series, a colour palette module which extends the possibilities of the basic frame store with a variety of colour effects.

D. E. A. CLARKE

The basic frame store enables most forms of image capture and manipulation to be performed in the monochrome domain. However, for certain types of image the use of colour vastly improves the presentation of data.

Adding the colour palette is straightforward since it is connected in parallel with the existing boards; monochrome and pseudo-colour displays are then available for simultaneous display on separate monitors if desired. Provision has also been made for overlaying either an external video source or the (monochrome) stored image.

COLOUR PALETTE FEATURES

The colour palette is constructed on a separate board with connections to the pixel data bus, the control bus, the host data bus and the host control bus (the board $\overline{cs}$ signal is unique).

The unit is programmed by the host computer and offers the following features:

- 16 colours displayed from a palette of 4096.
- 1V pk-pk RGB video output with selectable sync polarity
- t.t.l. RGB video output with selectable video/sync polarity
- 1V pk-pk video input for programmable overlays
- selectable programming modes.

Several colour palette chips are now available including devices from Signetics, Inmos and Thomson. The Thomson device (EF9369) was chosen because of its low cost, availability, excellent performance and a feature not available on other devices which enables dynamic switching between video sources.

The EF9369 combines the functions of look-up table, microprocessor interface and digital-analogue conversion in a 28-pin d.i.l. plastic package working at pixel rates to 30MHz (Fig.26). The RGB d.a.cs have gamma-weighted outputs and high output impedance. The M-bit is a digital output which in this design is used to switch between video sources.

The chip has been designed for interfacing to a variety of microprocessor buses including multiplexed and non-multiplexed types. With this design it is best to drive the board via i/o ports.

In multiplexed mode the processor must output the look-up table address followed by data. In non-multiplexed mode, the processor need only set an internal index register

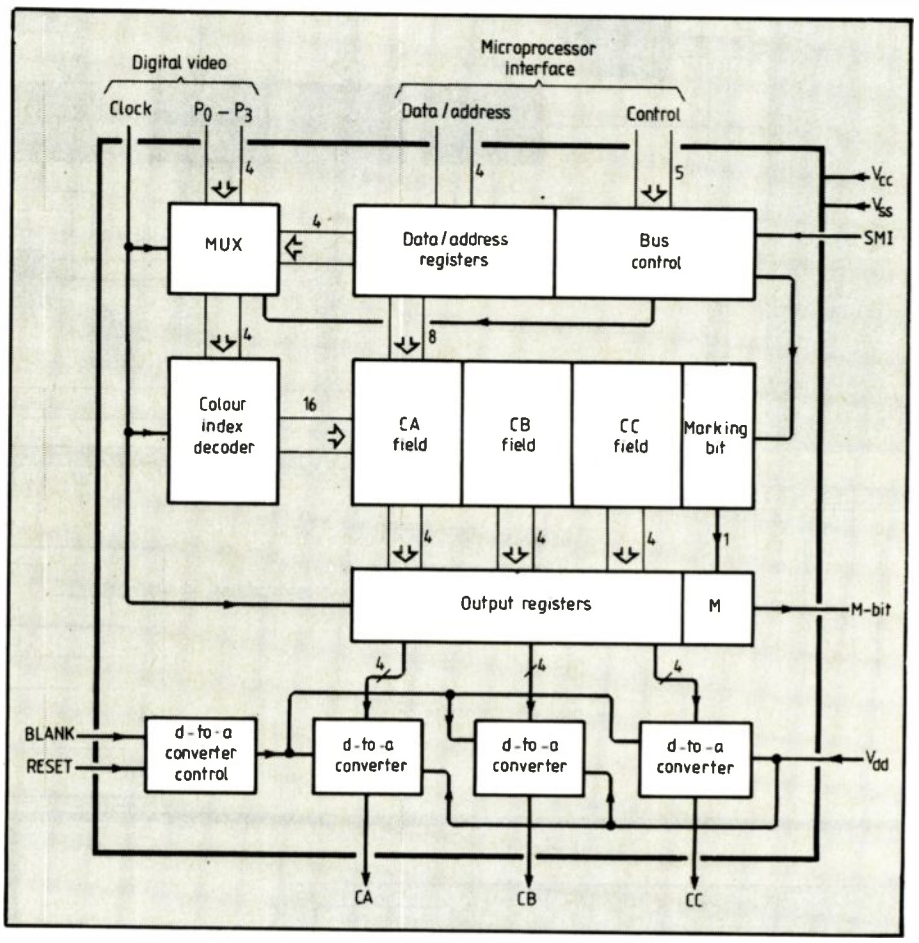


Fig.26. Thomson's EF9369 combines a microprocessor interface, d-to-a converters and a colour look-up table in a 28-pin package.

and the internal table address register will automatically be incremented after each data access. The method chosen will depend on your application but in non-multiplexed mode there is a dynamic timing condition which may not be met by slow processors (or i/o via non-compiled high-level languages). For maximum flexibility the board is configurable for operation by either technique.

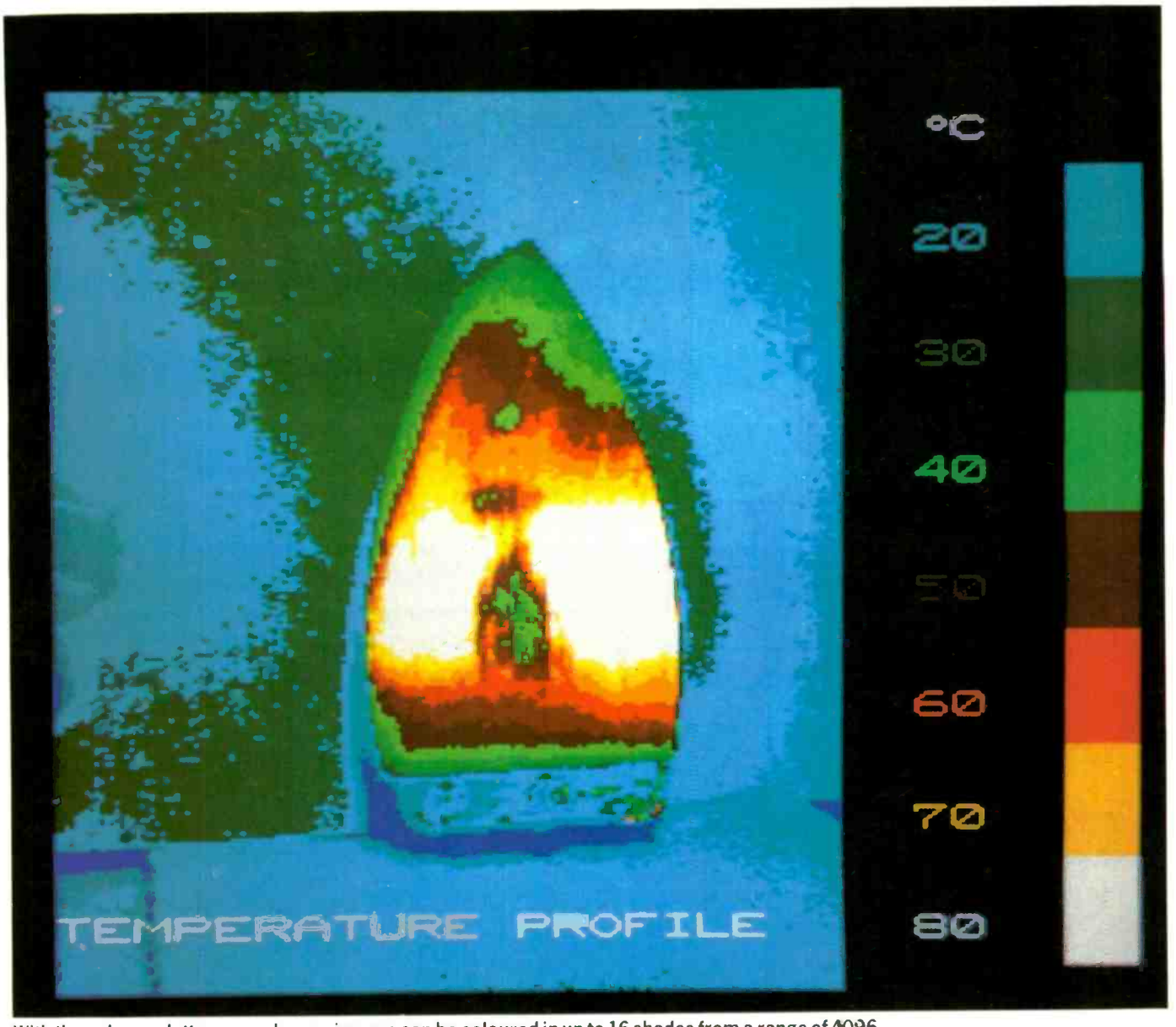
VIDEO BUFFER

Analogue outputs from the palette have a source impedance of about 300 ohms and they must therefore be buffered. The voltage swing is also inadequate, varying from 0.8V black level to 1.8V peak white. The functions of boost, buffer and high speed video switch are conveniently packaged in another inexpensive Thomson device, TEA5114 (Fig.27). Also provided by the chip is clamping for equalized video black levels. An interesting feature of the internal amplifiers is level-dependent gain which comes into effect for video output levels exceeding 1.5V pk-pk, thereby preventing overloading of monitor outputs.

The device is specified for driving 150Ω loads (i.e. 75Ω source/load) for supply voltage (V_{DD}) not exceeding 10V; this ties in nicely with the 10V supply needed for the converter board.

CIRCUIT DESCRIPTION

Pixel data from the bus is buffered and latched by IC_{305} (Fig.28) which introduces one clock cycle delay relative to the blanking waveform. This delay is necessary for matching the un-latched blanking on the converter board (the EF9369 latches both pixel data and blanking). Link 9 is available for use in the colour-keying mode.



With the colour palette, monochrome images can be coloured in up to 16 shades from a range of 4096.

Programming data/address lines are buffered by IC_{302}; the pull-up resistors prevent the c-mos buffers from entering their linear region of operation if the host is disconnected. These resistors should be omitted if the corresponding pull-ups on the memory board are fitted.

Programming signals ($\overline{ALE}$, $\overline{CS}$ and $\overline{WR}$); are buffered by IC_{301}. The chip select signal $\overline{CS}$ is unique while the others have dual function: it enables programming of the palette to take place without disrupting operation of the control board. Processing access mode is determined by Links 5,6 and 7.

Colour palette outputs from IC_{303} are a.c. coupled to the buffer-amplifier/switch IC_{304} where they are d.c.-restored and clamped to an internal reference voltage. The palette outputs are also boosted to t.t.l. levels by a high speed line receiver IC_{307} which switches at the mid-point of the output voltage range of the d.a.cs. as set by R_{307}, R_{308}. The palette should therefore be programmed for maximum output excursions for bright/dark levels when driving t.t.l. monitors.

The t.t.l. outputs could be derived from the outputs of IC_{304}; however, the d.c. output levels of IC_{304} shift significantly with temperature and would require a temperature-tracking comparator reference in place of the simpler $R_{307, 308}$.

The M-bit could also be used as an additional t.t.l. video output, but the overall delay in the d.a.c. boosting circuitry is about 60ns which would need to be compensated. In any case, few monitors have four t.t.l. video inputs.

Buffer IC_{306} also enables selection of output video polarity via link 4.

Composite sync from the converter board is buffered by Tr_{301} and limited to about 2V pk-pk; polarity is determined by Link 1. Sync at t.t.l. levels is also available via IC_{306} with polarity set by link 2.

External RGB video is a.c. coupled, d.c. restored and connected at IC_{304}; the black levels are then identical. The M-bit from the palette IC_{303} pin 7 then determines whether the palette or external source is selected, depending on image content and the programming of the lookup table. The video source gates are internally or-ed with an external signal FBI and the resulting signal FB appears as an output.

An optional switch S_{301} selects unterminated or terminated RGB positions 1,2 and combined RGB for monochrome sources in positions 3,4.

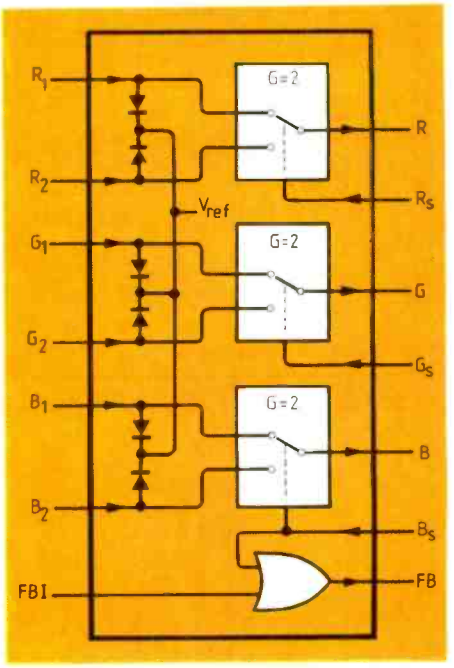


Fig.27. A second Thomson device, TEA5114, includes black-level clamps, high-speed video switches and output limiting.

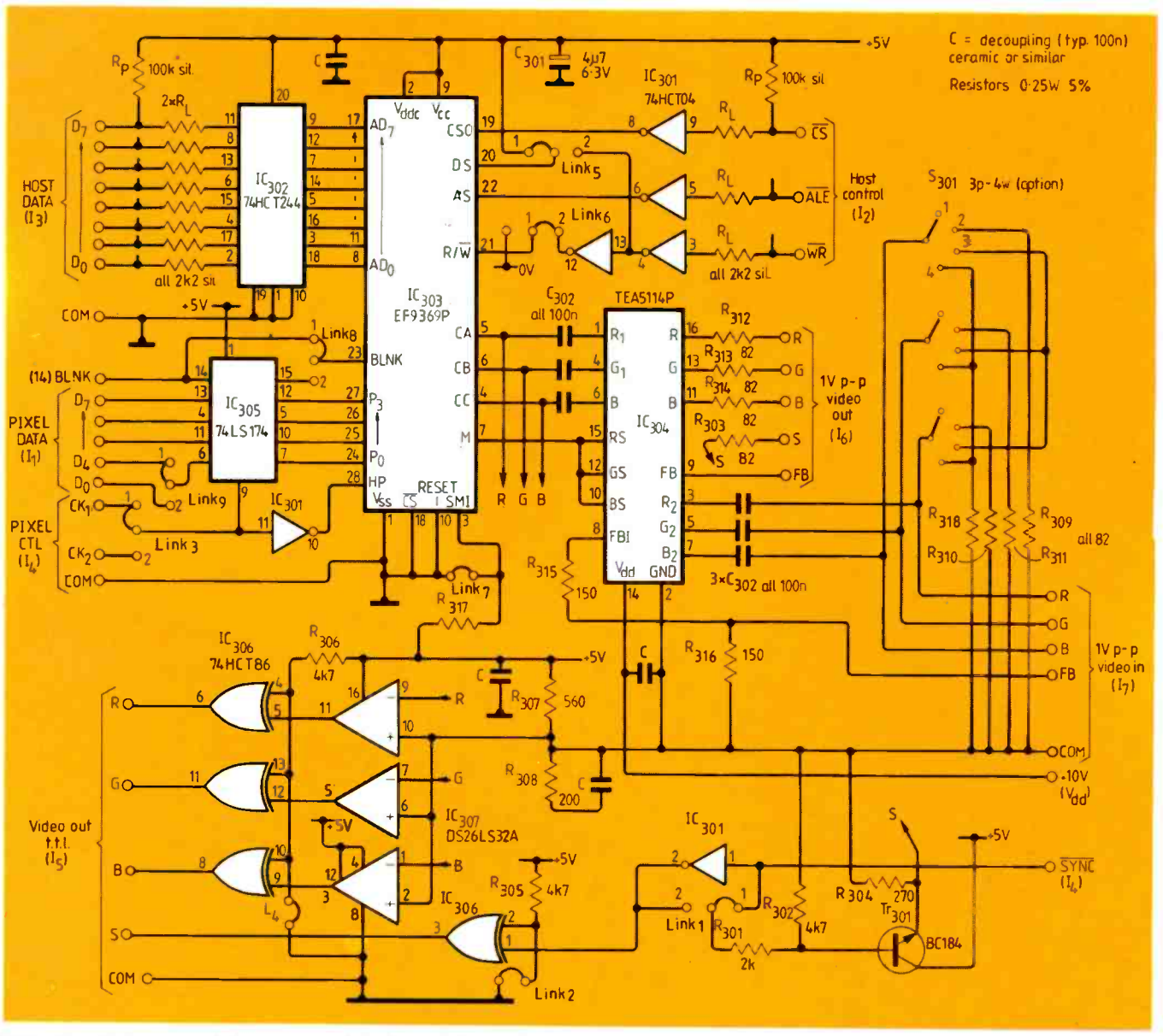


Fig.28. Circuit diagram of the colour palette board. This unit can be added to the basic frame store described in the November 1986-February 1987 issues.

PALETTE PROGRAMMING

The format of the EF9369 look-up table is shown in Fig.30; corresponding palette output voltages are listed in Table 1. After power-up, the device must be initialized by the host computer before a colour display can be obtained.

The EF9369 can be both read and written to by the host c.p.u. In this application, the read function is not implemented since it is a trivial procedure to keep track of device status in software, and so the interface circuitry can be simplified. Timing sequences for interfacing via parallel ports are detailed in Fig.31.

Table 1: EF9369 video output voltages

Binary input	Analogue output (V)
0000	0.80
0001	1.18
0010	1.28
0011	1.36
0100	1.42
0101	1.47
0110	1.52
0111	1.56
1000	1.60
1001	1.63
1010	1.66
1011	1.69
1100	1.72
1101	1.75
1110	1.78
1111	1.80

Multiplexed mode: referring to Fig.28, links 5,6 are in position 1 and link 7 is shorted. Address (binary 0-31) is output and latched on the positive edge of $\overline{ALE}$ while $\overline{CS}$ is low; $\overline{CS}$ is also latched at this time and the video outputs are blanked. Data is now output and $\overline{WR}$ pulsed low; the data is captured on the rising edge of $\overline{WR}$. In this way any combination of look-up table locations can be accessed, but the final operation must be to re-enable the video outputs by pulsing $\overline{ALE}$ with $\overline{CS}$ high.

Non-multiplexed mode: In this mode links 5,6 are in position 2 and link 7 is open. The R/$\overline{W}$ input (IC_{303} pin 21) is tied low to select write operation permanently (the condition is latched on the rising edge of $\overline{WR}$). The first operation is to output the initial lookup table address with the EF9369 address input high ($\overline{ALE}$ low); this will usually be address 0. Subsequent writes with $\overline{ALE}$ high access the EF9369 look-up tables and an internal index register auto-increments. This is a very fast way to initialize the entire chip and the video is blanked only while $\overline{CS}$ is low. However, maximum $\overline{WR}$ pulse width is 10μs because of dynamic structure on the chip.

DISPLAY MODES

The best combination of on-screen colours is usually obtained by trial and error. For many of the colour displays shown in this series the palette was initialized with the values in Table 2. Notice that the M bit is low.

External video will be selected when the M bit is set to logic 1 in a particular palette location and therefore a form of colour-keyed overlay of video sources on the sorted image can be obtained. In its simplest form, the look-up table is configured so that, say, locations 8 to 15 have the M bit set to logic 1. Image pixels should then be shifted right by one bit, and bit 7 set to 1 by software in those areas where it is required to select overlay video.

This technique has the advantage of reduced grey-level range on the monochrome

Table 2: typical look-up table values (all hexadecimal).

Location	MBGR
0	0000
1	0200
2	0600
3	0640
4	0F61
5	0010
6	0030
7	0060
8	00F0
9	0002
A	0003
B	000F
C	004F
D	009F
E	00FF
F	0FFF

video output. A better method is to switch video sources according to the state of pixel data bit 0 as selected by link 9.

Recall that when images are digitized, bit 0 is tied to logic 0. Software manipulation of this bit has minimal effect on the displayed monochrome image, but it can be used for overlay purposes. When overlay is not required, full 16-colour images can still be obtained by software copying bit 4 into bit 0.

VIDEO OVERLAYS

A powerful use for the overlay facility is the mixing of pseudo-colour images from the palette board and high quality stored images from the main converted board. This is achieved by connecting a cable from the main (monochrome) video output to any external RGB input with S_{301} in position 4.

Of course, the overlay video could originate from a live source such as a tv camera as long as the source is also connected to the main video input to synchronize the frame store.

When overlaying live video images with their stored equivalents, bear in mind that the frozen image has accumulated several clock cycles' delay with respect to the original, and will be noticeably displaced to the right.

Components and a set of printed circuit boards for this design are available from Ipswich Electronics Ltd, Hadleigh Road Industrial Estate. Ipswich IP2 0HB. tel: 0473-216056. Semiconductors can be obtained from Technomatic Ltd. 17 Burnley Road. London NW10 1ED.

IC101	74LS221	R101	1k	C101	1.5n
IC102	74LS74	R102	560	C102	6.8n
IC103	74LS74	R103	5.6k	C103	390p
IC104	74F74	R104	20k	C104	220p
IC105	74F161	R105	3.3k	C105	10-100p
IC106	74F161	R106	2.7k		variable
IC107	74HCT244	R107	3.3k	C106	see
IC108	74LS157	R108	2.7k		Table 1,
IC109	74LS04	R109	470		January
IC110	74LS02	R110	220	C107	4.7µ
IC111	74F00	R111	82k	C	10n
IC112	74LS132	R112	27k		
IC113	74F74	R	33	L1	6.8µH
IC114	74LS74	RP	100k		
IC115	74HCT393	P101	10k		
IC116	74HCT393	P102	20k		

Component values were accidentally omitted from the circuit diagram of the control board, Fig.13 in the December 1986 article. A list appears above.

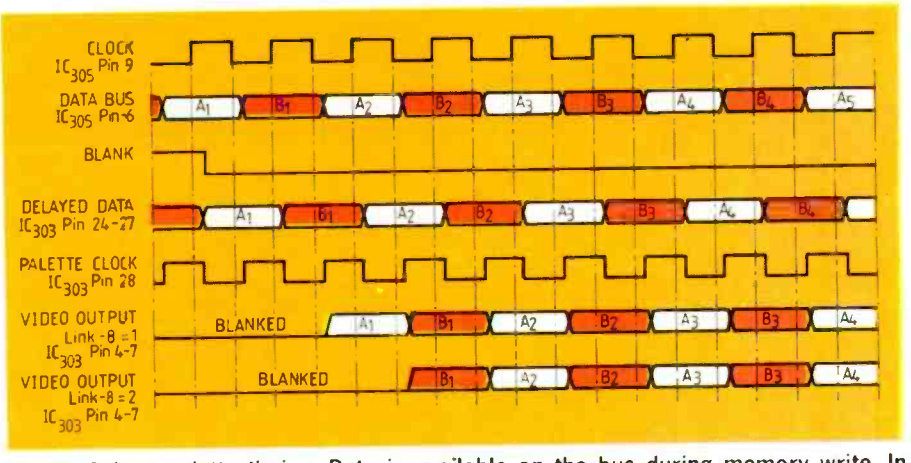

CLOCK IC305 Pin 9
DATA BUS IC305 Pin 6
BLANK
DELAYED DATA IC303 Pin 24-27
PALETTE CLOCK IC303 Pin 28
VIDEO OUTPUT Link-8 = 1 IC303 Pin 4-7
VIDEO OUTPUT Link-8 = 2 IC303 Pin 4-7
BLANKED
BLANKED

Fig.29. Colour palette timing. Data is available on the bus during memory write. In memory read mode, data appears after the positive clock edge.

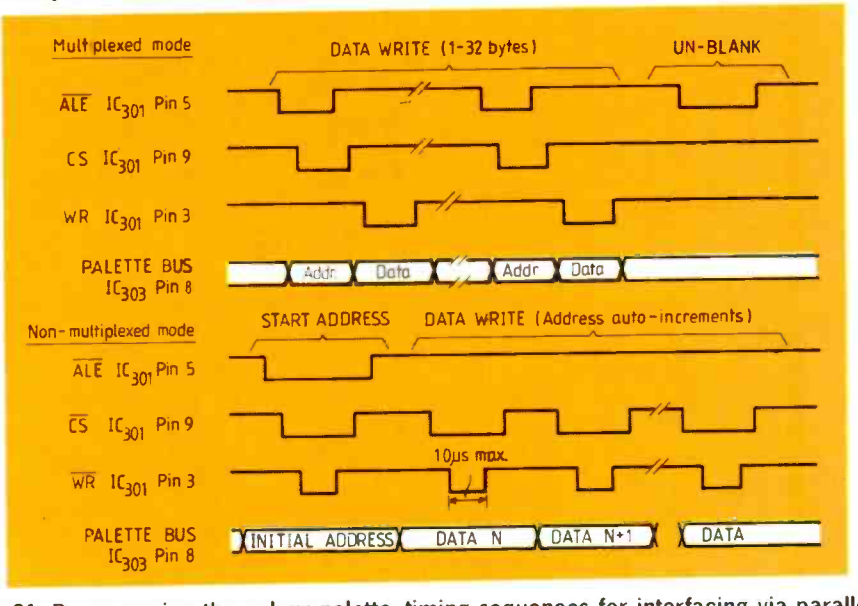
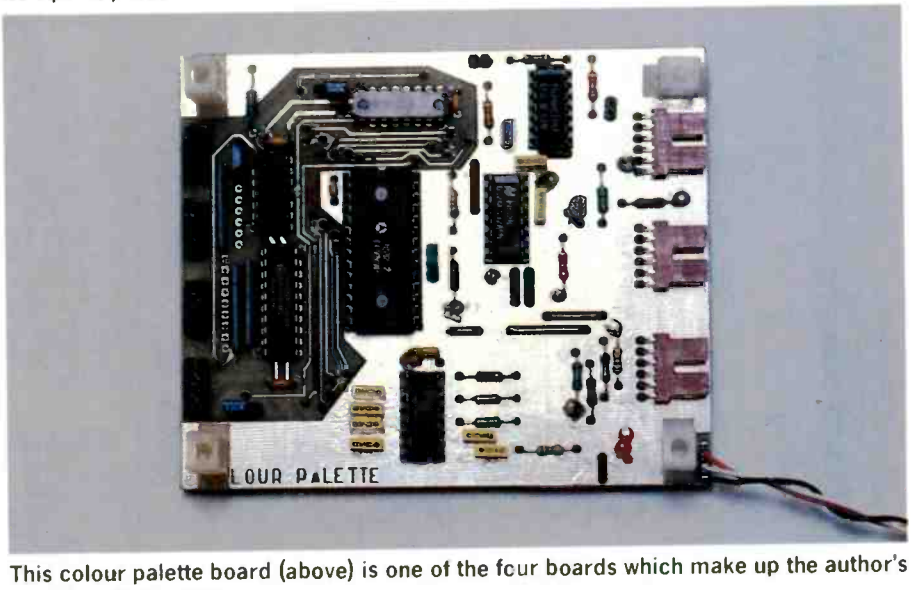


Fig.31. Programming the colour palette: timing sequences for interfacing via parallel computer ports.



This colour palette board (above) is one of the four boards which make up the author's prototype frame store.

Colour look-up table								Byte address						Register index
7	6	5	4	3	2	1	0	7 6	5 4	3 2	1 0			
CB3	CB2	CB1	CB0	CA3	CA2	CA1	CA0	X X	X 0	0 0	0 0			0
X	X	X	M	CC3	CC2	CC1	CC0	X X	X 0	0 0	0 1			
CB3	CB2	CB1	CB0	CA3	CA2	CA1	CA0	X X	X 0	0 0	1 0			1
X	X	X	M	CC3	CC2	CC1	CC0	X X	X 0	0 0	1 1			
CB3	CB2	CB1	CB0	CA3	CA2	CA1	CA0	X X	X 1	1 1	1 0			15
X	X	X	M	CC3	CC2	CC1	CC0	X X	X 1	1 1	1 1			

Fig.30. EF9369 look-up table format (abridged): X indicates 'don't care'.

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INTEGRATED CIRCUITS

AN124	2.50	MC1307P	1.00
AN214Q	2.50	MC1310P	1.50
AN240P	2.80	MC1327	1.70
AN612	2.15	MC1349P	1.20
AN7116	1.50	MC1351P	1.50
AN7140	3.50	MC1357	2.35
AN7145	3.50	MC1358	1.58
AN7150	2.95	MC1495	3.00
BA521	3.35	MC1496	1.25
CA1352E	1.75	MC145106P7	7.95
CA3086	0.46	MC1723	0.50
CA3123E	1.50	MC3357	2.75
HA1366W	3.50	ML231B	1.75
HA1377	3.50	MSM5807	6.75
HA1156W	1.50	PLL02A	5.75
HA1339A	2.95	SAA500A	3.50
HA1398	2.75	SAA1025	7.25
HA1551	2.95	SAS560S	1.75
LA1230	1.95	SAS570S	1.75
LA4031P	1.96	SAS580	2.85
LA4102	2.95	SL917B	7.50
LA4140	2.95	SL1310	1.80
LA4400	4.15	SL1327	1.10
LA4420	1.95	SL1327Q	1.10
LA4422	2.50	SN76003N	3.95
LA4430	2.50	SN76023N	3.95
LA4461	3.95	SN76033N	3.95
LC7120	3.25	SN76110N	0.89
LC7130	3.50	SN76115N	1.25
LC7131	5.50	SN76131N	1.30
LM324N	0.45	SN76226DN	2.95
LM380N8	1.50	SN76226N	1.05
LM380N14	1.75	SN76533N	1.65
LM383T	2.95	SN76544	2.65
LM3900N	3.50	SN76650N	1.15
M51513L	2.30	SN76660N	0.80
M51515L	2.95	STK014	7.95
M51521L	1.50	STK015	7.95
MB3712	2.00		

STK025	11.95	TBA540Q	1.35
STK043	15.50	TBA550Q	1.95
STK078	11.95	TBA560C	1.45
STK433	5.95	TBA460CQ	1.45
STK435	7.95	TBA570	1.00
STK437	7.95	TBA651R	2.50
STK439	7.95	TBA720A	2.45
STK461	11.50	TBA750Q	2.65
TA7061AP	3.95	TBA800	0.89
TA7108P	1.50	TBA810AS	1.65
TA7120P	1.65	TBA810P	1.65
TA7130P	1.50	TBA820M	0.75
TA7176AP	2.95	TBA820Q	1.45
TA7203	2.95	TBA890	2.50
TA7204P	2.15	TBA920	1.65
TA7205AP	1.15	TBA950/2X	2.35
TA7222AP	1.80	TBA990	1.49
TA7227P	4.25	TCA270	1.50
TA7310P	1.80	TCA270SQ	1.50
TA7313AP	2.95	TCA650	3.50
TA7321P	2.25	TCA800	6.95
TA7146P	2.50	TCA940	1.65
TA7609P	3.95	TDA440	3.50
TA7611AP	2.95	TDA1001	2.95
TAA310A	3.50	TDA1002A	2.95
TAA320A	3.50	TDA1006A	2.50
TAA350A	3.50	TDA1035	2.50
TAA570	1.95	TDA1037	1.95
TAA661B	1.95	TDA1170	1.95
TBA700	1.70	TDA1190	2.15
TBA120AS/B/C/		TDA1270Q	5.50
SA/SB/T/U	1.00	TDA1327	1.70
TBA395	1.50	TDA2002	1.95
TBA396	0.75	TDA2003	1.95
TBA440N	2.55	TDA2004	2.95
TBA480Q	1.25	TDA2005	2.95
TBA510	2.50	TDA2006	1.95
TBA510Q	2.50	TDA2020	2.95
TBA520	1.10	TDA2030	2.80
TBA520Q	1.10	TDA2522	1.95
TBA530	1.10	TDA2523	2.95
TBA530Q	1.10	TDA2524	1.95
TBA540	1.25	TDA2530	1.95

TDA2532	1.95		
TDA2540	1.95		
TDA2545A	2.15		
TDA2451	2.15		
TDA2560	2.15		
TDA2571A	4.50		
TDA2581	2.95		
TDA2593	2.95		
TDA2600	6.50		
TDA2610	3.50		
TDA2611A	1.95		
TDA2640	3.50		
TDA2680A	2.75		
TDA2690	2.45		
TDA3310	2.95		
TDA3560	5.50		
UPC566H	2.95		
UPC575C2	2.75		
UPC1025H	1.95		
UPC1028H	1.95		
UPC1032H	1.50		
UPC1156H	2.75		
UPC1158H	0.75		
UPC1157C2	1.95		
UPC1181H	1.25		
UPC1182H	2.95		
UPC1185H	3.95		
UPC1191V	1.50		
UPC1350C	2.95		
UPC1353C	2.45		
UPC1365C	3.95		
UPC2002H	1.95		
555	0.36		
556	0.60		
723	0.50		
741	0.35		
747	0.50		
748	0.35		
7805	0.65		
7808	0.60		
7815	0.65		

SEMICONDUCTORS

AAY12	0.25	BC182	0.10
AC125	0.20	BC182LB	0.10
AC126	0.45	BC183	0.10
AC127	0.20	BC183L	0.09
AC128	0.28	BC184LB	0.09
AC128K	0.32	BC204	0.10
AC141	0.28	BC207B	0.13
AC142K	0.45	BC208B	0.13
AC176	0.22	BC212	0.09
AC176K	0.31	BC212L	0.09
AC187	0.25	BC212LA	0.09
AC187K	0.28	BC213	0.09
AC188	0.25	BC213L	0.09
AC188K	0.37	BC214	0.09
ACY17	1.15	BC214C	0.09
AD142	0.79	BC214L	0.09
AD143	0.82	BC237B	0.09
AD149	0.70	BC238	0.09
AD161	0.39	BC239	0.12
AD162	0.39	BC251A	0.12
AD161/2	0.90	BC252A	0.15
AF106	0.50	BC258	0.25
AF114	1.95	BC258A	0.39
AF115	2.95	BC2284	0.30
AF116	2.95	BC300	0.30
AF117	2.95	BC301	0.30
AF121	0.60	BC303	0.26
AF124	0.65	BC307B	0.09
AF125	0.35	BC327	0.10
AF126	0.32	BC328	0.10
AF127	0.65	BC337	0.10
AF139	0.40	BC338	0.09
AF150	0.60	BC347A	0.13
AF178	1.95	BC461	0.35
AF239	0.42	BC478	0.20
AFZ12	3.75	BC527	0.20
ASY27	0.85	BC547	0.10
AU106	4.50	BC548	0.10
AU107	3.50	BC549A	0.10
AU110	3.50	BC550	0.14
AU113	4.50	BC557	0.08
BC107A	0.11	BC557B	0.80
BC107B	0.11	BC558	0.10
BC108	0.10	BCY33A	1.60
BC108A	0.11	BD115	0.30
BC108B	0.12	BD124P	0.59
BC109	0.10	BD131	0.42
BC109B	0.12	BD132	0.42
BC109C	0.12	BD133	0.40
BC114	0.11	BD135	0.30
BC116A	0.15	BD136	0.30
BC117	0.19	BD137	0.32
BC119	0.24	BD138	0.30
BC125	0.25	BD139	0.32
BC139	0.20	BD140	0.30
BC140	0.31	BD144	1.10
BC141	0.25	BD150C	0.29
BC142	0.21	BD159	0.65
BC143	0.24	BD160	1.50
BC147A	0.12	BD166	0.95
BC147B	0.12	BD179	0.72
BC148A	0.09	BD181	0.45
BC148B	0.09	BD182	0.70
BC149	0.09	BD183	0.70
BC157	0.12	BD201	0.83
BC158	0.09	BD202	0.65
BC159	0.09	BD203	0.78
BC1174	0.09	BD204	0.70
BC174A	0.09	BD222	0.46
BC177	0.15	BD223	0.59
BC178	0.15	BD225	0.48

BD232	0.35	BFR91	1.75
BD233	0.35	BFT42	0.35
BD234	0.35	BFT43	0.35
BD236	0.49	BFW92	0.85
BD237	0.40	BFX29	0.30
BD238	0.40	BFX84	0.26
BD242	0.65	BFX85	0.32
BD246	0.75	BFX86	0.30
BD376	0.32	BFX88	0.25
BD410	0.65	BFY50	0.21
BD434	0.65	BFY51	0.21
BD437	0.75	BFY52	0.25
BD438	0.75	BFY90	0.77
BD520	0.65	BLY48	1.75
BD538	0.65	BRY39	0.45
BD597	0.95	BR100	0.26
BD589	0.45	BR101	0.49
BD701	1.25	BR103	0.55
BD702	1.25	BRC4443	1.15
BD707	0.90	BT100A/0	20.85
BDX32	1.50	BT106	1.49
BF115	0.35	BT116	1.20
BF119	0.35	BT119	3.15
BF119	0.65	BT120	1.65
BF127	0.39	BU105	1.95
BF154	0.20	BU108	1.69
BF158	0.22	BU124	1.25
BF160	0.27	BU125	1.25
BF167	0.27	BU126	1.60
BF173	0.22	BU204	1.55
BF177	0.38	BU205	1.30
BF178	0.26	BU208	1.39
BF179	0.34	BU208A	1.52
BF180	0.29	BU208D	1.85
BF181	0.29	BU3276	1.20
BF182	0.29	BU407	1.24
BF183	0.29	BU500	2.25
BF184	0.28	BU508A	1.95
BF185	0.28	BU526	1.90
BF194	0.11	BU80Y	2.25
BF195	0.11	BUY69B	1.70
BF196	0.11	MJ3000	1.98
BF197	0.11	MJE340	0.40
BF198	0.16	MJE520	0.48
BF199	0.14	MPSA13	0.29
BF200	0.40	MPSA92	0.30
BF241	0.15	MRF237	4.95
BF245	0.30	MRF450A	13.95
BF257	0.28	MRF453	17.50
BF258	0.28	MRF454	26.50
BF259	0.28	MRF455	17.50
BF271	0.26	MRF475	2.95
BF273	0.18	MRF477	13.50
BF336	0.34	MRF838	13.95
BF337	0.29	OC16W	2.50
BF338	0.32	OC23	1.50
BF355	0.37	OC28	1.50
BF362	0.38	OC29	2.25
BF363	0.65	OC32	2.25
BF371	0.25	OC42	0.75
BF394	0.19	OC44	0.75
BF422	0.32	OC45	0.55
BF457	0.32	OC70	0.45
BF458	0.36	OC71	0.55
BF459	0.36	OC72	0.85
BF467	0.68	OC75	0.95
BF494	0.45	OC81	0.50
BF495	0.45	OC171	3.50
BF595	0.23	R2008B	1.45
BF597	0.25	R2010B	1.45
BFR39	0.23	R2322	0.58
BFR81	0.25	R2323	0.66
BFR88	0.30	R2540	2.48
BFR90	1.50	HCA16334	0.90

RCA16335	0.80		
SKE5F	1.45		
TIP29	0.40		
TIP29C	0.42		
TIP30C	0.43		
TIP31C	0.55		
TIP32C	0.42		
TIP33C	0.95		
TIP34B	0.95		
TIP41A	0.45		
TIP41C	0.45		
TIP42C	0.47		
TIP47	0.65		
TIP120	0.60		
TIP125	0.65		
TIP142	1.75		
TIP161	2.95		
TIP2955	0.85		
TIP3055	0.55		
TIS91	0.20		
TV106/2	1.50		
2N1308	1.35		
2N2219	0.28		
2N2222	0.29		
2N2905	0.40		
2N3053	0.40		
2N3054	0.59		
2N3055	0.52		
2N3702	0.12		
2N3703	0.12		
2N3704	0.12		
2N3705	0.20		
2N3706	0.12		
2N3708	0.12		
2N3733	9.50		
2N3733	2.75		
2N3792	1.35		
2N4427	1.95		
2N4444	1.15		
2N5294	0.42		
2N5296	0.48		
2N5298	0.60		
2N5485	0.45		
2N5496	0.95		
2SA715	0.60		
2SC495	0.80		
2SC496	0.80		
2SC1096	0.80		
2SC1106	2.50		
2SC1172Y	2.20		
2SC1173	1.15		
2SC1306	1.40		
2SC1307	2.95		
2SC1364	0.50		
2SC1449	0.80		
2SC1678	1.25		
2SC1909	1.25		
2SC1945	3.75		
2SC1954	0.95		
2SC1957	0.80		
2SC1969	1.95		
2SC2028	1.15		
2SC2029	1.95		
2SC2078	1.45		
2SC2091	0.85		
2SC2098	2.95		
2SC2166	1.95		
2SC2314	0.80		
2SC2371	0.36		
2SD234	0.50		
3SK88	0.95		

DIODES

AA119	0.80	BY208-800	0.33
BA115	0.13	BY210-800	0.33
BA145	0.16	BY223	0.90
BA148	0.17	BY298-400	0.22
BA154	0.06	BY299-800	0.22
BA156	0.15	BYX10	0.20
BA157	0.30	BYX36-150R	0.20
BAX13	0.04	BYX38-600R	0.60
BAX16	0.12	BYX55-600	0.30
BB105B	0.30	BYX71-600	1.75
BT151	0.79	BZY95C30	0.35
BY126	0.10	CS4B	8.00
BY127	0.11	CS107	16.50
BY133	0.15	OA47	0.15
BY164	0.45	OA90	0.10
BY176	1.20	OA91	0.15
BY179	0.63	OA95	0.10
BT182	0.55	OA202	0.20
BY184	0.35	IN21DR	5.00
BT199	0.40	IN23B	5.00
BY206	0.14	IN23C	5.00
		IN23ER	5.00
		IN23WE	5.00

IN4001	0.04
IN4003	0.04
IN4004	0.05
IN4005	0.05
IN4007	0.06
IN4148	0.02
IN4448	0.10
IN5401	0.12
IN5402	0.14
IN5403	0.12
IN5406	0.13
IN5407	0.16
IN5408	0.16
ITT44	0.04
ITT923	0.15
ITT2002	0.20

ZENER DIODES

BZX61 Series	0.15
BZY88 Series	0.10

LINE OUTPUT TRANSFORMERS

DECCA 100	7.95
DECCA 1700 MONO	9.95
DECCA 1730	8.95
DECCA 2230	8.25
GEC 2040	8.95
GRUNDIG 1500	15.45
GRINDIG 510-6010, 2222, 5011-6011	13.45
ITT CVC20	8.20
ITT CVC30	8.25
PHILIPS G8	8.50
PHILIPS G9	8.99
PHILIPS G11	13.99
PYE 725	10.95
RBM T20A	12.40
TANDBERG 90°	11.15
TELEFUNKEN 711A	11.15
THORN 1590	9.50
THORN 8000	23.50
THORN 9000	9.95
THORN 9800	22.40

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CME822W	19.00	DH3-91	55.00	M38-103GR	65.00
CME822GH	25.00	DH7 91	45.00	M38-120W	65.00
CME1428GH	45.00	DP7 5	35.00	M38-120WA	65.00
CME1428W	39.00	DP7 6	35.00	M38-121GHR	65.00
CME1523W	39.00	DN13.78	35.00	M38-121LA	65.00
CME1431GH	39.00	F16-101GM	75.00	M38-122GW	65.00
CME1431W	39.00	F16-101LD	75.00	M38-140LA	65.00
CME202GH	45.00	F21-130GR	75.00	M38-142LA	65.00
CME2024W	45.00	F21-130LC	75.00	M38-341P31	65.00
CME2325W	45.00	F31-10GM	75.00	M38-344P39	65.00
CME3218W	45.00	F31-10GR	75.00	M40-120W	59.00
CME3132GH	45.00	F31-10LC	75.00	M43-12LG/01	65.00
CME3155W	45.00	F31-10LD	75.00	M44-120LC	65.00
CRE1400	25.00	F31-12LD	75.00	M50-120GH	65.00
CV1450	35.00	F31-13GR	75.00	M50-120GR	65.00
CV1526	19.00	F31-13LD	75.00	M50-120GV	65.00
CV2185	15.00	F31-13LG	75.00	M50-120LC	65.00
CV2191	19.00	F41-123LC	185.00	SE3A/P31	40.00
CV2193	15.00	F41-141LG	185.00	SE4/D/P7	45.00
CV5119	85.00	F41-142LC	185.00	SE42BP31AL	55.00
CV5320	85.00	M7-120W	19.00	SE42BP31	55.00
CVX389	55.00	M7-120GH	19.00	SE5FP31	55.00
D9-110GH	39.50	M14-100GM	45.00	T948H	65.00
D10-210GH	45.00	M14-100LC	45.00	V50/4LD	59.00
D10-210GH68B	65.00	M17-151GVR	175.00	V6048CAL	59.00
D10-230GH	35.00	M17-151GR	175.00	V6048J	49.00
D10-230GM	35.00	M19-100W	45.00	V6064BP31	55.00
D10293-GY/90	55.00	M19-103W	55.00	V6069GH	55.00
D13-30GH	49.50	M23-110GH	55.00	V6070P31	65.00
D13-51GL/26	85.00	M23-111LD	55.00	V7030	59.00
D13-51GM/26	85.00	M23-112GM	55.00	V7031GH	59.00
D13-45GH/01	55.00	M23-112GV	55.00	V7031/67A	59.00
D13-610GH	59.00	M23-112GW	55.00	V7035A	49.00
D13-511GH	59.00	M23-112KA	55.00	V7037GH	45.00
D13-611GM	59.00	M24-120GM	59.00	V8004GR	65.00
D13-630GH	59.00	M24-120LC	59.00	V8006GH	65.00
D14-150GH	75.00	M24-120WAR	59.00	V8010A	65.00
D14-150GM	75.00	M24-121GH	55.00	3BPI	11.50
D14-162GH/B4	59.00	M28-12GH	55.00	3DPI	11.50
D14-172GR	55.00	M28-13LC	49.00	3H/OBM	55.00
D14-172GV	55.00	M28-13LG	49.00	3WPI	18.50
D14-173GH	55.00	M28-13GR	49.00	4EPI	30.00
D14-173GM	53.00	M28-131GR	55.00	5BHP1	30.00
D14-173GR	55.00	M28-133GH	55.00	5BHP1FF	30.00
D14-181GH/98	65.00	M31-101GH	55.00	5BHP31	30.00
D14-181GJ	55.00	M31-182GR	55.00	5CPI	10.00
D14-181GM	53.00	M31-182GV	53.00	5T01A	15.00
D14-181GM50	59.00	M31-184W	65.00	6EP7/S	39.00
D14-182GH	59.00	M31-184GH	85.00	13BPI	13.50
D14-200BE	89.00	M31-184P31	65.00	13BPA4	17.50
D14-200GA/50	85.00	M31-185W	69.00	17DWP4	25.00
D14-200GM	75.00	M31-190GH	55.00	32J/1085	69.00
D14-210GH	75.00	M31-190GR	55.00	1273	39.00
D14-270GH/50	75.00	M31-190LA	55.00	1564	45.00
D14-310W	110.00	M31-191GV	55.00	1844	80.00
D14-320GH/82	85.00	M31-220W	59.00	9442E1	75.00
D14-340GH/KM	45.00	M31-270GY	65.00	95447GM	75.00
D14-340KA	45.00	M31-271P31	65.00	95449GM	75.00
D16-100GH	65.00	M31-271GW	65.00	7709631	78.50
D16-100GH-65	69.00	M31-271W	65.00		
D16-100GH/67	65.00				
D16-100GH/79	69.00				
D16-100GH/97	65.00				
D18-160GH	69.00				
D21-10GH	65.00				
DG7-5	55.00				
DB7.6	35.00				
DB7 36	55.00				
DG7 32	45.00				
DG13 2	45.00				

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7 watt	R47-22K	0.20	
11 watt	1R-15K	0.25	
17 watt	1R-15K	0.30	

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Ferguson 3V16	4.50	Sanyo VTC 9300	3.75
JVCH HR 3330/3600	4.50	Sanyo VTC 9300P	3.90
JVC HR 3360/3660	4.50	Sharp VC 6300	3.75
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Panasonic NV 2000B	3.75	Sharp VC 8300	3.75
Panasonic 3000B	3.75	Sharp VC 9300	3.75
Panasonic NV7000	3.50	Sony SL 3000B	3.75
Panasonic NV8600B/ 8610B/VOII	3.75	Sony SL 8000/8080	4.50
		Sony SL C7/J7	4.00
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SANYO ORIGINAL VIDEO PARTS

SANYO ORIGINAL PART NUMBER	MODEL	DESCRIPTION	
4-529-10800B	VTC5150	Reel Motor 3.6W	9.95
4-527T-23501	VARIOUS	Motor Assy	9.75
4-527V-51000	5150	Capston Motor	29.95
143-0-4904-00900	FVHP615	Gear Idler Assy	5.95
143-2-457T-05900	VTC9455	Stopper Reel Base	0.55
143-0-545T-01701	VARIOUS	Pinch Roller Assy.	8.95
143-0-545T-01700	VARIOUS	Pinch Roller Assy. Idler Assy.	1.95
143-0-561T-VTC9300			0.95
143-0-661T-03800	VARIOUS	Loading Roller	0.95
143-0-662T-01201	VTC5150	Reel Drive Pulley	8.50
143-0-9974-00100	FVHP615	Mod Kit IC BA6304A	1.95

Video Head Cleaning Tape (VHS Automatic wet/dry)	6.50
Video Head Aerosol Cleaner	0.85
Video Copying Lead and Connector Kit 'Z/V'	7.95

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S-2P Colour Bars 30 min	49.50
S-3P Stairsteps 30 min	49.50

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ITT CVC30	6.35
PHILIPS G8.550	6.96
RANK T20A	6.91
THORN 3000/3500	7.57
THORN 8500	8.00
THORN 9000	8.00
UNIVERSAL TRIPLER	5.45

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DECCA 30(400-400/350V)	2.85
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DECCA 1700 (200-200-400-350V)	3.55
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PHILIPS G (600/300V)	2.25
PHILIPS G9 (2200 +63V)	1.19
PHILIPS G11 (470/250V)	2.35

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ELC1043/06 MULLARD	8.65
U321	8.25
U322	8.25
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VA1056S	0.23
VA1104	0.95
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DECCA, ITT, CVC20 6WAY	7.95
ITT VCV5-7-WAY	10.19
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100MA	8p each
200MA - 5 AMP	5p each

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100MA-800MA	15p each
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23	35.00
24	39.00
25	22.00
29	32.00
11	1.00
12	0.60
16	0.65
17	1.00
53	1.20
77	0.90
79	0.56
149	2.00
91	0.90
92	1.20
35	2.50
53	1.00
70	2.50
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92	0.95
93	1.10
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S10	13.50
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62CC	9.00
80F	8.50
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1148	1.00
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B34	1.50
B41	3.95
B91	0.85
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BC81	1.50
BC90	0.90
EBC91	0.90
EBF80	0.95
EBF83	0.95
EBF85	0.95
EBF89	0.70
EBF93	0.95
EBL1	2.50
EBL21	2.00
EC52	0.75
EC70	1.75
EC81	7.95
EC86	1.00
EC88	1.00
EC90	1.10
EC91	5.50
EC92	1.95
EC93	1.50
EC95	7.00
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ECC32	3.50
ECC33	3.50
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ECC81	1.15
ECC81 Special Quality	2.25
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ECC85	0.75
ECC86	2.75
ECC88	0.95
ECC91	2.00
ECC180	0.72
ECC189	1.95
ECC801S	3.50
ECC803S	4.95
ECC804	0.60
ECC2000	12.00
ECF80	1.15
ECF82	1.15
ECF86	1.70
ECF200	1.85
ECF202	1.85
ECF801	0.85
ECF805	2.50
ECF806	10.25
ECH3	2.50
ECH4	3.00
ECH35	3.50
ECH42	1.50
ECH81	1.00
ECH83	1.00
ECH84	1.00
ECH2000	1.50
ECL80	0.60
ECL82	0.79
ECL83	2.50
ECL84	0.74
ECL85	0.69
ECL86	0.95
ECL805	0.69
EF37A	2.50
EF39	1.50
EF40	4.50
EF41	3.50
EF42	3.50
EF50	2.50
EF55	4.95
EF70	1.20
EF71	1.50
EF72	1.20
EF73	1.00
EF80	0.55
EF83	3.95
EF85	0.50
EF86	2.25
EF86 Mullard	4.50
EF89	1.50
EF91	1.95
EF92	2.15
EF93	0.95
EF94	0.95
EF95	1.95
EF97	0.90
EF98	0.90
EF183	0.65
EF184	0.65
EF730	1.80
EF731	3.50
EF732	3.50
EF800	11.00
EF804S	19.50
EF805S	19.50
EF806S	14.50
EF812	0.65
EFL200	1.50
EH90	0.72
EK90	0.95
EL32	0.95
EL33	5.00
EL34	2.25
EL34 Mullard/ Philips	4.50
EL36	1.95
EL37	9.00
EL38	6.50
EL41	3.50
EL42	2.00
EL81	6.95
EL83	5.95
EL84 Brimar	0.95
EL84 Mullard	2.95
EL85	4.50
EL86	1.25
EL90	1.75
EL91	6.00
EL95	1.75
EL153	12.15
EL183E	3.50
EL18P	3.50
EL360	6.75
EL500	1.40
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EL509	5.25
EL519	6.95
EL802	3.65
EL821	8.50
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EM1	9.00
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EM80	0.70
EM81	0.70
EM84	1.65
EM85	3.95
EM87	2.50
EN32	15.00
EN91	1.95
EN92	4.50
EX35	235.00
EY51	0.80
EY81	2.35
EY83	1.50
EY84	5.95
EY86/87	0.50
EY88	0.55
EY91	5.50
EY802	0.70
EZ40	2.75
EZ41	2.75
EZ80	0.75
EZ81	0.75
EZ90	1.50
F606	2.95
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G55/1K	9.00
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GC10B	17.50
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GS10C	18.50
GS10H	12.00
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GT1CS/S	13.00
GTN175M	8.00
GTR150W	1.00
GU20	35.00
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GXU1	13.50
GXU3	24.00
GXU50SS	14.50
GY501	1.50
GY802	1.50
GZ30	1.50
GZ31	1.50
GZ32	1.50
GZ33	4.50
GZ34	2.15
GZ37	4.50
HAA91	1.00
HABC80	0.90
HBC90	0.75
HBC91	0.80
HF93	0.75
HF94	1.50
HK90	1.05
HL2K	4.95
HL23DD	4.00
HL41	3.50
HL42DD	3.50
HL90	0.70
HL92	1.50
HL13DD	3.50
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HVR2	3.00
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KT36	2.00
KT44	4.00
KT45	4.00
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KT66 GEC	17.50
KT66 Sp Yellow Spot	19.50
KT67	9.00
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KT88 USA	10.95
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KTW62	2.50
KTW63	2.00
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M8079	6.00
M8082	7.50
M8083	3.25
M8091	7.50
M8096	3.00
M8098	5.50
M8099	5.00
M8100	5.50
M8136	7.00
M8137	7.95
M8161	6.50
M8162	5.50
M8163	5.50
M8190	4.50
M8195	6.50
M8196	5.50
M8204	5.50
M8223	4.50
M8224	2.00
M8225	3.95
ME1401	29.50
ME1402	29.50
ME1501	14.00
MH4	3.50
MHL6	4.00
ML4	4.50
MP25	195.00
MS4B	5.50
MU14	3.50
MZ1-100	125.00
N37	12.50
N78	9.85
OA2	0.85
OA2WA	2.50
OA3	2.50
OB2	0.85
OB2WA	2.50
OC2	2.50
OC3	1.50
OD3	1.70
OM4	1.00
OM5B	3.00
OM6	1.75
ORP43	2.50
ORP50	3.95
P61	2.50
P41	2.50
PABC80	0.75
PC86	0.75
PC88	0.75
PC92	3.50
PC97	1.10
PC800	1.10
PC900	1.25
PCC84	0.40
PCC85	0.55
PCC88	0.70
PCC89	0.70
PCC189	0.70
PCC805	0.70
PCC806	0.80
PCE82	0.80
PCF80	0.65
PCF82	0.60
PCF84	0.65
PCF86	1.20
PCF87	0.40
PCF200	1.80
PCF201	1.80
PCF800	0.40
PCF801	1.35
PCF802	0.60
PCF805	1.25
PCF806	1.00
PCF808	1.25
PCH200	1.50
PCL82	0.85
PCL83	2.50
PCL84	0.75
PCL85	0.80
PCL86	0.85
PCL800	0.80
PCL805	0.90
PE1-100	69.00
PEN25	2.00
PEN40DD	2.50
PEN45	3.00
PEN45DD	3.00
PEN46	2.00
PE06-40N	42.00
PFL200	0.95
PL21	2.50
PL36	1.75
PL38	1.50
PL81	0.72
PL81A	0.72
PL82	0.60
PL83	0.52
PL84	0.78
PL88	1.00
PL95	2.95
PL302	1.00
PL345	12.50
PL500	1.10
PL504	1.15
PL508	1.75
PL509	4.85
PL519	4.95
PL802T	3.50
PL820	2.95
PL5557	29.50
PY32	0.60
PY33	0.50
PY81	0.70
PY82	0.70
PY83	0.70
PY88	0.65
PY500A	1.95
PY800	0.79
PY801	0.79
QB3-300	54.95
QB3-1750	139.50
QB5-3500	595.00
QE03-10	4.95
QE08-200	145.00
QF40	65.00
QP25	1.00
QQE02-5	19.50
QQE03-12	7.95
QQE03-20	35.00
QQE06-40	45.00
QQV02-6	19.50
QQV03-10	5.50
QQV03-10 Mullard	15.00
QQV03-20	25.00
QQV06-40A	27.50
QQV06-40A Mullard	45.00
QQV07-50	63.50
QQZ03-20	42.50
QS75/20	1.50
QS75/40	3.00
QS92/10	5.00
QS95/10	4.85
QS108/45	4.00
QS150/15	6.95
QS150/30	1.15
QS150/45	7.00
QS1200	3.95
QS1202	3.95
QS1203	4.15
QS1205	3.95
QS1206	1.05
QS1207	0.90
QS1208	0.90
QS1209	3.15
QS1210	1.50
QS1211	1.50
QS1212	3.20
QS1213	5.00
QS1215	2.10
QS1218	5.00
QU37	9.50
QV03-12	5.75
QV05-25	2.50
QV06-20	29.50
QV08-100	145.00
QV2-2500	45.00
QY3-125	65.00
QY4-250	70.00
QY4-400	76.00
R10	4.00
R16	12.00
R17	1.50
R18	2.50
R19	2.50
R20	1.20
R1169	55.00
RG1-125	4.95
RG1-240A	14.50
RG3-250A	3.50
RG3-1250A	35.00
RK2K25	62.50
RK-208	12.00
RL16	1.50
RPL16	12.00
RPY13	2.50
RPY43	2.50
RPY82	2.50
RR3-250	15.00
RR3-1250	35.00
RS613	45.00
RS685	54.95
RSS688	52.15
S6F17	5.95
S6F33	28.95
S11E12	38.00
A30/2K	12.00
S104/1K	10.00
S109/1K	15.00
S130	5.59
S130P	5.95
SC1/800	5.00
SC1/1100	6.00
SC1/1200	5.00
SC1/1300	6.00
SC1/200	9.00
SP41	5.00
SU42	4.95
TB2.5/300	85.00
TB2-300	45.00
TB3-2000	395.00
TD1-100A	25.00
TD03-10F	35.00
TD3-12	4.00
TDD4	5.50
TP25	1.50
TSP4	7.00
TT11	1.50
TT21	45.00
TT22	45.00
TT100	57.00
TTR-31MR	65.00
TY2-125A	85.00
TY4.400	85.00
TY8-600W	365.00
TYS2/250	375.00
U18-20	2.75
U19	11.95
U24	2.00
U25	0.90
U26	0.90
U37	9.00
U41	6.95
U50	2.00
U82	3.00
U191	0.70
U192	1.00
U193	0.65
U251	1.00
U801	0.75
UABC80	0.65
UAF42	1.00
UBF80	0.60
UBC41	2.25
UBC81	1.50
UBF89	1.00
UBL21	1.75
UC92	1.20
UCC84	0.70
UCC85	0.60
UCF80	1.00
UCH21	1.20
UCH41	2.50
UCH42	2.50
UCH81	1.00
UCL82	1.75
UCL83	2.50
UF41	1.15
UF42	1.15
UF80	1.75
UF85	1.20
UF89	2.00
UL41	5.50
UL44	3.50
UL84	1.50
UL85	0.85
UU5	3.50
UU7	8.00
UU8	9.00
UY41	3.50
UY85	0.70
V235A/1K	250.00
V238A/1K	295.00
V246A/2K	315.00
V2406/1K	225.00
V241C/1K	195.00
V339	3.50
V453	12.00
VLS631	10.95
VP4B	4.50
VP133	2.00
VR75-30	3.00
VR101	2.00
V105/30	1.50
VR150/30	1.15
VT52	2.50
VU29	4.50
VU39	1.50
W21	4.50
W77	5.00
W729	1.00
W739	1.50
X24	4.50
X65/X65	4.95
X76M	1.95
XC24	1.50
XC25	0.50
XFW47	1.50
XFW50	1.50
XG1-2500	75.00
XG5-500	22.50
XL628FT	7.50
XNP12	2.50
XR1-1600A	49.50
XR1-3200A	79.50
XR1-6400A	149.50
Y502	25.00
Y65	6.95
YD1100	75.00
YJ1060	265.00
YL1020	42.50
YL1060	195.00
YL1070	195.00
YL1071	195.00
YL1290	65.00
Z77	1.20
Z302C	12.00
Z359	9.00
Z505S	15.00
Z520M	4.00
Z521M	8.00
Z700U	3.00
Z749	0.60
Z759	19.85
Z803U	18.95
ZA1000	12.50
ZA1001	1.50
ZM1005	8.00
ZM1020	6.00
ZM1021	8.00
ZM1023	7.95
ZM1041	14.00
ZM1082	9.00
ZM1084	10.00
ZM1177	9.00
ZM1202	55.00
ZM1263	4.00
1822	10.00
1827	55.00
1B35A	45.00
1B63A	75.00
1C1	2.50
1C5GT	2.50
1FD1	2.50
1G3GT	2.50
1J3GT	2.50
1K3	2.50
1N2	4.50
1N5GT	2.50
1P28	25.00
1P39	19.50
1R5	8.90
1T4	1.00
1U4	1.75
1U5	1.00
1X2B	1.40
1Z2	8.95
2AS15A	11.50
2B7	1.50
2B22	69.50
2C39A	32.50
2C39BA	39.50
2C40	37.00
2C42	29.50
2C43	60.00
2C51	0.75
2C53	45.00
2CY5	1.50
2D21	1.95
2D21W	2.50
2E22	49.00
2E26	7.95
2J42	93.00
2J55	295.00
2K25	35.00
2K25 Raytheon	75.00
2K26	95.00
2K29	250.00
2K48	140.00
2K56	250.00
2X2A	5.00
3A/107B	12.00
3A/108A	9.00
3A/109B	11.00
3A/110B	12.00
3A/141K	11.50
3A/147J	7.50
3A/167M	10.00
3A/2	3.95
3A3A	3.95
3A4	1.10
3A5	4.50
3IL5	0.95
3AT2	3.35
3B2	3.00
3B4	4.95
3B7	4.50
3B24	10.00
3B26	24.00
3B28	15.00
3BZ6	1.50
3C4	1.00
3C23	19.00
3C45	24.00
3CB6	1.50
3CN38A	2.50
3CS6	0.95
3CX3	2.50
3CY5	1.50
3D6	4.50
3D21A	29.50
3E22	49.50
3EH7	1.95
3EJ7	1.95
3V4	1.75
3W4GT	2.50
4B5518	115.00
4-65A	59.00
4-250A	79.50
4-400A	87.50
4-1000A	425.00
4B32	35.00
4BQ7A	1.75
4BZ6	1.95
4C27	25.00
4C28	25.00
4C35	145.00
4CX1000A	425.00
4CX4000A	1100.00
4CX250B	49.00
4CX250B EIMAC	59.50
4D32	125.00
4CX250BN	75.00
4CX250K EIMAC	95.00
4CX125C EIMAC	125.00
4CX350A	95.00
4CX350F	79.50
4XC1500B	398.50
4E27	195.00
4GS7	2.25
4J52	75.00
4JC6A	2.95
4KT6	1.50
4X150A	35.00
5A/102D	9.50
5A152M	9.00
5A163K	10.00
5A170K	6.25
5A-180M	9.00
5A-206K	10.00
5AM8	2.15
5AN8	1.20
5AR4	2.00
5AU4	1.50
5B 110M	10.00
5B-254M	14.50
5B-255M	19.50
5B-256M	15.00
5B-257M	15.00
5B-258M	14.50
5C22	125.00
5CL8A	2.50
5J180E	2950.00
5R4GB	5.50
5R4GY	4.95
5T4	5.95
5U4G	2.95
5U4GB	4.50
5V4G	1.50
5Y3GT	1.95
5Z4GT	1.50
6/30L2	0.75
6A/203K	9.00
6A7	4.95
6A8G	1.50
6AC7	2.00
6AG5	1.50
6AG7	1.95
6AH6	2.50
6AJ4	2.00
6AJ7	2.00
6AK5	1.95
6AK6	2.50
6AL5	0.60
6AM4	3.25
6AM5	6.00
6AM6	1.95
6AN5	4.50
6AN8A	3.50
6AQ5	1.75
6AQ8	0.85
6AR5	5.95
6AR8	3.95
6AS5	1.50
6AS6	2.50
6AS7G	4.50
6AT6	0.75
6AT8	1.75
6AU4	2.00
6AU5GT	4.50
6AU6	0.95
6AV6	0.75
6AW8A	2.50
6AX4GT	1.95
6AY3B	1.95
6AZ8	4.50
6B7	2.80
6B8G	1.50
6B10	1.95
6BA6	0.95
6BA7	4.50
6BA8A	3.50
6BE6	0.95
6BG6G	3.00
6BH6	1.95
6BH8	1.50
6BJ6	1.50
6BK4	4.00
6BK7A	1.95
6BL6	85.00
6BL8	1.15
6BM6	115.00
6BM8	0.58
6BN4	1.65
6BN6	1.65
6BN7	4.50
6BN8	3.95
6BQ5	0.75
6BQ7A	0.72
6BL7GTA	3.95
6BR5	0.70
6BR7	4.95
6BR8	2.15
6BR8A	2.15
6BS7	5.50
6BW4	1.50
6BW6	5.35
6BW7	1.50
6BW8	4.00
6BX6	0.48
6BX7GT	3.50
6BZ6	2.50
6BZ7	2.95
6C4	1.25
6C5	1.95
6C6	2.50
6C8G	1.50
6C11	2.50
6C18	2.50
6CA4	4.95
6CA7	3.50
6CB5	3.95
6CB6	1.95
6CD6GA	4.50
6CF6	1.50
6CG7	2.25
6CH6	6.95
6CL3	3.95
6CL6	3.25
6CL8A	1.50
6CN5	1.60
6CM7	2.95
6CS6	0.75
6CS7	0.95
6CW4	6.50
6D6	2.50
6DC6	2.35
6DJ8	0.95
6DK6	1.50
6DQ5	5.95
6DQ6B	2.50
6DT6A	1.50
6DW4	2.15
6E5	3.95
6EA4	4.95
6EA7	2.50
6EA8	2.50
6EB8	1.75
6EM5	2.50
6EM7	2.50
6EU7	1.95
6EU8	1.75
6EV7	2.95
6EW6	2.95
6EW7	4.50
6F1	2.00
6F5	4.95
6F6	2.50
6F6G	2.00
6F7	5.50
6F12	1.50
6F13	3.00
6F14	1.00
6F17	2.75
6F21	2.50
6F23	0.60
6F24	1.25
6F25	1.25
6F28	1.25
6F32	1.25
6F33	17.00
6FG5	1.95
6FQ7	2.95
6G6G	5.50
6GE5	3.95
6GH8A	0.80
6GK5	1.50
6GK6	1.95
6GM6	2.65
6GS7	2.15
6GV7	2.50
6GW6	2.50
6GW8	2.50
6GY5	3.95
6H1	9.50
6H3N	2.50
6H6	1.95
6H6GT	1.95
6HF5	5.50
6HF8	2.50
6HS6	4.95
6J4	2.15
6J4WA	3.15
6J5	2.50
6J6	2.00
6J7	4.15
6J86A	4.50
6JE6C	8.50
6JU8	2.50
6JS6C	6.50
6J7G	4.15
6K7G	2.00
6K8G	3.00
6KD6	6.50
6KM8	2.50
6KT8	2.95
6L1	2.50
6L15	3.15
6L19	3.95
6L6GC	2.95
6L6GC(GE)	4.95
6L6GT	1.95
6LD20	1.15
6LF6	7.50
6L6GAY	5.50
SYLVANIA	
6LJ8	2.50
6LQ6	6.50
6Q7GT	1.20
6R7G	3.15
6S4A	1.50
6SA7GT	1.35
6SC7	1.50
6SG7	2.50
6SH7	1.35
6SJ7GT	1.20
6SK7	1.35
6SK7GT	1.35
6SL7GT	0.85
6SN7GT	1.35
6SQ7	1.35
6SS7	1.95
6U4GT	1.75
6U6WA	3.50
6U8	1.15
6U8A	1.50
6V6G	1.25
6V6GT	1.50
6VGG	1.00
6Y6G	3.95
6X2N	1.00
6×4	1.50
6×5GT	1.00
6×5GTY	1.00
6×8B	2.25
7A6	4.50
7A7	2.00
7AD7	1.75
7AU7	1.50
7B6	3.50
7B7	2.50
7C5	3.50
7C6	2.50
7E7	2.50
7H7	3.50
7J7	5.50
7Y4	2.50
8B8	2.50
8B10	2.50
8BQ5	1.95
8FQ7	1.95
10D2	1.25
10DE7	2.50
10EW7	2.95
10F1	0.75
10GK6	1.95
10P14	2.50
10P18	0.75
10LD12	0.65
11E2R	45.00
11E3	55.00
11R3	5.50
12A6	3.95
12AD6	1.50
12AH7GT	4.95
12AL5	1.00
12AT6	1.25
12AT7	1.15
12AT7WA	2.50
12AU6	1.50
12AU7	0.65
12AV6	1.95
12AV7	2.50
12AX7GT	1.00
12AX7	0.65
12AX7WA	2.50
12AY7	3.95
12AZ7A	1.95
12B4A	4.50
12BA6	1.50
12BE6	1.95
12BH7A	2.50
12BL6	1.75
12BY7A	2.75
12C8	2.50
12CA5	1.95
12CX6	1.20
12DQ6B	3.50
12DW4A	3.50
12E1	19.50
12E14	38.00
12GN7	3.95
12HG7	4.50
12HG7A	4.50
12J5GT	3.95
12J7GT	3.50
12JZ8	2.95
12K5	1.95
12K7GT	1.50
12K8	1.95
12SA7GT	1.95
12SG7	4.75
12SH7	1.95
12SK7	1.95
12SJ7	1.50
12SQ7GT	1.95
12SN7GT	1.85
12X4	1.95
12SY7	4.50
13D3	3.20
13D7	3.20
13D9	3.20
13DE7	2.50
13DR7	2.95
13E1	145.00
13EM7	3.50
14S7	1.95
16GY5	2.95
17BE3	2.50
17DW4A	2.95
17EW8	0.95
17JZ8	4.50
18D3	6.00
18GB5	3.50
19AQ5	3.50
19AU4GT	2.50
19G6	5.00
19G3	17.00
19H4	35.00
19H4R	25.00
19H5	33.50
19Q6

FEEDBACK

Aftersales

There must be many sales representatives who are unaware of the existence of The Royal Pinner School Foundation, which is an educational trust set up for the purpose of assisting, by means of grants and awards in the education, up to the age of 25, of sons and daughters of sales representatives where the family has suffered adversity.

The majority of children assisted attend local state schools, colleges of further education, university or special schools in the case of handicapped children, and financial assistance is given towards school clothing, books, tools, school trips, field courses, music, etc.

If any of your readers would like to have further information I would be pleased to send them, on request, a copy of our leaflet.

S. Thurtell
Secretary
The Royal Pinner School Foundation
110 Old Brompton Road,
London SW7 3RB

Frequency allocations

Mr Eaton, in his reply (*E & WW*, December, 1986) to my letter said that the UK's l.f. allocation of 200kHz could have been maintained if a different channel grouping plan had been adopted for the l.f. band at the 1979 WARC.

This would be the case if the major cause of intermodulation product interference in the l.f. band were other l.f. signals. However, in practice, a more significant problem is that of intermodulation products generated by strong signals in the m.f. band. Since the m.f. band is organised on multiples of 9kHz any intermodulation products will also fall on multiples 9kHz. To minimize the effect of such products falling inside the l.f. band, the broadcast carriers have to be located at multiples of 9kHz.

For example, if two strong m.f. signals were present at 909kHz and 1107kHz then a second order intermodulation product could be generated in the receiver front end at 198kHz (1107-909kHz). This would cause a 2kHz interfering whistle to the reception of 200kHz but because it would fall directly on the carrier of a 198kHz signal the degradation to this service would not be so severe.

Henry Price
Assistant Head of Engineering Information Department, BBC

S5/8

In reply to Mr Hayard's letter in the January, 1987 issue, I am sorry that he remains "unconvinced" about the merits of S5/8, but I must, again, take issue with his comments.

Transmission and termination: a series resistor at the transmitter end is a terminator! The value suggested (270Ω) is not the ideal value (which would be calculated from line impedance – driver output impedance), but was a compromise between ringing suppression and output short circuit protection. A higher value will, in any case, reduce the problem of ringing. The incidence of ringing is dependent on the rise and fall time of the driver in relation to the cable's electrical length. The series resistor, in conjunction with the cable's capacitance, reduces the rise and fall time, so reducing the ringing. I suggest Mr Hayward tries driving some cable with an HCMOS gate with 270Ω series resistor and observes the result. The resistor used on the floppy disc interface is more of an open-collector pullup than a terminator (although it is often referred to as such), and with only a uni-directional (sink) drive at the source end can hardly be considered an example of how to properly terminate a line! Also, modern low power 90mm (3.5 in) disc drives are using 1kΩ pullups, because of the high power consumption of the traditional 150Ω.

Costs and consumption: the question of an "extra pound or two" for components is a question that will be settled by market forces, not by discussion in a letters column, but I think if Mr Hayward took the matter up with Alan Sugar, he might get a more forthright answer! On the consumption question, 100mA (or more) for differential is highly significant, despite Mr Hayward's dismissal. In battery portable equipment, an extra 100mA would make the difference between a saleable product, with a useful battery endurance, and an unsaleable one, in constant need of recharging. Of course, every user would like an error-free transmission link, but using RS-485, or any other electrical transmission scheme, does not guarantee that; a detection/retransmit or correction protocol would still be required.

On Mr Hayward's final point, about consultation, the Draft for Development (DD) is the normal method of inviting comments on proposed British Standards. So far as publication of early information in the pages of *Wireless World* is concerned, if the editor is willing, I am quite prepared to write a regular column summarizing standards activities in my field (Data transmission, floppy discs, ISDN, etc.).

However, there is a way in which Mr Hayward could participate at an earlier stage: I have just been appointed convenor of IST6/7 and I am actively looking for new members. IST6/7 is not only responsible for S5/8, but also is the UK member for ISO SC13, which handles equipment interface standards, such as Small Computer System Interface (SCSI, used on intelligent disc and cartridge drives), Fibre Distributed Data Interface (FDDI, a fibre-optic token ring network), Intelligent Peripheral Interface (IPI, also used for disc and tape drives), and the proposed standard for floppy-disc interfaces. How about it, Mr Hayward? Are you willing (and able) to join IST6/7 and participate in the meetings (about six a year, in London)?

This invitation is also open to any other readers who are interested. You don't need to be an expert, but you may become one when you have finished a project. You will need to have some level of technical knowledge, of course, but your chief requirements are interest and enthusiasm. Most of the proposals listed above are manufacturers' "*de factos*" originating in the USA. It is time the UK took a more active and contributory role, actually creating new standards, not just commenting on US proposals. *Wireless World* is well known for its knowledgeable and active readership. Any readers interested in taking up this offer are invited to contact me.

Andrew Hardie
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Courtwick Lane
Littlehampton
West Sussex

Maximum power theorem

Whilst I can claim no originality for Thévenin's theorem or maximum power transfer theorem I believe it would have been both useful to readers and courteous to give reference to my article 'Impedance Mismatching', *Wireless World*, March 1980, pages 59 and 78, which follows the same theme as the above article appearing in the January issue of your journal.

F.J. Lidgey
Principal Lecturer in Electronics
Oxford Polytechnic

Heat transfer

I was delighted to read the letter by Nigel Pritchard of SGS (January, 1987) on heat transfer in transistors. I cannot agree that the differences in sound which I noted were due to factors other than junction temperature. Firstly, the TO126 devices (BD139 & BD140) had replaced TO92 small signal transistors with a marked improvement in sound quality. Secondly the sound quality improved further when the TO 220 transistors were mounted on the same sheet of aluminium. Further evidence in other parts of the circuit backs up my argument. An amplifier project which I designed for low-temperature-generated distortion for *Practical Electtronics* was very favourably reviewed by one of their regular contributors.

It should be possible to measure the increased harmonic distortion caused by temperature-generated distortion, but I would guess that it would be a small part of the overall distortion.

The real problem, I find, is to locate suitable transistors for audio use which combine the gain at low collector currents of small-signal transistors and the

FEEDBACK

low thermal resistance of typical audio output transistors. Such transistors must have a metal tab to screw to a heat sink, constant H_{fe} over a wide current range (e.g. 10μA to 100mA). Minimum H_{fe} guaranteed not less than 50 or 100 over this current range, V_{be} close to 600mV at 1mA collector current and a thermal resistance from junction to case of 3 to 5°C/watt. Such transistors would need to be available in n-p-n and p-n-p configurations, with dual matched pairs available and high-voltage versions also available with some relaxation of the above specs.

If Mr Pritchard can help, I would be delighted to hear from him.

Graham Nalty
Audiokits
Borrowash
Derby

Relativity

I have always found the experience of reading James MacHarg's letters rather like reading a novel by James Joyce, and the feeling was intensified when I saw his letter in the January issue. The letter includes a 'mathematical proof' that 2 equals 1. If every schoolboy had been taught that dividing both sides of an equation by a factor whose value is zero is a legitimate mathematical operation British technology would be in a poor way. In Victorian times continental mathematicians accused their English colleagues of lack of rigour and of excessive formalism. It seems old habits die hard.

Lee Coe in a letter in the same issue used the phrase 'wave velocity' for what is evidently the phase velocity for the wave pattern within a pulse travelling down a wave guide. Information is propagated, not at the phase velocity, but at the group velocity, so that Special Relativity does allow phase velocities in excess of the velocity of light. For a normal waveguide the product of the phase and group velocities is c^2. It is easier to avoid exciting unwanted modes at drive frequencies near cut-off, but at those frequencies the group velocity falls well below the velocity of light and the phase velocity therefore rises well above it. Loading the guide in the way described in the *Scientific American* article reduces the product of the phase and group velocities. The guide can then be driven at near its cut-off frequency to minimise the rise of mixed-mode excitation, yet will still allow a pulse to propagate for which the phase velocity is less than the velocity of light, so that it is suitable for accelerating electrons. Thus the design of the Stanford Electron Linac does not provide evidence against Special Relativity. How could it, seeing that the Lorentz Transformations were first introduced as transformations which when made left the form of Maxwell's equations unaltered?

Alex Jones' letter in the January issue includes a photograph of an interesting demonstration devised by the arch-engineer, inventor, and showman Professor Eric Laithwaite. However, the account he gives is wrong. The downward force on the finger is almost exactly 24 pounds, as anyone can verify who cares to weigh a toy gyroscope and its tower first stationary, and secondly after the gyroscope has been brought up to speed, and then set with its axis horizontal and supported at one end on the top of the tower. There is nothing out of the way about supporting a 24 pound weight from a little finger if it is suspended directly below; what is surprising is that with the weight over a foot off to one side the finger isn't twisted out of its socket. In fact the twist exerted is almost nil.

I have seen Professor Laithwaite's demonstration, which makes it quite obvious that most of us have very little feeling for the behaviour of gyroscopes, even if we are aware that the equations governing the motion of a rigid body include some which describe the effects of torques on angular momentum as well as those which describe the effects of resolved forces on linear momentum. On that occasion members of the audience were invited to try their hand. A secretary who did what she was told to do coped with the massive rotor very well, but a senior physicist who 'knew' he would have to exert a substantial torque on the end of the shaft to prevent the rotor from swinging downwards lost control in a fraction of a second!

The rotor demonstrations themselves have no relevance to Special Relativity, but a rotor does have one very important characteristic. While rotating at constant speed it experiences strong internal stresses, which if it is rotating fast enough can cause it to fly to bits. The existence of these stresses must therefore be recognised in every inertial frame, i.e. rotational motion is not relative, but absolute. It may be more than a coincidence that the experiments by Aspect which violate the Bell inequality (derived from Special Relativity) involve the observation of quantised rotations.

C.F. Coleman
Grove
Oxfordshire

Mathematics

In the January 1987 issue of *E&WW*, Mr James MacHarg of Northumberland has tried to demonstrate "the lack of value of mathematics, unless backed by an active dynamic logic which may be expressed in words" In this he has totally failed.

By its very nature, as any mathematician will tell you, the science of mathematics is an active dynamic logic and may be expressed in plain English if so required.

I hope not all schoolboys have been shown his 'proof' that 2 = 1, for in his attempt to create a mathematical catastrophe, he has made a number of fundamental errors, as I will now show.

MacHarg	Remarks
Let $a = b$	
Multiply both sides by a $a^2 = ab$	
Subtract b^2 from both sides $a^2 - b^2 = ab - b^2$	As $a = b$, so $a^2 = b^2$, therefore $a^2 - b^2 = 0$ and $(ab) - b^2 = b^2 - b^2 = 0$ very neat, we have just proved that zero equals zero.
Let us continue	
Factorize $(a + b)(a - b) = b(a - b)$	
Cancel $\frac{(a + b)(a - b)}{(a - b)} = \frac{b(a - b)}{(a - b)}$	Dividing by $(a - b)$ At the beginning, we set $a = b$ i.e. $(a - b) = 0$ Dividing by zero is an invalid operation as it is non defined. So, this reduces to $\frac{(a + b) \times 0}{0} = \frac{b \times 0}{0}$ What is zero divided by zero? It certainly isn't unity as Mr MacHarg is suggesting.
$(a + b) = b$	Perfectly OK so long as
but $a = b$	$a = 0$, but as $a = b$ (our definition) b must also be zero.
$2b = b$	Correct if $b = 0$
$2 = 1$	Nonsense.

I'm sorry, Mr MacHarg, but one should be sure of one's methods before attempting to discredit anything, especially by reductio ad absurdam.

John R. Ridley
Ruwi
Sultanate of Oman

RESEARCH NOTES

Purple plague is back

'Purple plague', thought to have been eliminated in the 1970s as an i.c. failure mode, is now back with a vengeance according to a paper by P.K. Footner, B.P. Richards and R.B. Yates of the GEC Hirst Research Centre (*GEC Journal of Research* Vol.4 No3). Named after the characteristic colour of a gold/aluminium intermetallic compound $AuAl_2$, purple plague is a failure of the joint between the i.c. connecting pads and the wire that connects them to the external packaging.

Ten years ago, the cause of purple plague was attributed to chemical action between the bonding wire and the metallization of the chip's connecting packs. This process was shown to be accelerated by high temperatures, leading to the formation not just of $AuAl_2$, but a whole range of intermetallics having different physical and electric properties from either parent element. The prime cause of bond failure, however, was shown to be mainly due to a process called Kirchendall voiding. Gold diffuses faster than aluminium, resulting in vacancies in the atomic lattice on the gold-rich side of a bond. Towards the end of the 1970s the application of low-temperature fabrication processes – such as ultrasonic bonding – and improved bond design virtually eliminated purple plague failures.

Now, it appears, the disease is back. According to Footner *et al* there is a variety of reasons, including an increasing use of gold bonding wire and a tendency among manufacturers and users to disregard the possibility of purple plague failure. Other factors are also mentioned, including a Russian suggestion that intermetallic formation in a bond is governed in some way by the current or field conditions operating through or around the bond.

In analysing the failure of some modern l.s.i. chips using optical and scanning electron microscopy, the GEC team conclude that the reappearance of purple plague is partly the result of carelessness and partly a consequence of the changing design of chips. They say that the constraints currently applied to MSI are not directly applicable to l.s.i. and v.l.s.i. Also, the use of plastic encapsulants has in some cases led to new mechanisms of bond failure, such as chemical attack or the ingress of water.

How then can purple plague be eliminated in today's chips? Footner and his colleagues suggest that if it is impractical to employ single-metal systems, then at least it might be possible to use intermediate alloys such as titanium/tungsten to reduce the rate of diffusion. Apart from that, it is a matter of re-establishing sound design criteria and carefully monitoring factors such as fabrication temperature and chemical cleanliness. Ultimately, they say, it is important to remember that as long as dual-metal systems are employed, there will be the possibility of purple plague existing in some form.

Transputer in the driving seat

Dr Barry Thomas and Professor Erik Douglas of Bristol University are developing a transputer-based expert system for the control of unmanned vehicles. The three-year project, backed by £150,000 from the Ministry of Defence, will lead to a fully engineered vision system together with the control software necessary to drive a robot vehicle accurately and smoothly.

By human standards, the driving ability will be unspectacular; the robot is expected to be able to drive itself along an empty road and to turn right and left as necessary. Yet even this modest capability will require real-time algorithms capable of analysing each separate frame from a video camera mounted on the vehicle. Dr Thomas is developing the software to run on a bank of between 10 and 20 transputers which, unlike existing vision systems, will take the video signal directly without any external frame store.

The Bristol system is expected to be an order of magnitude more responsive than that of other robot vehicles, which up till now haven't been able to drive safely at more than a few km/h. Nor have they been able to distiguish the road from the verge by its texture alone.

In answer to the obvious question of why such large sums are to be spent in doing what a human being can do far better, Dr Thomas points to the possibility of having a military surveillance vehicle that could explore dangerous enemy territory entirely by passive sensing. There would be no need for give-away emissions or jammable radio links.

Ultimately, however, its true value is seen as 'enabling technology'. A real-time vision analyser could equally well act as a security guard or a method of checking goods on a production line...or eventually perhaps as a means of getting you home safely from the pub?

3-D optical memory

A new kind of photorefractive effect has been discovered by two physicists at the Racah Institute of Physics at the Hebrew University of Jerusalem's Faculty of Science. With further work, this effect, discovered by Professor Yizhak Yacoby and Aaron Agranat, will possibly be used for the development of holographic-type memories and other optical computing devices.

In contrast with ordinary disc or tape memories of computers where the information is stored on the surface of a magnetic film, holographic memories store information in the volume of special types of crystals. The information is written in and read out using laser beams.

Scientists in many laboratories have realised the distinct advantages of such memories and are looking for ways to put them into effect. The information density of such memories is enormous, and large quantities of information can be written and read in parallel using one flash of a laser beam. In these memories, it is possible to store directly both numbers and pictures without the need to translate the pictures into numbers first, as is done in present computers. The information is stored in such a way that defects in the crystal and small scratches do not spoil any part of the information. They just increase the intensity of the light which is needed to write the information.

The most promising way to write the information into the crystals is to use the photorefractive effect. This effect is simply the change in the refractive index of the crystal by the absorption of light in it. Experimental holographic memories using the photorefractive effect have been built, but the information stored in the memory is usually erased when the memory is read. This is a fundamental problem resulting from the very nature of the classical photorefractive effect.

Professor Yacoby proposed a new mechanism for a photorefractive effect which has been experimentally demonstrated and investigated by Agranat. The new photorefractive effect is at least as efficient as the classical effect and holograms written into the crystal are not erased when the information is read. Thus, this new effect may open a new way to utilize holographic computer memories.

Even though the Hebrew University scientists still have a long way to go to produce a practical device, they draw confidence form the fact that they have already demonstrated the existence of their new photorefractive effect and have the ability to produce the type of crystals that they need.

Radio telescope commissioned

The first radio signals have recently been received by the James Clerk Maxwell Telescope (JCMT) at the Mauna Kea Observatory in Hawaii. This is good news for British and Dutch astronomers, who have planned a schedule of observations for the whole of the year.

To test the new telescope it was first pointed at the moon with a receiver tuned to 230 GHz. As expected, a strong signal was received. All 'warm' objects emit signals at these high frequencies and for an object as close as the moon they are very easily detected. The telescope was then pointed at the planets Jupiter and Mars, and again strong signals were detected.

These events mark the successful commissioning of the telescope, in which engineering tests of its drive systems were carried out and the reflecting surface (consisting of 276 individual panels) was measured and adjusted to the correct paraboloidal shape. The sensitivity of the telescope is now being tested on far more distant objects and over a wide range of frequencies.

The construction of the 15 metre diameter radio telescope,

RESEARCH NOTES

The James Clerk Maxwell Telescope, the Mauna Kea. The viewing aperture can be covered by a p.t.f.e. membrane.

which is at the 4000 metre high Mauna Kea Observatory, by an arrangement with the University of Hawaii, began in the early summer of 1983. The foundations for the telescope and its co-rotating enclosure were completed in the following year. The major components of the telescope and its control system were delivered during 1985 and the assembly of the facility completed in June 1986.

The Rutherford Appleton Laboratory, in close collaboration with the University of Cambridge, has been responsible for the design, development, construction and commissioning phases of the project. Significant UK contributions have also been made by the Royal Observatory, Edinburgh, Queen Mary College, London and the University of Kent. In the Netherlands major contributions have been made by the University of Utrecht and the Laboratory for Space Research, Groningen. When it is commissioned the facility will be handed over to the Royal Observatory Edinburgh to be operated in conjunction with the United Kingdom Infrared Telescope (UKIRT) which is adjacent to the JCMT at Mauna Kea.

The James Clerk Maxwell Telescope will be the world's largest instrument capable of observing at frequencies above 300 GHz. This new telescope will give astronomers the opportunity to study one of the few remaining relatively unexplored parts of the electromagnetic spectrum where, in particular, they hope to discover new information about star formation and the microwave background radiation.

Supercomputer for advanced research

The Science and Engineering Research Council's Atlas Centre at the Rutherford Appleton Laboratory is now using a Cray X-MP/48 Supercomputer as part of a national initiative in research computing, covering all disciplines.

The Cray has four processors and 8 Mwords of memory. There is also a 32 Mword solid state device memory and about 14 Gbytes of disc storage. It is front-ended by existing powerful IBM mainframe equipment and accessed through the Joint Academic Network JANET. Other front-end computers may be added in due course. The new machine, together with the centres already established at Manchester and London, is the latest of three national supercomputer centres dedicated to advanced research. Professor John Forty of Stirling University has agreed to be the chairman of a new management committee for the supercomputing service.

Use of the Cray will help UK scientists and engineers engaged in a wide range of computer simulation problems including: aircraft design; the evolution of the galaxies; molecular reactions; the physics of liquids and solids; large scale integrated circuit design; the circulation of the atmosphere and oceans; and the design and operation of new pharmaceutical products.

In many areas of science the laws governing the behaviour of matter and materials are well known, but the application of these laws to complex systems, which is of pressing scientific interest, results in equations which require enormous computing power to solve. The advent of supercomputers makes it possible to tackle problems of this type which could not be approached with earlier generations of computers.

These areas spread across the boundaries of the five Research Councils, and many are relevant to the needs of industry. The expoitation of supercomputers is thus an area of rapidly growing importance.

Radioactivity and soft faults

Studies at the University of Southampton have investigated in detail the phenomenon of 'soft' errors in dynamic rams due to alpha particles. Ever since the introduction of high density rams in the mid 1970s, alpha sensitivity has been widely reported, though there has always been some uncertainty over which parts of the chips are most sensitive.

What the Southampton researchers did was to take samples of 65K nmos drams from three different manufacturers and prepared them by removing their lids and protective coatings. Then, by using masks and an alpha source (Americium-241), they were able, selectively, to expose different areas of the chips to radiation.

With each different position of the masks, the rams were then subjected to a sequence of write/read cycles, with the error rates being noted in each case. These tests showed quite clearly that alpha hits cause the most errors, not when they hit a memory cell, but when they hit a bit line. In fact, for chips from all three manufacturers, hits on the bit lines provided the only significant source of alpha-induced soft errors. Cells, sense amplifiers and peripheral circuitry appear to make a negligible contribution.

The Southampton workers, in a recently published paper (*Electronics Letters*, Vol.22 No 25), provide evidence that the increased sensitivity of the bit lines, compared with other components is, in all probablity, a reflection of nothing more than their greater collecting area.

Research Notes is written by John Wilson.

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NEW PRODUCTS
COMPUTING

Mastering multi-tasking

The multi-tasking version of BBC Basic, supplied by Cambridge Microprocessor Systems, has been converted for use on the BBC Master Series computers. MultiBasic is control oriented and adds over 50 commands to the Basic language to enable the development of event-driven multi-tasking programs. Up to eight background tasks can be run concurrently with the main program: up to seven v.i.a. commands, two five-byte timers which can be accessed from within normal Basic, and three four-byte timers which can hold an integer value.

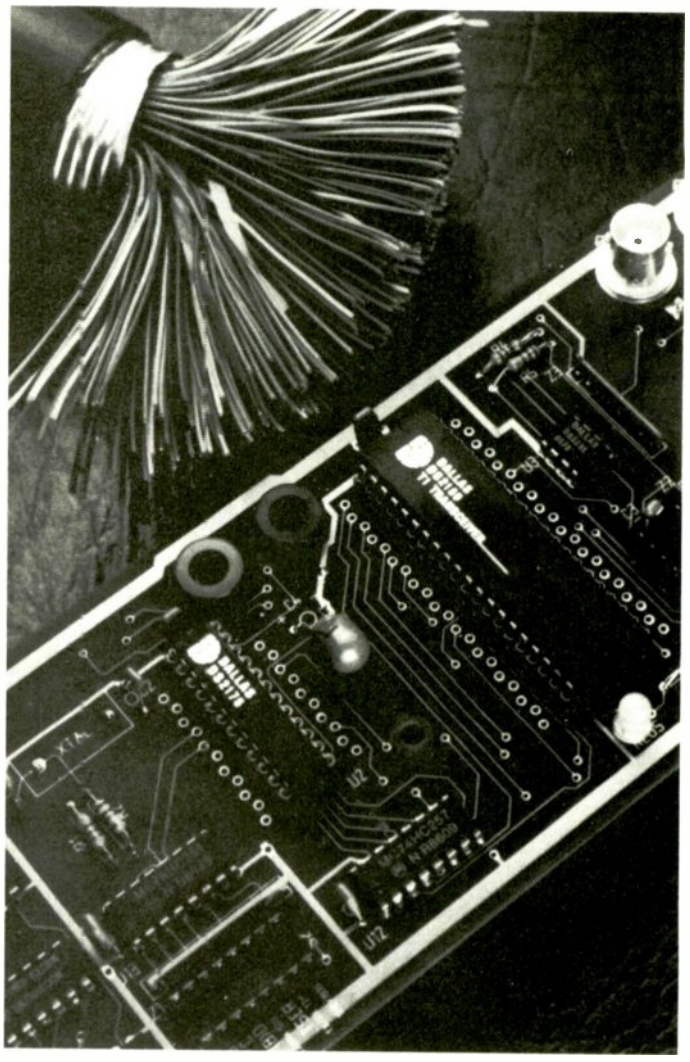
The language on a rom can be used in conjunction with the CMS 1MHz card which connects the Master to an industrial racking system for the addition of a range of i/o cards. These include digital i/o, 12 and 13-bit a-to-d, GPIB interface, additional graphic facilities. The system card includes a real-time calendar/clock, 8K of battery-backed ram and gives the user access to additional MultiBasic commands. The rom comes with a comprehensive manual with a detailed description of each command and example programs. CMS Ltd, Brookfield Business Centre, Twentypence Road, Cottenham, Cambridge CB4 4PS. Tel: 0994 51122.

Test and control from a PC

An analogue and digital multi-function plug-in card for the IBM PC/XT/AT and their equivalents, can be used in test, measurement and control applications. The card, from Kenda Electronic Systems, has 16 analogue input channels, expandable to 32, with programmable gain. 12-bit, 25μs a-to-d converters (optional 12 and 8μs versions) provide throughputs of up to 71,000 samples/s. It is provided with three a-to-d trigger modes including an external trigger and offers eight digital input and eight digital output channels along with two analogue output channels. Three counter/timer channels are provided. The system has a resolution of 12 bits.

Internally the card supports direct memory access with polled status and interrupt operation. It comes with software including machine-code routines for analogue and digital input/output and is also supported by several high-level language extensions including some to Basic, Pascal, Turbo Pascal, C, and Fortran running under MS/PC-DOS.

Typical applications include test and measurement, data logging, robot and machinery control and quality assurance work. Kenda Electronic Systems Ltd, Nutsey Lane, Totton, Southampton SO3 3NB. Tel: 0703 869922.

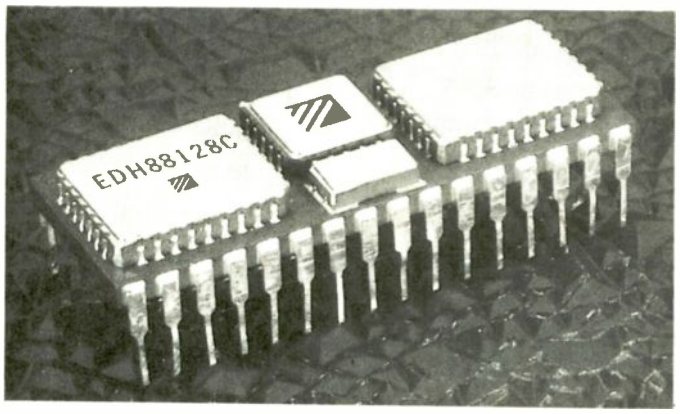


Megabit static ram

One-megabit static ram modules are now available from Electronic Designs with a standard 32-pin package. The EDI88128C is a 1024K-bit c-mos static ram based on four 32K by eight rams in leadless chip carriers. The device has an on-board decoder circuit that interprets the higher order address to select one of the static rams.

Organised as 128K by eight bits, the module is suitable for use in bus-based systems. Access times of 100, 120 and 150ns are available, and the fully static operation means no clocks or strobe signals are required. Power requirements are a single +5V supply, and all inputs are t.t.l. compatible.

This module has a 32-pin dual-in-line outline and is suitable for upgrading systems using existing 8K by eight or 32K by eight modules or monolithic memory parts. A future upgrade path to a maximum of 512K by eight will be available. Electronics Designs Europe, Shelley House, The Avenue, Lightwater, Surrey GU18 5RF. Tel: 0276 72637.



Design kit for twisted-pair networks

A design kit to help engineers take advantage of a high-speed twisted-pair data communications network for digitized voice and video services has been announced by Dallas Semiconductor. High-speed links (1.5-2.0 mbits) of the T1* kind can cause difficulties with framing and supervision, synchronization and twisted-pair interference, when linked to equipment running on a different time-base. The DS1280K kit provides a source of specialised information, necessary for successful T1 design, or the CEPT equivalent.

Based on a Dallas DS2180 transceiver and DS2176 receive buffer, the kit contains demonstration p.c.b. support logic circuits and provision for microprocessor control and line interface. Central to the kit is the transceiver. Featuring flexible control architecture, it allows soft configuration of T1 links under microprocessor control. The DS2176 buffer uses a two frame deep fifo memory, on-chip control and contention logic to adapt the data transmission rate. The buffer therefore acts as a 'system shock absorber' eliminating synchronization problems which can occur when linking with equipment running on a different timebase.

The buffer is claimed to be the first integrated solution to problems of synchronization between dissimilar backplanes and signalling supervision, and extracts, integrates and buffers signalling information embedded in the data stream.

Also supplied with the kit is an 80-page instruction manual, which contains data sheets on both the DS2180 and 2176, software control examples and assembly instructions, as well as discussions of the recent revisions of T1 standards. The European equivalent of T1, defined by CEPT, has a data transmission rate 2 million bits per second, and requires a different kit, also available from Joseph Electronics Ltd, Westminster House, 188 Stratford Road, Solihull, B90 3AQ. Tel: 021-745 3251.

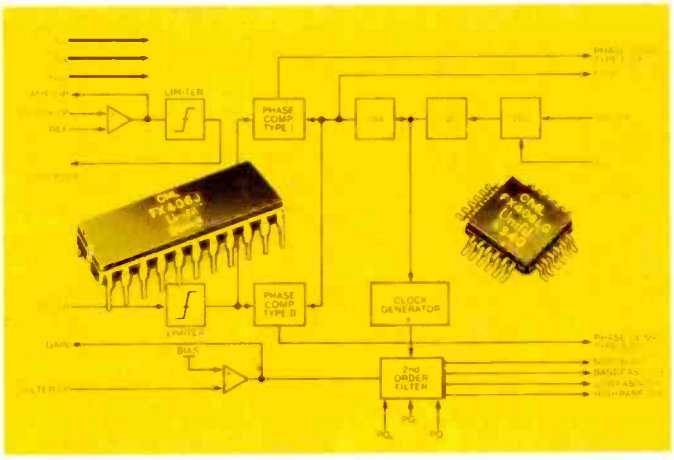
*T1 (and T-carrier) is a high-speed digital communications network developed by AT&T in the early 1960s to carry digitized voices. They can multiplex 24 p.c.m. channels on a twisted pair.

NEW PRODUCTS
COMPONENTS

Small dual digital-to-analogue converters

Dual 12-bit monolithic digital-to-analogue converters come from Analog Devices. The AD7537 and AD7547 are packaged in small 0.3in, 24-pin dips, claimed to be the smallest dual d-to-a available in the industry. Both devices contain two 12-bit current-output converters on one chip and different bus structures to handle a variety of applications. Also on-chip are data registers and control logic for easy microprocessor interfacing. The dual device occupies half of the area required by two separate 12-bit converters and allows ladder resistance matching of 0.5% and precise tracking.

The AD7537 offers a two-byte (8+4) input structure for right-justified loading from an eight-bit bus. The AD7547 features a 12-bit parallel loading structure that enables the user to load data in one 12-bit word, facilitating loading from a 16-bit data bus. Applications for the dual converters include automatic test equipment, programmable filters, audio and synchro applications, process control, and designs with space-sensitive requirements that could not accommodate two discrete devices. Analog Devices Ltd, Central Avenue, East Molesey, Surrey KT8 0SN. Tel: 01 941 0466.



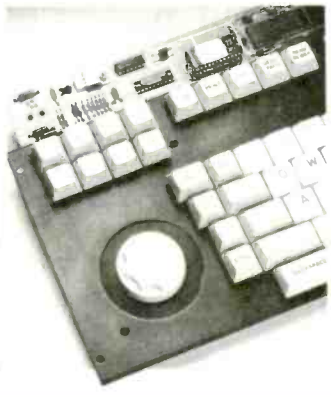
Analogue signal processor

The FX406 Unifil is a c-mos l.s.i. circuit with a variety of signal processing applications. The device consists of a switched-capacitor second-order active filter with outputs for bandpass, notch, low-pass frequency responses, together with a clock generator to provide the sampling clock frequency. The centre frequency of the bandpass and notch filters is the same as the cut-off frequency f(c) of the low-pass and high-pass filters. The filter sampling clock is derived from a multiplying phase-locked loop whose reference or input frequency is the same as the desired cut-off frequency of the filters. Facilities are provided to programme the cut-off frequency of the filters by injecting an external signal into the p.l.l, or by using the on-chip clock oscillator circuit. The filters have gain adjustment on the input and the Q is programmable to eight values between 0.54 and 8.0. A signal 5-volt supply rail and choice of d.i.l./surface mount package style make the FX406 suitable for universal analogue signal processing applications, such as programmable filters, voltage-controlled filters, sinewave oscillators, tracking filters/oscillators, f.s.k. modems, square-to-sine and pulse-to-sine converters. Consumer Microelectonics Ltd, Wheaton Road, Industrial Estate East, Witham, Essex CM8 3TD. Tel: 0376 513833.

Cursor controller

An alternative to roller balls, mice and joysticks, offered by the Cursor Disc from Alphameric Keyboards, can be incorporated into custom keyboards designed for cad/cam applications. The Cursor Disc eliminates the need to press separate keys to move in different directions, enabling users to operate it by feel. This increases ease of use since the designer's eye can be concentrated on the screen, rather than constantly having to look at the keys.

The cursor disc is the latest addition to the wide range of keyboard accessories including keytop shapes, legends and colours offered by Alphameric who produce capacitative keyboards tailored to individual requirements. Alphameric Keyboards Ltd, 6 Manor Way, Old Woking, Surrey GU22 9JX. Tel: 04862 71555.



NEXT MONTH

Spectrum analysis. Two leading manufacturers of spectrum analysers describe the functions and applications of their latest instruments. Also included are details of new analysers from other sources.

H.f. receiver development. An account of the development of a new communications receiver designed by Lowe Electronics: a discussion of the design principles, which include microprocessor assistance with alignment as well as control of circuit functions.

Image acquisition: a three-part article describing the use of a low-cost sensor as an alternative to a television camera in the capture and subsequent processing of images. The application is in computer vision used in manufacturing indutsry.

Reducing quantizing error. A description of a technique to enable analogue-to-digital converters to achieve a higher resolution that can be obtained directly from the converter. As an example, a temperature measuring device can resolve 0.1 degrees over a 110 degree range, using a 8-bit converter.

Microprocessor tone decoder. When tone-controlled, suppressed-squelch systems are used in mobile f.m. radio, the tone transmitted with the signal must be decoded in the presence of noise and fading. This article discusses the use of a microprocessor in this function.

Pulse-width-modulated power amplifiers are staging a come-back, the results of advances in mosfet technology and new circuit thinking.

Gould OS300 versus Douglas DC3

OS300	DC3
Proven worldwide	Proven worldwide
Inexpensive	Inexpensive
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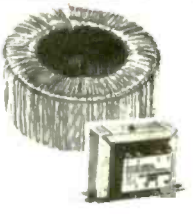
Toroidal & E.I. Transformers



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Quantity prices and delivery on request

AIR LINK

Air Link Transformers
Unit 6, The Maltings, Station Road, Sawbridgeworth, Herts. Tel: 0279 724425

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NEW PRODUCTS
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Mini portable 30MHz oscilloscope

ITT Instruments has introduced a compact, portable oscilloscope incorporating features that make it suited to heavy-duty field use. The OX709 has a large rectangular screen with 8 by 10 divisions of 8mm each. The use of a 10kV accelerating potential gives an exceptionally bright trace, so that the instrument can be used outdoors in normal daylight. In addition to an internal rechargeable lead-acid battery, the OX709 offers a choice of power sources; from 95 to 260V r.m.s. at 48 to 440Hz with no need for switching, and from 110 to 330V or 10V to 36V d.c.

The OX709 is a two-channel 30MHz-bandwidth oscilloscope, with vertical and horizontal reciprocal sensitivities from 1mV to 5V per division and a timebase, reciprocal sensitivity of 0.5µs to 0.2sec per division. Comprehensive trigger capabilities include automatic peak-to-peak triggering. The safety features, which include double isolation and the use of non-metallic BNC input connector housing and special safety probes, mean that the instrument meets the requirements of most safety standards. ITT Instruments. 346 Edinburgh Avenue. Slough, Berks SL1 4TU. Tel: 0753 82413.

Probe oscilloscope

A small hand-held oscilloscope made Lefax, makers of the Ledscope, may be used either in the manner of a logic probe or as a bench instrument, the virtue of avoiding frequent changes of view-point from probe to screen. The instrument is internally powered to eliminate leads, though the unit can accept standard scope leads. It will display most waveforms in the range 10Hz to 20Hz, and includes a voltmeter for direct voltage measurement up to 20V. Controls include gain, sweep speed, y-shift, mode and range select.

A 140-element led display is the key to the low cost of the unit (£90), which is expected to appeal to repair workshops, and educational and training establishments. Lefax Ltd, Unit 6, Genesis Business Centre, Redkiln Way, Horsham, West Sussex RH13 5QH.
Tel 0403 54135.

Hand-held capacitance meter with 0.2% accuracy

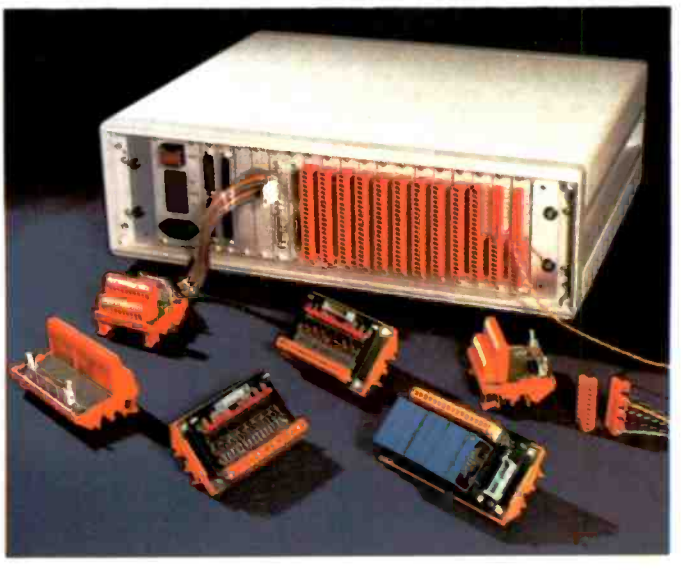
Capacitance measurements from 1pF to 20mF are possible with the Model 3000 capacitance meter from Global. It has 3.5 digits and a accuracy of 0.2% of the reading. A zero-adjust control permits the nulling of stray and incidental capacitance. Applications include sorting capacitor by value, checking tolerances, matching sets of capacitors and measuring unmarked devices. Global Specialities, Shirehill Industrial Estate, Saffron Walden, Essex CB11 3AQ. Tel: 0799 26699.

Temperature controllers

A range of temperature controllers based on Eurotherm's 800 series features RS232 or 422 communication and fast low-level a-to-d conversion. Model 818 has some built-in intelligence which allows it to 'tune' itself and adjust its programs to the conditions. It has two setpoints, two programmable logic outputs, two 4.5-digit displays, plug-in output options and display legends for all the functions. Type 818P has a 16-segment programmer and an additional display to show which segment is operating. Further indicators show the ramp, dwell and hold states, as well as the time remaining in ramp or dwell. The 815 model retains the accuracy of the 818 but leaves out some of the functions including the programmer, though it does include a programmable setpoint rate limit. Model 808 takes the minimum panel space and has fewer features, however plug-in options allow it to be adapted to the needs of the process. Eurotherm Ltd, Faraday Close, Durrington, Worthing, W. Sussex BN13 3PL. Tel: 0903 68500.

High voltages on STE bus

Many automation and control projects involve the sensing or switching of high voltages that can be dangerous to the operator and to computer equipment. Kemitron has devised a system of conditioning cards which will sense or switch mains or d.c. of 3A or over. The boards may be mounted in a cabinet some metres away from the computer system and the 12V signals are opto-isolated at both ends to increase safety. The boards have been designed for use with Kemitron's own range of industrial STE-bus computer systems, as previously featured in these pages, but will interface with almost any digital signals. Kemitron Ltd, Hawarden Industrial Park, Manor Lane, Deeside, Clwyd CH5 3PP. Tel: 0224 536123.



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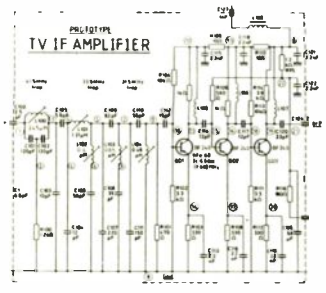

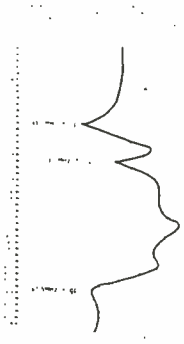

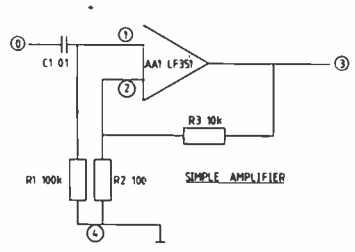

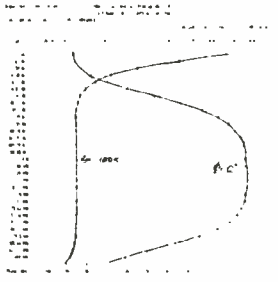

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NEW PRODUCTS
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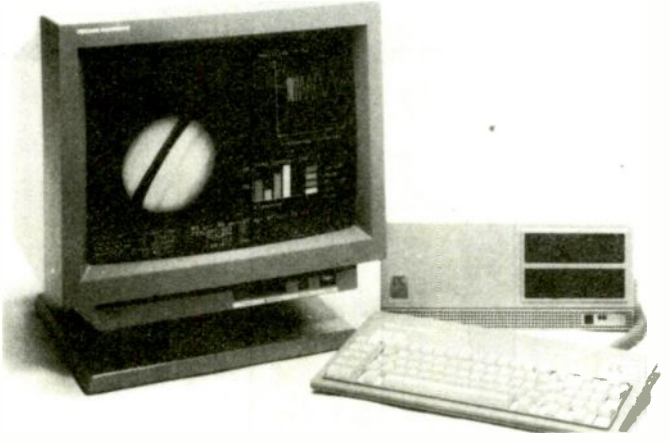
Cad plotter

New from Hi-Tek Solutions is the HP DraftPro plotter, an eight-pen drafting plotter specially designed for use with a personal cad system. The Draftpro has an RS232C V.24 interface which enables its use with a wide range of popular computers including HP Vectra, Apple Macintosh, DEC VAX, and IBM AT.

DraftPro plots on paper, vellum, or double-matte polyester film and accepts media widths from 550 to 640mm and media lengths from 400 to 1000mm. It uses fibre-tip, disposable liquid-ink, or refillable liquid-ink pens. The eight-pen carousel offers automatic pen changing and pen capping facilities. Features of the HP DraftPro include an addressable resolution of 0.025mm, a mechanical resolution of 0.013mm, and repeatabilities of 0.10mm (same pen) and 0.20mm (pen-to-pen). The plotter also has an acceleration of 2g, a pen cycle time of 100ms, and an accuracy of 0.5mm or 0.2% of the specified line length, whichever is greater.

The plotter offers the same variety of software support as other HP drafting plotters, including AutoCAD and VersaCAD. For users who write their own code, HP DraftPro supports more than 80 HP-GL (Hewlett-Packard Graphics Language) instructions to simplify graphics programming. Hi-Tek Solutions, Beadle Trading Estate, Ditton Walk, Cambridge CB5 8QD. Tel: 0223 213535.



Combined functions in cad workstation

A personal engineering workstation based on the Olivetti M245P has been produced by Densitron Computers. The DPE24 is a cad/cam graphics workstation with a fast graphics board set which is 100% software compatible with the IBM professional graphics controller. It enables the designer to access a variety of high-resolution cad packages but also provides full IBM CGA emulation for everyday engineering and business use. As the board supports both CGA and PGC modes no additional video adaptors are required and therefore only one monitor is needed for all applications.

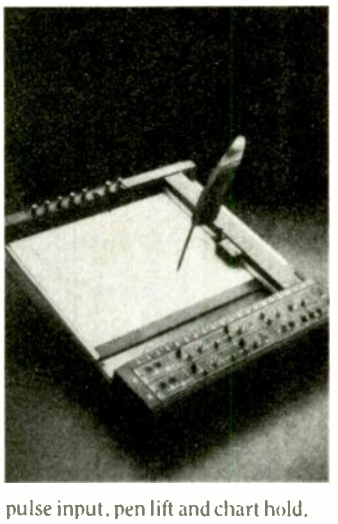
The DPE24 offers 640 by 480 resolution with 256 simultaneous colours capable of running a wide range of standard software. Drawing speeds of 35,000 vectors/s and 5,000 characters/s are possible. The high-speed operation is achieved by using a combination of the M245P, which uses an arithmetic co-processor, with the 10MHz central processor and an intelligent graphics board set. The graphics board set itself incorporates two pipelined processors, one v.l.s.i. drawing processor, the Hitachi 63484, and a 32016 display list processor. This hardware architecture is coupled with 128Kbytes of on-board firmware and 90 high-level PGC and enhanced graphic commands.

The workstation package is completed by a 20in (optionally 14in) high-resolution colour monitor. Densitron Computers Ltd, Unit 4, Airport Trading Estate, Biggin Hill, Kent TN16 3BW. Tel: 0959 76331.

...and an x-y recorder/plotter from Brown-Boveri

New to the UK is an eight-colour, flat-bed recorder that combines analogue x-y format recording and digital plotting capabilities. It is a 0.1% linearity, universal recording instrument suitable for applications in industry, research and education. Called the 5E780, it has been designed to be easy to use, with an inclined plotting surface. It has a potentiometric, digital d.c. servo system with non-contact optical positioning for accuracy, claimed to be maintenance-free by the makers. Traces may be made with up to eight colours on sheet paper, roll paper or overhead transparencies in A3, A4 or ANSI A and B format, horizontal or vertical.

An RS232C interface allows the recorder to be connected to a computer and function as a stand-alone, eight-colour digital plotter. Computer data may be plotted with analogue curves and alphanumeric information combined on the same chart. The recorder has a maximum drawing area of 380 by 280mm with switchable, electronic drawing area limits and has remote-control functions: hold, frame advance,



pulse input, pen lift and chart hold.

Input modules may be exchanged to suit the application. There are currently two available: one with 18 calibrated ranges (0.05mV/cm to 20V/cm) and zero suppression, and one with nine calibrated ranges (0.5mV/cm to 1V/cm). Both have variable adjustment for intermediate ranges.

The x or y axis may be switched to timebase operation with 12 selectable sweep speeds (0.05 to 200s/cm). Pen acceleration on both axes is approximately 5g, slewing speed 1m/s, accuracy 0.3%. British Brown-Boveri Ltd, Normelec Division, Grovelands House, Longford Road, Exhall, Coventry CV7 9ND. Tel: 0952 502000.

Computerizing p.c.b. design

A small modification to a p.c.b. board which would take two days to effect by manual tape methods now takes only two hours according to Fidelity plc, who earlier this year took delivery of a Wayne Kerr Datum Artworker 2000 p.c.b. designer. The company is currently in a major design phase of its new television range, involving the designing of several new tv chassis from scratch, and in addition has an on-going programme of minor modifications to existing designs.

This increase in workload was the reason behind the decision to computerize. "With only a small team of tv designers and layout artists", explained Derek Hayes, Fidelity's technical director, "we just could not cope. With the Artworker, not only do the layout artists work on it but also the designers, thus greatly improving productivity. With the library functions of pre-defined components, it is particularly easy-to-use and they enjoy working with it."

The Artworker has shown itself to be flexible. "Most of our work involves designing boards that use a mixture of digital and analogue,

which it copes with without any problems," says Hayes. "It is also good at prompting operators if they have forgotten to do something. One of the first designs came up with flashing prompts about all the points that were floating around unconnected, which puzzled us for a few moments until we realised that they were all test points!"

"We chose the Wayne Kerr Artworker because it offered us all the facilities that we required at a good price. An added benefit is that an Artworker is also used by the bureau to whom we send our overload work. We can send them a floppy disc with the schematic on it and they send it back either the finished job or finished to any stage we specify." For further information on the Artworker p.c.b. designer contact Wayne Kerr Datum Limited, Jenner Road, Fleming Way, Crawley, West Sussex. RH10 2GA. Tel: 0293 549011.

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AAZ13	0.30	ASZ20	4.50	BC184	0.11	BD238	0.35	BFW11	1.01	MJE371	1.05	OC29	4.40	TIP29A	0.35	ZTX531	0.20	2N2148	2.75	2N3904	0.10
AAZ15	0.30	ASZ21	4.75	BC212	0.11	BDX10	0.91	BFX84	0.28	MJE520	0.75	OC35	4.00	TIP30A	0.36	ZTX550	0.25	2N2218	0.32	2N3905	0.10
AAZ17	0.30	AUY10	3.50	BC213	0.11	BDX32	2.00	BFX85	0.28	MJ521	0.73	OC36	4.00	TIP31A	0.25	IN914	0.03	2N2219	0.32	2N3906	0.10
AC107	0.55	BA145	0.13	BC214	0.11	BDY10	2.40	BFX87	0.28	MJE2955	2.25	OC41	1.20	TIP32A	0.25	IN916	0.03	2N2220	0.22	2N305X	0.12
AC125	0.35	BA148	0.15	BC238	0.09	BDY20	4.50	BFX88	0.28	MJE3055	2.00	OC42	1.50	TIP33A	0.53	IN4001	0.04	2N2221	0.22	2N3959	0.20
AC126	0.35	BA154	0.06	BC239	0.09	BDY60	1.50	BFY50	0.28	MPF103	0.55	OC43	1.50	TIP34A	0.60	IN4002	0.04	2N2222	0.20	2N4060	0.12
AC127	0.40	BA155	0.11	BC301	0.36	BFY51	0.30	BFY51	0.28	MPF104	0.55	OC44	1.25	TIP41A	0.38	IN4003	0.04	2N2223	7.50	2N4061	0.12
AC128	0.35	BA156	0.06	BC303	0.36	BFY52	0.16	BFY52	0.28	MPF105	0.55	OC45	0.85	TIP42A	0.42	IN4004	0.04	2N2368	0.23	2N4062	0.15
AC141	0.35	BAW62	0.05	BC307	0.09	BFY53	0.19	BFY64	0.36	MPSA06	0.47	OC71	0.65	TIP110	0.10	IN4005	0.04	2N2369	0.23	2N4124	0.13
AC141K	0.45	BAX13	0.05	BC308	0.09	BFY54	0.17	BFY90	0.65	MPSA56	0.17	OC72	2.20	TIP117	0.45	IN4006	0.04	2N2484	0.23	2N4126	0.13
AC142	0.40	BAX16	0.06	BC327	0.09	BFY55	0.20	BSX19	0.27	MPSU01	1.11	OC73	1.45	TIP125	0.35	IN4007	0.05	2N2646	0.75	2N4248	0.25
AC142K	0.45	BC107	0.12	BC328	0.09	BFY56	0.20	BSX20	0.27	MPSU06	1.33	OC74	1.40	TIP130	0.45	IN4009	0.06	2N2904	0.30	2N4288	0.15
AC176	0.35	BC108	0.13	BC337	0.09	BFW16	0.35	BSX21	0.27	MPSU56	1.71	OC75	1.40	TIP131	0.45	IN4148	0.03	2N2904	0.30	2N4289	0.12
AC187	0.35	BC109	0.14	BC338	0.09	BFW17	0.30	BT106	1.65	NE555	0.45	OC76	1.60	TIP132	0.48	IN5300	0.10	2N2905	0.30	2N4400	0.12
AC188	0.35	BC113	0.12	BCY30	3.75	BFW30	0.45	BTY79 400R	3.00	NKT401	4.00	OC77	2.75	TIP135	0.45	IN5301	0.11	2N2906	0.22	2N4401	0.12
ACY17	2.25	BC114	0.12	BCY31	7.50	BFW31	0.30	BU205	1.20	NKT403	3.50	OC81	0.90	TIP137	0.48	IS44	0.04	2N2907	0.22	2N4402	0.12
ACY18	1.55	BC115	0.12	BCY32	3.75	BFW39	0.30	BU206	1.20	NKT404	4.00	OC81D	4.00	TIP140	0.85	IS200	0.10	2N2924	0.12	2N5457	0.45
ACY19	1.80	BC116	0.19	BCY33	2.50	BFW50	0.30	BU208	2.00	OA5	1.75	OC82	0.95	TIP141	0.85	IS921	0.12	2N2925	0.22	2N5458	0.40
ACY20	1.50	BC117	0.24	BCY34	2.50	BFW60	0.30	BY100	0.42	OA7	0.75	OC83	1.40	TIP142	0.85	2G301	1.00	2N2926	0.12	2N5459	0.40
ACY21	1.55	BC118	0.30	BCY40	3.00	BFW61	0.25	BY126	0.15	OA10	0.55	OC84	1.40	TIP2955	0.60	2G302	1.20	2N3053	0.30	2S017	16.00
ACY39	4.00	BC125	0.25	BCY42	0.32	BFW62	0.30	BY127	0.15	OA47	0.15	OC122	6.50	TIP29551	0.45	2G306	1.50	2N3054	0.55	2S019	25.00
AD149	1.00	BC126	0.25	BCY43	0.45	BFW63	0.30	BZX61	0.17	OA70	0.22	OC123	6.50	TIP3055	0.60	2N404	1.50	2N3055	0.70	2S024	35.00
AD161	0.50	BC135	0.18	BCY56	0.25	BFW64	0.30	Series		OA79	0.21	OC139	12.00	TIP30551	0.45	2N696	0.30	2N3440	0.60	2S025	35.00
AD162	0.60	BC136	0.18	BCY58	0.25	BFW65	0.30	BZY88	0.10	OA81	0.21	OC140	18.00	ZS140	0.25	2N697	0.36	2N3441	0.75	2S026	40.00
ADZ11	12.50	BC137	0.22	BCY70	0.21	BFW94	0.15	Series	◆	OA85	0.21	IC141	18.00	ZS170	0.21	2N698	0.38	2N3442	1.00	2S103	2.50
ADZ12	12.50	BC138	0.18	BCY71	0.21	BFW95	0.15	BZY91	8.00	OA90	0.08	OC170	4.40	ZS178	0.54	2N705	2.50	2N3641	5.00	2S302	5.50
AF106	0.60	BC147	0.12	BCY72	0.21	BFW96	0.15	Series		OA91	0.08	OC171	4.40	ZS272	0.23	2N706	0.20	2N3702	0.11	2S303	5.50
AF114	3.50	BC148	0.12	BCZ11	3.50	BFW97	0.15	BZY93	0.12	OA95	0.08	OC200	4.00	ZS278	0.57	2N708	0.22	2N3703	0.11	2S322	5.00
AF115	3.50	BC149	0.12	BD115	0.35	BF200	0.33	Series		OA200	0.15	OC201	5.50	ZTX107	0.12	2N930	0.25	2N3704	0.11	2S325	4.00
AF116	3.50	BC157	0.12	BD123	2.30	BF224	0.12	BZY95	1.64	OA202	0.15	OC202	5.50	ZTX108	0.12	2N1131	0.35	2N3705	0.11	2S701	12.50
AF117	4.00	BC158	0.13	BD124	2.50	BF241	0.12	Series		OA211	1.00	OC203	5.50	ZTX109	0.12	2N1132	0.35	2N3706	0.11	2S745A	1.75
AF118	3.50	BC159	0.12	BD131	0.42	BF244	0.35	BZY96	2.00	OAZ200	1.50	OC204	7.00	ZTX300	0.13	2N1302	1.80	2N3707	0.11	2S746A	1.75
AF117	4.00	BC159	0.12	BD132	0.42	BF257	0.30	Series		OAZ201	1.50	OC205	10.00	ZTX301	0.14	2N1303	0.90	2N3708	0.11		
AF139	0.55	BC167	0.10	BD135	0.27	BF258	0.30	CRS1 40	0.60	OAZ206	1.50	OC206	8.50	ZTX302	0.14	2N1304	3.00	2N3709	0.11		
AF186	0.75	BC170	0.09	BD136	0.27	BF259	0.30	CRS3 40	0.75	OAZ207	1.50	OC207	18.00	ZTX303	0.14	2N1305	3.00	2N3710	0.11		
AF239	0.65	BC171	0.11	BD137	0.30	BF336	0.30	CRS3 60	0.90	OC16	5.00	OC971	2.50	ZTX304	0.14	2N1306	3.00	2N3711	0.10		
AFZ11	3.75	BC172	0.09	BD138	0.30	BF337	0.30	GEX66	3.00	OC20	6.00	ORP12	2.40	ZTX311	0.13	2N1307	1.20	2N3771	1.20		
AFZ12	5.00	BC173	0.09	BD139	0.30	BF338	3.00	GEX541	6.50	OC22	4.50	R2008B	1.95	ZTX314	0.25	2N1308	4.00	2N3772	1.20		
ASY26	1.40	BC177	0.15	BD140	0.30	BFS21	4.00	GJ3M	3.00	OC23	10.00	R2009	2.25	ZTX500	0.14	2N1309	1.50	2N3773	1.60		
ASY27	1.00	BC178	0.28	BD144	2.00	BFS28	2.50	GM0378A	1.75	OC24	3.00	R2010B	2.00	ZTX501	0.14	2N1613	0.30	2N3819	0.50		
ASZ15	2.20	BC179	0.15	BD181	0.75	BFS61	0.30	KS100A	0.45	OC25	1.75	TIC44	0.48	ZTX502	0.14	2N1671	5.00	2N3820	0.60		

VALVES

A1834	9.00	E180CC	10.50	ET55	1.75	GS16	16.00	OB3	2.50	QU37	12.50	UCL82	1.75	4-125A	60.00	6CD6GA	5.00	12AV6	2.50	5552A	165.00
A2087	13.50	E180F	12.05	ET56	3.50	G11C	25.00	OC2	4.55	QV03-12	6.80	UCL83	2.75	4-250A	80.00	6CG7	2.50	12AW7	3.50	5642	9.00
A2134	17.50	E182CC	13.25	ET89	2.50	GU50	20.00	OC3	2.50	QV04-7	3.50	UF41	2.00	4-400A	87.00	6CH6	13.00	12AX7	1.75	5654	3.00
A2293	16.00	E186F	11.50	EY81	2.95	GU51	20.00	OC4	2.50	QV08-100		UF42	2.10	4B32	20.00	6CL6	13.75	12AY7A	4.00	5651	4.45
A2134	17.50	E188CC	8.91	EY82	6.37	GXU1	15.35	OD3	2.50		197.40	UF80	1.75	4C35	120.00	6CW4	8.00	12BH7A	3.50	5670	4.50
A2293	16.00	E280F	22.51	EY83	1.50	GXU2	30.00	OZ4	3.50	QY3-65	63.24	UF85	1.75	4CX250B	58.00	6D2	1.50	12BA6	2.50	5675	28.00
A2426	35.00	E283CC	12.00	EY84	2.50	GXU3	25.40	PC86	2.50	QY3-125	78.48	UF89	2.00	4CX350A	105.00	6D6	3.00	12BE6	2.50	5687	6.00
A2521	25.00	E288CC	17.50	EY85	5.99	GXU4	44.50	PC88	2.50	QY3-250	71.00	UL41	5.00	4X150A	60.00	6D06B	4.75	12BH7	2.75	5696	4.50
A2900	15.00	W810F	35.48	EY86	2.00	GXU50	20.00	PC97	1.75	QY4-400	87.20	UL84	1.75	4X150D	56.00	6F4A	3.00	12BY7	3.00	5718	7.50
A3343	45.00	EA52	32.25	EY88	2.00	GY501	3.00	PC900	1.75	QY5-500	208.00	UM80	2.00	5B254M	35.00	6F8G	2.50	12E1	20.00	5725	5.50
AZ31	2.75	EA76	2.50	EF184	2.00	GZ32	4.00	PCC84	1.50	QY5-3000A		UY41	2.25	5B255M	35.00	6EW6	2.25	12E1T	28.00	5726	11.37
AZ41	2.60	EABC80	1.25	EF804S	12.00	GZ33	4.75	PCC85	1.50		566.80	UY81	2.25	5C22	160.00	6F6	3.00	12E1	20.00	5727	7.05
BK348	114.90	EAC91	3.50	EF805S	15.00	GZ34	4.00	PCC88	2.00	QZ06-20	46.00	VLS631	15.00	5J100E	2500.00	6F23	1.60	13E1	170.00	5749	2.50
BK384	165.00	EAF42	2.50	EH90	1.75	GZ37	4.95	PCC89	1.75	R10	6.00	XG1-2500		5R1GY	5.50	6F28	1.60	19H5	47.50	5751	4.00
B309	58.00	EAF801	2.00	EK90	1.50	K161	5.00	PCC189	2.50	R17	3.00		100.00	5U4G	3.00	6F33	33.50	24B9	67.25	5763	4.50
BS810	60.00	EB41	4.00	EL32	2.50	K166	15.00	PCC805	1.60	R18	3.00	XG5-500	30.00	5U4GB	2.50	6H1	14.00	30C15	2.00	5814A	4.00
B15	58.95	EB91	1.50	EL33	4.00	K177 Gold		PCC806	1.60	R19	9.24	XG2-6400		5V4G	2.50	6H13N	2.75	30C17	2.00	5840	4.00
B117	185.00	EBC33	2.50	EL34MUL	1.00	Lion	12.00	PCF82	2.00	R20	2.50		165.00	5Y3GT	2.50	6H16	3.00	30C18	2.00	5842	12.00
B119	44.05	EBC41	2.50	EL36	2.50	K188 Gold		PCF86	2.00	RG3-1250	59.50	XR1-1600A		5Z3	4.00	6J4	5.50	30F5	1.60	5876A	31.50
B129	349.15	EBC81	1.50	EL41	2.80	Lion	20.00	PCF802	1.50	RG3-2501A	32.68		53.75	5Z4G	2.50	6J6	8.93	30FL1, 2	1.35	5879	5.00
B169	354.80	EBC90	1.25	EL42	2.50	K1W61	2.50	PCF86	2.50	RG3-3250	59.00	XR1-3200		5Z4GT	2.50	6J7	4.75	30FL12	1.80	5886	17.50
BT95	139.90	EBF80	1.50	EL60	18.50	K1W62	2.50	PCF87	2.00	RG4-1250	61.60		110.00	6-30L2	1.75	6JE6	7.50	30FL14	2.00	5963	2.50
CBL31	4.00	EBF83	1.75	EL81	5.25	K1W63	2.50	PCF200	3.25	RG4-3000	99.45	XR1-3200A		6AB4	1.75	6K4N	2.50	30L1	1.50	5965	3.50
CL33	4.00	EBF89	1.50	EL83	6.00	M8079	12.34	PCF201	3.25	RR3-2501	40.00	XR1-6400		6AB7	3.00	6K6GT	2.75	30L15	2.00	6005	2.25
CY31	3.00	EBL31	10.00	EL84	2.25	M8080	8.25	PCF801	2.50	RR3-1250	45.75		165.00	6AC7	3.00	6K7	3.00	30L17	2.00	6021	4.50
C1K	20.00	EC91	1.25	EL86	2.75	M8081	9.82	PCF802	2.50	SIE12	65.00	YD1120	395.00	6AH4A	4.25	6K8	3.00	30P4	2.50	6057	12.25
C3A	22.00	EC92	3.95	EL90	3.95	M8082	9.69	PCF805	1.70	S130	6.00	YD1240	586.00	6AG7	3.00	6KD6	8.00	30P19	2.50	6058	12.34
C3JA	22.00	EC92	1.75	EL95	7.39	M8083	10.25	PCF806	1.70	S130P	6.00	Z803U	35.00	6AH6	5.00	6L6G	5.00	30PL1	3.00	6059	6.00
DA41	25.00	EC175	462.00	EL95	2.00	M8091	10.43	PCF808	1.70	STV250-40	14.00	Z259	25.00	6AK5	5.99	6L6GA	3.50	30PL14	1.80	6061	6.00
DA42	18.70	ECC33	4.50	EL156	30.00	M8096	6.80	PCL82	2.00	STV280-80	21.00	ZM1001	8.00	6AK6	2.50	6L6GC	5.75	30PL15	1.80	6063	3.75
DAF91	1.75	ECC35	4.50	EL360	8.50	M8097	8.10	PCL83	3.00	SU41	5.00	ZM1020	9.00	6AL5	1.50	6L6GT	3.00	35W4	1.00	6064	10.25
DAF96	1.75	ECC40	5.50	EL500	3.00	M8098	8.15	PCL84	2.00	SU42	10.00	ZM1021	9.00	6AM4	4.00	6L7	2.50	50C4	1.50	6067	11.75
DET22	35.00	ECC81	1.75	EL509	10.00	M8099	8.00	PCL85	2.50	TD03-10	35.00	ZM1022	9.00	6AM5	9.69	6N2P	2.50	75B1	6.05	6072	6.00
DET24	49.00	ECC82	1.75	EL821	13.00	M8100	10.30	PCL86	2.50	TD03-10F	35.00	ZM1023	9.00	6AM6	6.02	6N3P	2.50	75C1	4.50	6080	14.00
DF91	1.75	ECC83	1.75	EL822	13.97	M8136	11.75	PCL805/85	2.50	TH5	50.00	ZM1040	19.36	6AN5	4.75	6N7	3.00	85A1	7.50	6097A	235.00
DF96	1.75	ECC84	1.75	EL1.80	25.00	M8137	12.25	PD500	6.00	TT21	37.50	ZM1041	16.66	6ANKA	3.50	6P25	4.00	85A2	6.45	6097B	280.00
DK91	1.75	ECC85	1.75	EM34	8.00	M8140	6.00	PE06-40N	45.00	TT22	37.50	ZM1042	17.77	6AQ5	3.25	6R1HB6KN8		90AG	20.00	6146A	12.00
DK92	2.00	ECC86	2.50	EM80	2.50	M8141	6.50	PFL200	2.50	TT100	60.00	1B3GT	3.00	6AS6	8.60		10.00	90AV	20.00	6146B	12.00
DK96	1.75	ECC88	3.50	EM81	2.50	M8142	8.75	PL36	2.50	TY2-125	76.75	1B24	25.00	6AS7G	8.75	6SA7	3.00	90C1	6.00	6159B	19.00
DL92	2.00	ECC89	2.00	EM84	2.00	M8144	7.50	PL81	2.75	TY4-400	88.20	1B35A	48.00	6AT6	1.25	6SC7	2.75	90CG	12.54	6189	9.50
DL94	1.75	ECC91	8.93	EM85	2.75	M8149	6.50	PL81A	2.00	TY4-500	124.26	1B63	64.00	6AU5GT	5.00	6SC7	2.50	90CV	15.45	6201	11.40
DL96	1.75	ECC189	2.10	EM87	2.50	M8161	9.35	PL82	1.50	TY5-500	272.50	1R5	1.75	6AU6	2.50	6SC7	2.75	92AG	20.00	6442	20.00
DLS10	14.00	ECC807	1.75	EN32	25.00	M8162	11.75	PL83	2.50	TY6-800	231.00	1S5	1.75	6AV5GA	4.50	6SH7	3.00	92AV	20.00	6550	8.00
DLS15	12.50	ECC808	5.00	EN91	6.50	M8163	8.25	PL84	2.00	TY6-5000A		1T4	1.75	6AV6	1.50	6SH7	3.00	95A1	8.45	6883B	12.50
DLS16	12.50	ECF80	1.50	EN92	6.96	M8190	5.00	PL508	5.50		632.20	2AS15	11.50	6AX5GT	3.00	6SJ7	3.25	150BA	6.50	6973	7.50
DLS19	12.50	ECF82	1.50	EY51	2.75	M8195	10.85	PL509	6.00	TY6-5000B		2C39A	60.00	6B7	3.25	6SK7	3.50	150B3	8.35	7025	4.50
DM70	2.00	ECF86	2.50	EY81	2.50	M8196	7.50	PL519	6.00		395.00	2C43	70.00	6B8	3.25	6SL7GT	3.00	150C2	3.25	7027A	8.00
DM71	2.00	ECH35	3.00	EY83	2.50	M8204	7.05	PL519	6.00	TY6-5000W		2D21	3.25	6BA6	1.50	6SN7GT	3.00	150C4	6.00	7551	6.25
DM160	4.75	ECH42	3.50	EY84	9.24	M8213	11.37	PL802	6.00		523.00	2E26	8.25	6BA7	5.00	6SQ7	3.30	211	35.00	7586	15.00
DY87	1.50	ECH81	3.00	EY86	1.75	M8223	6.00	PL802	6.00	TY7-6000A		2J42	98.00	6BA8A	4.00	6SR7	4.00	723AB	125.00	7587	23.00
DY802	1.50	ECH83	2.50	EY88	1.75	M8224	6.30	PY33	2.50		632.00	2J55	215.00	6BC4	4.00	6SS7	2.75	803	25.00	7609	56.00
E55L	50.00	ECH84	2.50	EY500A	3.00	M8225	4.50	PY81	1.50	TY7-6000W		2J70A	383.00	6BR6	1.50	6U8G	3.50	805	45.00	7868	6.00
E80CC	15.00	ECL80	1.50	EY802	1.75	M8248	14.03	PY82	1.50		579.00	2J70B	336.00	6BH6	2.50	6U8A	2.25	807	3.75	7895	12.00
E80CF	11.27	ECL81	1.75	EZ35	1.75	MD2001	115.00	PY83	1.25	LZ40	25.00	2K25	125.00	6BJ6	2.25	6V6GT	4.25	811A	18.33	8068	16.50
E80F	14.19	ECL82	1.50	EZ80	2.50	MU14	2.50	PY88	2.00	U18.20	3.50	3400Z	115.00	6BK7	3.50	6X4	3.00	812A	47.50	8136	2.50
E80L	13.73	ECL83	3.00	EZ81	2.50	MX119	55.00	PY500A	4.00	U19	13.75	3500Z	180.00	6BL6	125.00	6X5GT	1.75	813 valve	65.00	8417	6.00
E81CC	8.00	ECL84	1.50	EZ80	1.50	MX123	86.00	PY800	1.50	U25	2.50	3A5	3.00	6BL7GT	4.50	7B7	2.50	833A	193.16	8842	11.53
E81L	13.27	ECL85	1.50	EZ81	1.50	MX134	61.50	PY801	1.50	U26	2.50	3B24	11.00	6BM6	115.00	7C3	4.00	866A	35.03	18045	10.49
E82CC	8.10	ECL86	1.75	EZ90	3.00	MX151	18.30	QE45[1]	82.50	U37	12.00	3B28	15.35	6BN6	2.00	7C6	2.50	872A	20.00	18046	11.53
E83CC	8.40	EF37A	5.00	EW34/800	3.50	MX152	150.00	QQV02-6	38.00	UABC80	1.25	3B29	20.05	6BQ7A	3.50	7D7	4.00	931	6.00		
E86C	8.25	EF39	2.75	EW34/800	3.50	MX161	175.00	QQV03-10	36.25	UAF42	2.50	3B240M	17.50	6BR7	6.00	7R7	4.25	931A	18.50		
E88C	8.25	EF40	5.00	G1.371K	35.00	MX163	19.50	QQV03-20A		UB41	3.00	3B241M	17.50	6BR8A	3.50	7S7	3.50	1622	4.00	Tested	
E88CC	10.33	EF41	3.50	G551K	12.00	MX164	26.40		48.38	UBC41	2.25	3C23	25.00	6BS7	6.00	7Y4	2.25	1625	3.50	Ex-Equipment	
E90CC	9.47	EF42	4.50	G180 2KI	17.50	MX166	147.00	QQV06-40A		UBF89	1.50	3C24	12.00	6BW6	6.00	7Z4	2.00	2050	7.50	4CX250B 8.50	
E90F	9.90	EF50	2.50	G240 2D	12.00	MX168	57.00		46.00	UC84	1.75	3CX100A	47.00	6BW7	1.50	12AH8	5.00	4212E	300.00	CV Devices	
E91H	6.25	EF54	5.00	G400 1K	17.50	N78	15.00	QQV07-50		UC85	1.85	3E29	45.00	6BX7GT	5.00	12AT6	1.50	4212H	300.00	Large stocks	
E92CC	8.75	EF55	3.50	GN3	9.00	OA2	3.25		76.65	UCF80	2.00	3S4	2.00	6BZ6	2.75	12AT7	1.75	5544	110.00	Prices on	
E99F	9.10	EF80	1.75	GN4A	9.00	OA3	2.50	QQZ06-40A		UCH42	2.50	3V4	1.75	6C4	1.25	12AU6	2.50	5545	150.00	application	
E130L	18.50	EF83	4.00	GN4A	9.00	OA4	6.00		62.20	UCH81	2.50	4-65A	60.00	6CB6A	2.50	12AU7	1.75	5551A	120.00		

BASES

B7G Unskirted	0.40	2AP1	8.50	5CP1A	40.00	CRT sockets		7400	0.16	7416	0.48	7441	0.48	7484	0.72	74120	0.75	74159	1.75	LAA570	1.75
B7G Skirted	0.50	2BP1	9.00	5P15A	15.00	Prices on		7400	0.35	7417	0.48	7442	1.25	7486	0.54	74121	0.54	74160	1.20	LAA630	1.75
B9A Unskirted	0.40	3BP1	20.00	5UP7	25.00	application		7401	0.36	7420	0.48	7450	0.30	7490	0.72	74122	0.70	74172	4.00	LBA700	3.00
B9A Skirted	0.50	3DP1	5.00	DG7-5	63.32			6402	0.36	7422	0.36	7451	0.30	7491	0.36	74123	0.60	74173	0.72	LBA480Q	1.50
B9D	0.55	3EG1	10.00	DG7-31	58.07	I/C sockets		7403	0.36	7423	0.36	7453	0.30	7492	0.54	74125	0.60	74174	0.60	LBA520Q	1.50
Int Octal	0.40	3FP7	6.00	DG7-32	58.07	Texas		7404	0.42	7424	0.36	7454	0.30	7493	0.54	74126	0.54	74175	1.00	LBA530	1.50
Loctal	0.55	3GP1	6.00	DG7-36	65.00	low profile		7405	0.42	7427	0.36	7460	0.30	7494	0.78	74128	0.55	74176	1.00	LBA550Q	1.75
Navistor base	2.00	3JP1	8.00	DH3-91	56.83	8 pin	10p	7406	0.48	7428	0.36	7470	0.48	7495	0.65	74136	0.70	74180	1.48	LBA560Q	1.75
Valve screening		3JP2	8.00	DH7-11	113.12	14 pin	10p	7407	0.55	7430	0.36	7472	0.30	7496	0.60	74141	0.46	74190	1.20	LBA673	1.75
can all sizes	0.30	3JP7	10.00	VCR138	12.00	16 pin	10p	7408	0.36	7432	0.36	7473	0.48	7497	3.90	74143	1.30	74191	1.20	LBA700	1.50
		3KP1	15.00	VCR138A	12.50			7409	0.36	7433

NEW PRODUCTS
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Music synthesizer improved

A new version of the Music 500 (now the Music 5000) music synthesizer for the BBC micro adds many new facilities to turn what was rather user-unfriendly into a fully professional and easy-to-use system.

Based on its own programming language (Ample) and some clever hardware, the Music 5000 offers 16 channels, organized into up to eight voices, spread as required over the stereo output. Waveform and envelope synthesis is included as well as ring, frequency, amplitude and phase modulation to produce almost any sound as well as simulating 'real' musical instruments.

Ample is a language using nested words so that a whole program is ultimately contained in a single instruction. Its internal compilation is interactive, like Logo, and this allows words to be redefined in real time without recompiling the whole programme. The nucleus of Ample is now contained in a rom and there is a suite of utilities provided on disc which supplement the functions.

The staff editor, one of the disc utilities, enables the music to be entered directly on a musical staff presented on the screen, complete with clefs, key signatures, bar lines, dotted notes, tied notes etc. Above the staff is space to add instructions, such as changing the tempo, or mixture. Notes are entered, lengthened or shortened, dotted or tied by very simple cursor controls; it is not necessary to know the name of the note so it is quite straightforward to copy music from a score.

The real-time mixing desk, another module on the disc, presents a screen simulation of a mixing desk with faders, stereo pan pots, tempo and tuning controls, even a fast wind. This is also controlled by easy cursor manipulation and it is possible to substitute a different instrument or change the stereo position or tempo "on the fly" and listen to the effect immediately.

A number of waveforms, envelopes and instruments are already set up and by judicious substitution of a different preset wave or adding vibrato, channel frequency offset and/or modulation, it is possible to creat a great number of different sounds without needing to study harmonic synthesis of waveforms, although this is to be added later.

The system is designed to be modular and further hardware and software, including a keyboard, Midi-interface and other facilities are being developed.

The hardware, enclosed in a disc drive sized box, remains essentially the same as the Music 500, although there have been some timing changes to make the system compatible with the BBC Master series. It has its own power supply and needs to be plugged into a stereo amplifier (preferably of good quality) to hear the results. The system works on a sampling rate of 47kHz, which is faster than that used on compact disc. The complete system is available from the designers and manufacturers, Hybrid Technology, in Cambridge. Tel: 0223 316910.

Signal strength meter for tv signals

Built-in monitors are included in the Unaohm range of EP741 tv field strength meters, used typically for aerial and distribution cable installation. All the features necessary for detecting and evaluating tv signals are included: signal level measurement between 20 and 130μV, frequency readout on a four-digit l.c.d, zooming on the monitor for closer inspection of reflections or disturbances, band spectrum analysis around the tuned frequency with close-up inspection of selected parts of the spectrum, and monitoring of frame/line sync and chroma bursts.

Channels may be preset as on a tv receiver. Provisions are also made for voltage measurements on the devices associated with aerials and distribution systems. A sound output duplicates the field strength measurement so that it is possible to monitor the signal without looking at the instrument, while orienting an aerial, for example. The instrument operates on mains power or from an internal 12V rechargeable battery. An f.m. version adds the measurement of f.m. and i.f. signals and there is another model that can also decode teletext. Advid-Unaohm, 17A Mill Lane, Welwyn, Herts.

Microvoltmeter for a.c.

Audio measurements are the special province of the TM3B microvoltmeter which offers a wide frequency range and a variable bandwidth, high sensitivity and low noise level. The instrument has ranges from 15μV to 500V and +50dB, with an input impedance of 10MΩ in parallel with <20pF on ranges above 50mV. Maximum bandwidth is 1Hz to 3MHz but a pass band switch provides other ranges within that band. 1000h of use can be obtained from a PP9 battery; power unit for use on the mains is an optional extra. Levell Electronics Ltd, Moxon Street, Barnet, Herts EN5 5SD. Tel: 01-449 5028.

Synthesized receiver

A microprocessor-controlled synthesized receiver from Lowe Electronics seems to be the first British-made unit in its price range for many years. Coverage is continuous from 30kHz to 30MHz. Operating modes are a.m., s.s.b. and c.w.; an optional detector board adds f.m. plus a synchronous demodulator for enhanced a.m. reception. The design is intended to give excellent r.f. performance as well as convenience in use; image and spurious response rejection is said to be better than 80dB and dynamic range greater than 90dB at 50kHz from the tuned frequency (both intermodulation distortion and reciprocal mixing). Features include 30 battery-backed memories, a noise-blanker, four switchable filter bandwidths and a 400Hz audio filter for c.w. Further options are a plug-in keypad for frequency control and an internal nickel-cadmium battery pack, charger and active whip aerial for portable use. Basic price is £375. Lowe Electronics Ltd. Chesterfield Road, Matlock, Derbyshire DE4 5LE. Tel: Matlock 0629 2817.

Surface mount integrated circuit mixer

A symmetrical mixer i.c. designed by Siemens for receivers, converters and modulators of a.m. and f.m. signals with frequencies up to 200MHz is now packaged in a surface-mounted version, the TBB042G, supplied in a SO14 miniature package. In its new package, the TBB042G can still be operated either from an external source or via its internal oscillator, in line with the requests from manufacturers of radio sets and of all kinds of remote-controlled models; mobile radio is also an important future application area for the s.m.d mixer.

Unlike the dip package with pins through the printed-circuit board, the new s.m.d. mixer is suitable for double-sided board production. Samples of the TBB042G are available ex-stock. Siemens Ltd, Windmill Road, Sunbury-on-Thames, Middlesex TW16 7HS. Tel: 09327 85691.

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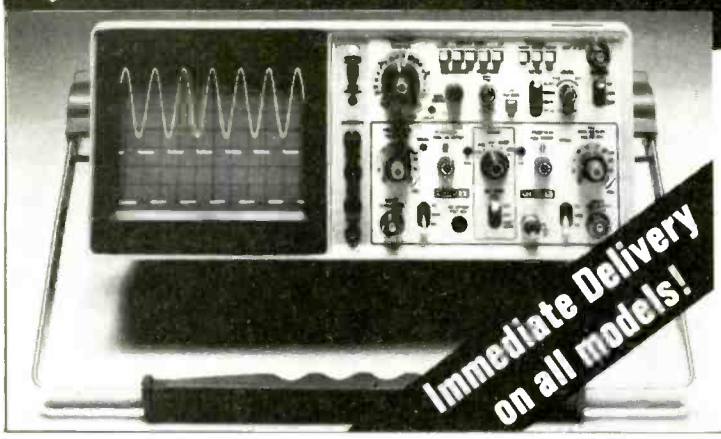

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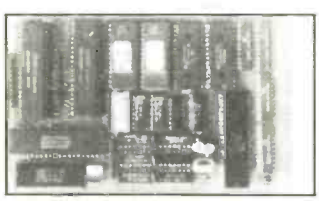

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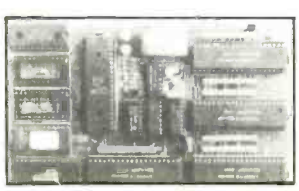

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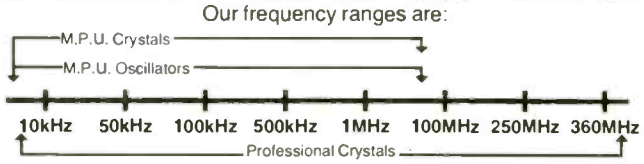

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Digital image processing — — an overview

Digital processing of images for data compression, enhancement or restoration – a major area of application of two-dimensional digital signal processing – has spurred a great deal of work in the analysis of random fields.

A. S. KWABWE, R. N. GORGUI-NAGUIB and R. A. KING

Digital images are frequently formed by spatially sampling points of a continuous image. The digital image representation consists of an array of numbers which correspond to the intensities of the image at the sampling points. Digitization involves sampling the grey level at each picture point and quantizing it so that it takes on open of the number of specified values in the range of grey levels. Both the number of samples taken within a given scene and the number of levels to which they are quantized affect the quality of the digital image.

The question of the optimum number of sample points and the quantization strategy are often addressed in terms of the sampling theorem and optimum quantization.

Given a bandlimited picture, one can determine a sampling strategy in the picture plane and an interpolation function such that the picture is reconstructed without error from the samples.

The sampling theorem indicates that samples taken at rate of $f_c/2$, where f_c is the spectrum of the image, would be sufficient. Considering the images as samples of a random field, one can find an optimum set of orthonormal functions that minimize the sampling error. Such a set is the Karhunen-Loeve function for the minimum mean square error (m.m.s.e.) criterion.

Optimum quantization was developed by Max[1]. For a given number of output levels, the values of the output levels and the decision levels are determined such that the mean square quantization error is minimum. For an optimum quantizer, the decision levels are located midway between the output levels. Thus an array consisting of element intensities is produced from the analogue image.

There are perhaps more interesting ways of forming a digital image that do not involve sampling an existing analogue image. In the various modalities of tomographic scanning, for instance, a digital image is reconstructed from projections taken at various angles of an object.

In the majority of applications of interest, the digital image array is then processed by a digital computer and another array of numbers is produced to reconstruct a continuous image for viewing.

The processing may take a variety of forms depending on the requirements of the application at hand, and these are reviewed in this article.

Image processing may consist of coding, filtering, enhancement, restortation, analysis and recognition of images, and it is important to differentiate between the various terminologies that are used in this area. For the purposes of this overview, the term image processing refers to operations that transform images into other images. This must be differentiated from image recognition which maps images into non-image descriptions, and from computer graphics which deal with the computer synthesis and manipulation of images that are specified by descriptions. Apart from digital methods, there are other analogue methods of image processing – optical for example – but these are not covered here.

IMAGE CODING REDUCES STORAGE

Digital image coding, or compression, is performed to minimize the number of bits that are needed to represent or transmit an image without introducing visually unacceptable degradation.

The need for compression may be illustrated by considering a standard digital television image: for acceptable representation, one normally needs 512 by 512 samples per frame, each quantized to 50 grey levels. This is a total of approximately six bits per sample or 1.5 million bits in all.

Obviously, if no compression is performed, an overwhelmingly number of bits would be needed to send tv images, imposing an unacceptable burden on the transmitting network.

The other major application for image coding is in image storage. With the increasing use of images in remote sensing, astronomy, atmospheric physics and medicine, large amounts of image data are being acquired and it is essential that the amount of storage used be minimized.

Many different approaches to image coding have been extensively investigated, but a few general classes are outlined here. The first step is to decorrelate the data. One basic approach is to apply an invertable transform to the given image, approximate the transform by truncating some of its components and then reconstruct the image by inverting the transform. The transform is designed so that it can be approximated using many

Fig.3. Cosine transform coding performed at 0.8bit/element (c) results in a better image quality reconstruction than predictive coding at 1bit/element (b). Original image at (a).

fewer bits and minimizing the errors in the reconstructed image when it is inverted. Compression ratios of up to 10:1 can be achieved using transform coding and a variety of orthogonal transforms can be used[2] e.g., Fourier, Hadamard, Harr, KLT.

The forward unitary transform of an N_1 by N_2 image array $f(n_1,n_2)$ is an N_1 by N_2 transformed array defined by

$$F(m_1,m_2)=\sum_{n_1}\sum_{n_2} f(n_1,n_2)A(n_1,n_2,m_1,m_2),$$

where $A(n_1,n_2,m_1,m_2)$ is the forward transform kernel. The inverse transformation from the transform domain to the space domain is defined by

$$f(n_1,n_2)=\sum_{m_1}\sum_{m_2} F(m_1,m_2)B(n_1,n_2,m_1,m_2),$$

where B is the inverse transform kernel. A and B must satisfy orthonormality conditions.

Unitary transforms have found wide application in image processing, particularly in image coding. They provide a spectral decomposition of an image into coefficients that tend to isolate certain features of this image. For example, the first spectral component is proportional to average image brightness and the higher sequency components are measures of the image edge content. The block diagram of a transform coding system is illustrated in Fig.1.

Another approach is predictive coding. Here, rather than coding the image samples, one codes the difference between successive image samples.

Because successive picture elements are correlated, it is possible to form a prediction $\bar{f}(m,n)$ for a given element, $f(m,n)$, in terms of the rest of the image. The difference

$$e(m,n)=f(m,n)-\bar{f}(m,n)$$

is the estimation error for that element, and is also called the differential signal.

Although the best estimate (for m.m.s.e. criteria) is generally a non-linear function of the elements, a simple linear estimate is often used in which the prediction is formed as

$$\bar{f}=a_1f(m-1,n)+a_2f(m-1,n-1)+a_3f(m,n-1)$$

where the coefficients a_1, a_2 and a_3 are chosen to minimize $e(m,n)$ and are functions of the autocorrelation. Compression is achieved because quantizing and coding the differential data rather than the pixel intensities requires fewer bits.

Because of the high correlation between successive samples, the differences will have a non-uniform probability density, peaked at zero (see Fig.2) and thus can be quantized acceptably using relatively few quantization levels. The differences may be either spatial (intraframe coding) or temporal (interframe coding) and further compression may be obtained by making the process adaptive.

A difficulty with this approach to compression is that when the image is reconstructed from the differences by summing them any errors will tend to propagate causing an unwanted distortion.

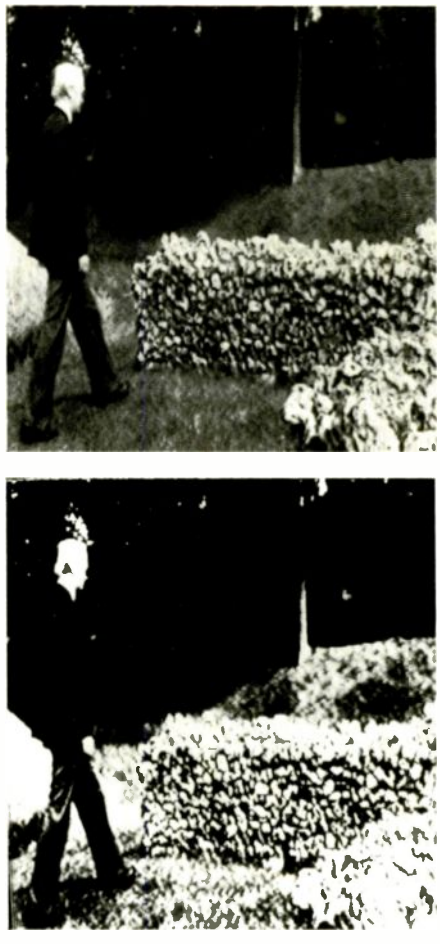

Fig.4. Histograms equalization can be used to enhance an image, remove blurring or correct geometric distortions. Applied to image (a) it results in subjective improvement (b).

Fig.3 shows transform and predictively coded images at various bit rates.

Another group of techniques combines the two classes discussed above. In hybrid coding, the image is first transformed, then adaptive prediction is applied to the transform samples. Here, the image quality obtained is often superior to that obtained from either predictive or transform methods.

Many other coding techniques, such as contour coding, run-length coding, interpolative coding, have been proposed and used. Some of the more interesting coding problems being addressed today consider motion estimation and compensation techniques for coding of moving-image sequences. There are interframe techniques that try to exploit the redundancy between consecutive frames and only code the moving parts of the frames.

Immediate applications of such techniques are found in videoconferencing where there is extremely little movement between frames, but good motion estimation techniques should widen their applicabilities to more general scenes.

Also, to evaluate or compare different coding methods, one has to determine the least number of bits needed to represent a picture, given that a certain level of distortion is acceptable, or conversely if only a certain bandwidth (number of bits) is available, which of the above techniques would minimize distortion?

In principle, this question is answered through rate distortion theory[3], which deals in quantitative terms with the concept of redundancy and its reduction to achieve data compression. A measure of the degradation is also needed as well as a model of the image being encoded.

Rate distortion theory may be used to derive absolute bounds for relatively simple error criteria and image models which may be useful, but it does not provide simple solutions when non-stationary image sources or complex error criteria are assumed.

IMAGE ENHANCEMENT

Some enhancement techniques – operations on the image that give rise to a subjectively improved image – are conceptually very simple and only involved pointwise modifications of the image. For example, one can analyse the grey levels in the neighbourhood of each image point, determine a grey-scale transformation that stretches these levels over the full display range, then apply this transformation to a given point. The class of histogram modification techniques based on this principle include equalization and hyperbolization techniques.

Image enhancement is often necessary when the observed image has undergone degradation, impairment or is the output of some processing system.

The methods discussed in this section differ from those of image restoration (see later) in the sense that they are basically simpler and do not explicitly attempt to model the degradation process or estimate the actual error, but only apply modifications to the given intensities to produce an image free of some general degradations.

The enhancement methods generally include contrast enhancement, smoothing, deblurring and correcting geometric distortion. The simplest enhancement techniques, however, involve grey-level correction where any obvious errors in the intensities are corrected: for example, any negative element intensities may be set to zero and any intensities outside the range of display set to the maximum intensity.

Another contrast enhancement technique, useful when some knowledge is available about the nature of the degradation function, is grey-level modification. For example, if a function of the non-uniform illumination under which the image was formed is known, then intensities may be modified to cancel the effect of the function.

If we assume that a function $p(z)$ denotes the relative frequency with which grey-level z occurs in f for all z in the grey-level range $[z_1,z_k]$ of f, then $p(z)$ is the histogram of the image and it can provide useful information about how to segment f into parts and as a basis for measuring certain textural properties of f.

In other words, histogram modification transforms a picture's grey scale so as to give the picture a specified histogram. Such modification may be useful if the type of histogram for a good image in that class is

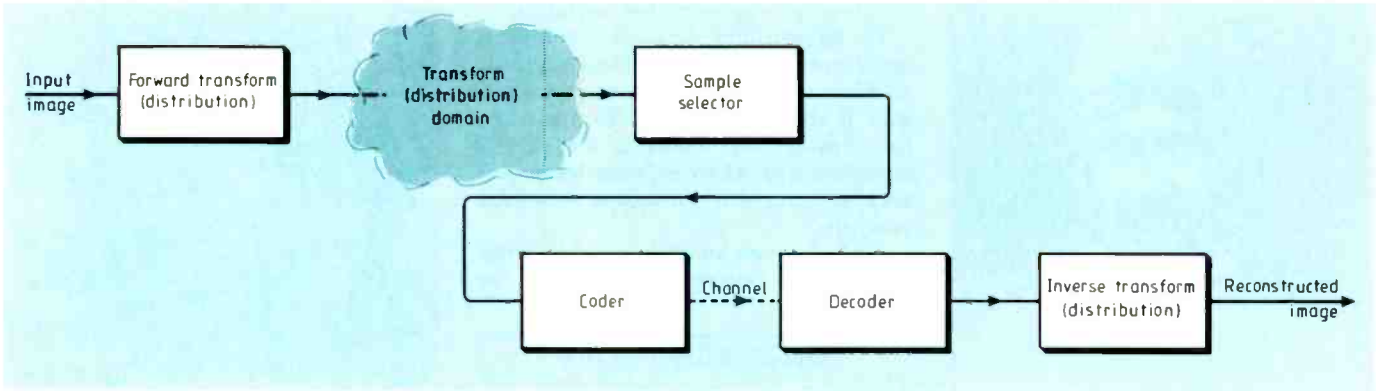

Fig.1. In a transform coding system, input image is first transformed from the time domain to the transform domain representation. Samples from this domain are then selected and coded for channel transmission. At the receiving end, reverse operations take place, i.e. decoding the samples and inverse transforming them reproduces the reconstructed image.

known. Given an image with poor contrast, it can be transformed into one with the same histogram as a 'good' image.

A commonly used technique to achieve this purpose is histogram equalization, where the new element intensity is

$$x(m,n)=\frac{K-1}{M\times N}\sum_{i=1}^{k} o_i$$

where M×N is the dimension of the image array, K is the total number of levels to which the image is quantized, k is the old intensity of the element at location (m,n) and o_i is the number of elements that have a given intensity i (i.e., a bar of the histogram).

Averaging and smoothing operations generally lead to a more pleasing image if the original image has high frequency noise, whose effect is then reduced by smoothing.

Gradient and differential operators may also be used for sharpening or edge enhancement (discussed later). Fig.4 shows some enhancement results on images.

IMAGE RESTORATION

There are also image operations that differ from enhancement by the fact that they operate on degraded images and attempt to undo the effects of degradation. A model of the degradation is essential and it is customary to use additive combinations of blurring and noise operations, where the blurring is a weighted sum or integral operation applied to the image and the noise is uncorrelated with the ideal image.

There are a number of approaches to image restoration[4]. Pseudoinverse techniques define a blurring operator that yields the best approximation to the ideal image in the expected least squares sense. Kalman filtering yields least squares estimates of an ideal image corrupted by additive noise.

Closely related to image restoration is image reconstruction. For example, three-dimensional objects can be reconstructed from sets of projections, e.g. x-rays taken at many angles. Much work has recently been done in this area.

Even though the image formation process is often non-linear, the model below is valid in many cases:

$$g(x,y)=\int h(x,y,x',y')f(x',y')dx'dy'+U(x,y)$$

where f(x,y) is the input image, g(x,y) the degraded image, h(x,y,x',y') a degradation function, also called the point spread function (p.s.f.), and U(x,y) the random noise that may be present.

This model is much simplified and real life degradations which are much more complex require modifications to the above model.

The simplest restoration method is inverse filtering which is aimed at removing the effects of blur and ignores the presence of additive noise. The estimate $\bar{f}(x,y)$ is

$$\bar{f}(x,y)=h^{-1}(x,y)*g(x,y)$$

where $h^{-1}(x,y)$ is the inverse of the degrading factor. However, h(x,y) may not be invertable or it may have zeros at which the calculation would be impossible.

Another method explicitly takes the presence of additive noise into the account and tries to find an estimate that minimizes the noise. This is the Wiener filter and it chooses the estimate as the minimum mean square estimate:

$$\min_{\bar{f}}\left[(f-\bar{f})^{T}E(f-\bar{f})\right]$$

Here, x^T denotes the transpose of x and E(x) is its expected value. If we assume gaussian statistics, the optimal estimate is obtained as

$$\bar{f}=PH^{T}(HPH^{T}+R)^{-1}.g$$

where $P=E(ff^{T})$, $R=E(uu^{T})$ and $E(fu^{T})=0$.

The Wiener filter overcomes some of the problems of the inverse filter but is not optimum because it is based on the m.m.s.e. criterion which is not well suited to the way the human visual system works. Stationarity assumptions made to make the calculation

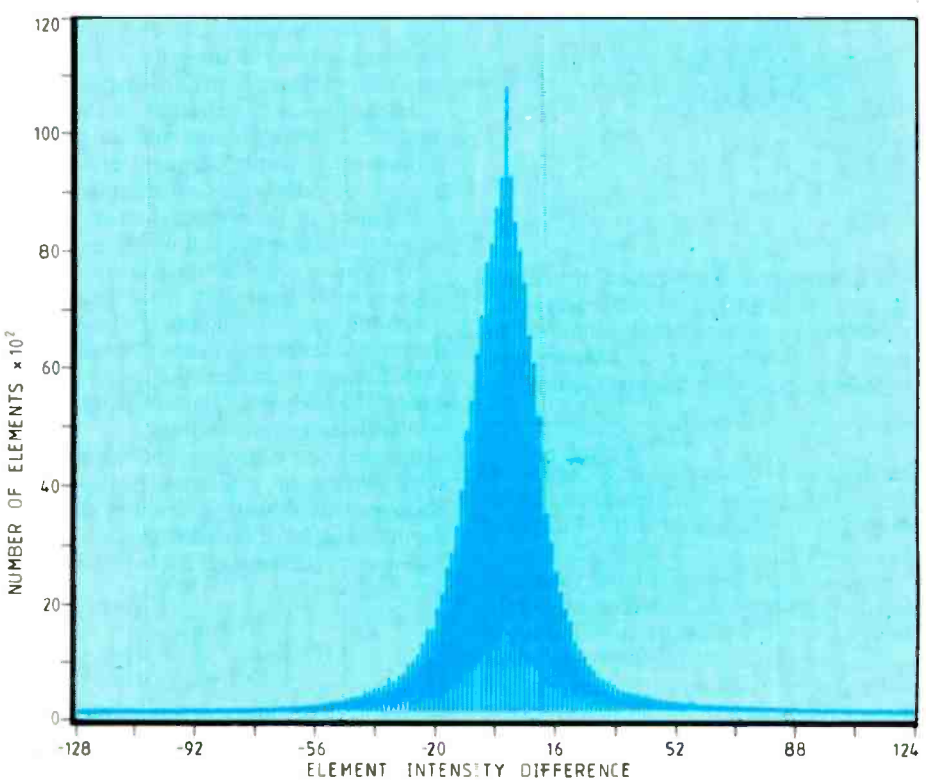

Fig.2. Histogram of difference signal for a predictive codec shows probability density is maximum at zero element intensity difference, and that successive samples result, overall, in a non-uniform probability density.

Fig.5. Intentional degradation of image at (a) by linear blurring (b) tests subsequent restoration by Wiener restoration (c). Image is filtered using a Wiener filter, regaining some of its original features.

feasible lead to the smoothing of edges and the reduction of contrast. It also requires too much *apriori* information on the statistics of the image.

Several other schemes have been proposed to overcome the short comings of the Wiener filter. These include the constrained least squares filter, parametric Wiener filter and a geometric mean of the inverse and Wiener filters. These are all non-recursive methods with high computational burden.

Research emphasis is now geared toward developing recursive restoration methods with computational advantages. The drawback is that more complex models are needed and, in this respect, several methods have been proposed using Kalman filters.

The Kalman filter, like the Wiener filter, is based on m.m.s.e. criteria and consequently sacrifices resolution for noise suppression as well. It also requires a detailed model and may not be very robust in the case of modelling error. However, many techniques have been proposed to overcome both these problems.

Fig.5 shows an image degraded by linear blur and its restored version using the Wiener filter.

IMAGE SEGMENTATION

Images are often composed of regions that have different values of some chosen property. For example, the regions may have a different range of grey-level intensities or a different texture, and in many applications, such as robotics, machine vision and pattern recognition, it is frequently necessary to process a given image or scene and delineate these regions.

An image may be segmented by examining its grey-level (or local property) histogram for the presence of peaks corresponding to the ranges, and using thresholds to single out individual regions.

If an image consists of an object and background, then its histogram will be bimodal and a threshold selected between the peaks of the histogram will segment the image into object and background. This is a conceptually simple segmentation method but unfortunately it is not always successful because many image histograms are not bimodal and some images are taken under such illumination conditions that a single global threshold is not sufficient for good segmentation.

More elaborate thresholding schemes are available such as multiple threshold selection, where the histograms and thresholds are computed over sections of the image that have reasonably uniform illumination.

Segmentation by selection of single or multiple thresholds does not in general perform satisfactorily. Some better methods use an edge-detection or enhancement technique prior to threshold selection, which gives a stronger region definition.

While many edge-detection operators and segmentation algorithms have been proposed and used, they do not always give all the essential edge information that correctly characterizes the image. Most useful techniques give a fairly good approximation to the ideal with few extraneous lines.

Simple linear edge-detection methods involve performing a discrete spatial differentation. Often this is achieved by convolving the image function with a gradient mask. A commonly used mask is the Laplacian edge detector:

$$H=\begin{vmatrix} 0 & -1 & 0 \\ -1 & 4 & -1 \\ 0 & -1 & 0 \end{vmatrix}$$

The new image intensity g(x,y) is formed from the image f(x,y) by using this mask to give.

$$g(x,y)=f(x+1,y)+f(x-1,y)+f(x,y+1)+f(x,y-1)-4f(x,y).$$

Other masks may be used to give maximum response to a preferred direction.

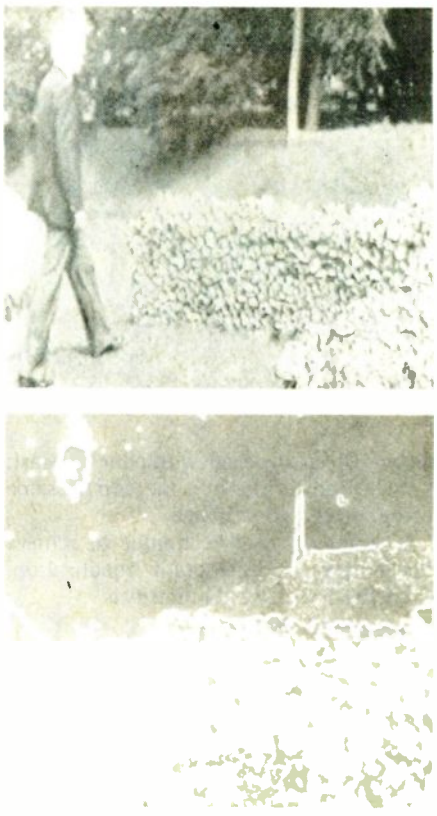

Fig.6. Image resulting from edge enhancement and thresholding techniques has sharpened edges (b). Though not ideal, the edge information gives a good approximation to the edge characteristics of the image (a).

Non-linear edge detection systems use non-linear combinations of elements over a limited window area. Examples include Roberts' operator where the edge image is formed by cross-operations:

$$g(x,y)=\{[f(x,y)-f(x,y+1)]^2+[f(x,y+1)-f(x+1,y)]^2\}^{1/2}.$$

Other operators that have been widely used include Sobel, Kirsh and the Marr-Hildreth operators.

Segmentation of texture differs from intensity segmentation because texture edges sometimes occur where there is no marked change in intensity level and thus would not be detected by grey-level methods.

Several methods have been used both to measure texture quantitatively and for segmenting texture images. Quantitative texture measures include Fourier spectrum texture analysis, spatial autocorrelation functions and co-occurence matrices.

The use of region growing with split and merge algorithms has been found to give some reasonable segmentation when suitable texture measures are used to determine whether regions should be merged or not. Fig.6 shows some results using these methods.

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RADIO BROADCAST

H.f. quarts in pint bottles?

Pressure for an early World Administrative Radio Conference to revise frequency allocations, particularly within the h.f. spectrum of 3 to 30 MHz, is likely to surface at the WARC for the planning of the h.f. bands allocated to the broadcasting service, of which the second session is currently being held at Geneva.

The prime purpose of this session is to agree the plan of using a central computer at Geneva to allocate or share channels within the bands agreed at the 1979 WARC, at which h.f. broadcasters failed to gain access to several bands of frequencies they sought, although a number of extensions were agreed. Since then the continued expansion of external broadcasting, the installation of more powerful transmitters and the constraints of the present sunspot-minimum period, plus the world-wide effects of the mainly-Russian jammers, have caused further serious congestion and increase of interference to h.f. broadcasting.

In a recent broadcast, John Tusa, managing director of BBC external broadcasting, emphasized that this year's meetings cannot make new frequencies available for broadcasting, but added: "I think this session may concentrate people's minds on the need to expand frequencies".

He emphasized that there is no point in making programmes if "reception is so bad that it needs dedicated or skilled listeners to tune into them".

At the first session of the h.f. broadcasting WARC, an elaborate central planning system of the International Frequency Board was proposed and the current session has as one of its main tasks to agree whether to adopt this system.

It is clear, however, that theoretical studies have resulted in serious doubts whether the IFRB planning procedures can deliver the required service areas. It is considered that interference levels would be so high that only part of the designated service areas would receive satisfactory service. EBU special groups concluded last year that "It is believed that results achieved in practice are not as poor as those suggested by theoretical analysis. While the fundamental reasons for this need to be examined further, one implication is that an automated planning system (based on the same propagation prediction model and the same protection ratios) which is required to give service areas which are protected, can only do so by suspending a large number of the notified requirements."

In other words, even the most optimistic forecasts show that with so many countries now attempting to provide virtually world coverage by means of relay bases and with multiple transmissions of the same broadcasts on more than the two frequencies recommended by the ITU to cope with variations in h.f. propagation, h.f. broadcasting seems set to degrade still further under the sheer power and number of transmissions. While broadcasters see this as a reason for expansion of their bands, other spectrum users are likely to suggest that h.f. broadcasting needs first to put its own house in order, to modify its demands for world coverage and to reach agreement on the reduction, if not elimination, of intentional jamming.

Pre-echo problems

Although distribution of programmes to overseas h.f. relay bases has brought about a major improvement in technical quality, it has given rise in a few areas to serious degradation in the form of a pre-echo, with the main signal received a fraction of a second after a weaker echo signal. It has been found that this form of interference is even more disturbing than a conventional echo signal.

The situation arises where a distant overseas base, for example Singapore, uses the same channel as a UK transmitter not intended to serve the same area (for example parts of the Indian sub-continent) but nevertheless on some days providing an audible signal in the target area of the Singapore transmitter radiating a signal delayed by the satellite distribution link.

The problem can be largely overcome by artificially delaying the UK transmission, but the BBC have been reluctant to adopt this technique since it would destroy the accuracy of the time signals from its UK transmitters. But would it not be possible to arrange automatically to switch out the delay for the time pips?

Waiting for R-DAT?

Most consumer electronics appear on the market at the earliest possible opportunity – indeed, it could be argued that too much of it appears well in advance of the system being standardized, with consequent format rivalry.

This is clearly not the case with digital tape recordings for which an agreed specification, R-DAT (Rotary-head Digital Audio Tape-recorder) has been in existence for some time. Meanwhile the consumer industry rumbles with forebodings or denials of forebodings about the potential impact of digital tape on the compact disc industry, particularly since the CD boom has been suffering its first slight upset following increases in the price of the discs. The cost of software is seen by the trade as having more influence on the long-term future of audio recordings than the hardware costs. The availability of a tape system that could produce excellent quality recordings off-air or from CD discs – and subsequently re-used for different material – is worrying to an industry that has seen the lack of enthusiasm for high-quality video discs in the face of the video cassette tape recorder. Much money has been invested in setting up plants for the production of CD records. Yet in practice, disc and tape have existed together for many years and there is no proof that R-DAT machines would kill the CD industry or even seriously dent its growth.

Details of the basic R-DAT specification and performance measurements made using metal particle and Ba-ferrite particle tapes have been presented by Toshiba engineers in *IEEE Transactions on Consumer Electronics*, November 1986, pages 707 to 712.

Pirate interference

Although illegal broadcasting in the UK fell away dramatically during the period when it was anticipated that community radio licenses would soon be authorized, by the end of 1986 many pirate stations were active again not only in the London area but in the provinces and in Northern Ireland. The Government has not rushed to publish its Green Paper on the future of radio broadcasting, originally promised by the end of 1986, although some inspired leaks have emerged. From these it appears that what may have been published by the time these notes appear will be more in the nature of election manifesto promises than intended for immediate action. It also seems doubtful whether the proposals will do much to encourage quality broadcasting but rather lean heavily on market forces in pursuit of what appears to be a long-term aim of cutting BBC and Independent Radio down to size.

It is often claimed by the pirates that they cause little or no interference to the authorized broadcast services. This is not, however, the experience of those of us living anywhere near to "pirate alley" located on the high ground of the Crystal Palace area. For some months it has become increasingly difficult to receive Radio 3 programmes from Wrotham without at least some splatter (and often capture) by the strong local signals of one or more pirate stations.

It was widely assumed that the extra powers given to the Radio Investigation Service of the DTI by the Telecommunications Act would serve as a powerful deterrent to pirates, few of whom can be realistically considered as falling within the scope of what many of us conceived as true community radio, for which a strong case can be made.

Radio Broadcast was written by PAT HAWKINS.

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7453	0.38
7454	0.38
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7484A	1.25
7485	1.10
7486	0.42
7489	2.10
7490A	0.55
7491	0.70
7492A	0.70
7493A	0.55
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74170	2.00
74172	4.20
74173	1.40
74174	1.10
74175	1.05
74176	1.00
74178	1.50
74179	1.50
74180	1.00
74181	3.40
74182	1.40
74184	1.80
74185A	1.80
74190	1.30
74191	1.30
74192	1.10
74193	1.15
74194	1.10
74195	0.80
74196	1.30
74197	1.10
74198	2.20
74199	2.20
74221	1.10
74251	1.00
74259	1.50
74265	0.80
74273	2.00
74276	1.40
74278	1.70
74279	0.90
74283	1.05
74742	3.20
74290	0.90
74293	0.90
74298	1.80
74351	2.00
74365A	0.80
74366A	0.80
74367A	0.80
74376	1.60
74390	1.10
74393	1.20
74490	1.40

74LS SERIES

74LS00	0.24
74LS01	0.24
74LS02	0.24
74LS03	0.24
74LS04	0.24
74LS05	0.24
74LS08	0.24
74LS09	0.24
74LS10	0.24
74LS11	0.24
74LS13	0.34
74LS14	0.50
74LS15	0.24
74LS20	0.24
74LS21	0.24
74LS22	0.24
74LS24	0.50
74LS26	0.26
74LS27	0.24
74LS28	0.24
74LS30	0.24
74LS32	0.24
74LS33	0.24
74LS37	0.24
74LS38	0.24
74LS40	0.24
74LS42	0.50
74LS43	1.50
74LS48	0.90
74LS49	1.00
74LS51	0.24
74LS54	0.24
74LS55	0.24
74LS73A	0.30
74LS74A	0.35
74LS75	0.45
74LS76A	0.36
74LS78	0.42
74LS83A	0.70
74LS85	0.75
74LS86	0.35
74LS90	0.48
74LS91	0.90
74LS92	0.35
74LS93	0.54
74LS95B	0.75
74LS96	0.90
74LS107	0.40
74LS109	0.40
74LS112	0.45
74LS113	0.45
74LS114	0.45
74LS122	0.70
74LS123	0.80
74LS125	0.50
74LS126	0.50
74LS132	0.65
74LS133	0.55
74LS136	0.45
74LS138	0.55
74LS139	0.55
74LS145	0.95
74LS147	1.75
74LS148	1.40
74LS151	0.65
74LS152	2.00
74LS153	0.65
74LS154	1.60
74LS155	0.65
74LS156	0.65
74LS157	0.50
74LS158	0.65
74LS160A	0.65
74LS161A	0.75
74LS162A	0.75
74LS163A	0.75
74LS164	0.75
74LS165A	1.10
74LS166A	1.50
74LS168	1.30
74LS169	1.00
74LS170	1.40
74LS173A	1.00
74LS174	0.75
74LS175	0.75
74LS181	2.00
74LS183	1.90
74LS190	0.75
74LS191	0.75
74LS192	0.80
74LS194A	0.75
74LS195A	0.75
74LS196	0.80
74LS197	0.80
74LS221	0.90
74LS240	0.80
74LS241	0.80
74LS242	0.90
74LS243	0.90
74LS244	0.70
74LS245	0.90
74LS247	1.10
74LS248	1.10
74LS249	1.10
74LS251	0.75
74LS253	0.75
74LS256	0.90
74LS257A	0.70
74LS258A	0.70
74LS259	1.20
74LS260	0.75
74LS266	0.60
74LS273	1.25
74LS279	0.70
74LS280	1.90
74LS283	0.80
74LS290	0.80
74LS292	14.00
74LS293	0.80
74LS295	1.40
74LS297	14.00
74LS298	1.00
74LS299	2.20
74LS321	3.70
74LS322A	3.90
74LS323	3.00
74LS324	3.20
74LS348	2.00
74LS352	1.20
74LS353	1.20
74LS356	2.10
74LS363	1.80
74LS364	1.80
74LS365	0.50
74LS366	0.50
74LS367	0.52
74LS368	0.50
74LS373	0.70
74LS374	0.70
74LS375	0.75
74LS377	1.30
74LS378	0.95
74LS379	1.30
74LS381	4.50
74LS385	3.25
74LS390	0.60
74LS393	1.00
74LS395A	1.00
74LS399	1.40
74LS445	1.80
74LS465	1.20
74LS467	1.20
74LS490	1.50
74LS540	1.00
74LS541	1.00
74LS608	7.00
74LS610	25.00
74LS612	25.00
74LS624	3.50
74LS626	2.25
74LS628	2.25
74LS629	1.25
74LS640	2.00
74LS640-1	3.00
74LS641	1.50
74LS642	2.50
74LS642-1	3.00
74LS643	2.50
74LS643-1	3.00
74LS644	3.50
74LS645	2.00
74LS645-1	4.00
74LS668	0.90
74LS669	0.90
74LS670	1.70
74LS682	2.50
74LS683	3.00
74LS684	3.50
74LS687	3.50
74LS688	3.50
74LS783	16.00

74S SERIES

74S00	0.50
74S02	0.50
74S04	0.50
74S05	0.50
74S08	0.50
74S10	0.50
74S11	0.75
74S20	0.50
74S22	0.50
74S30	0.50
74S32	0.60
74S37	0.60
74S38	0.60
74S40	0.50
74S51	0.60
74S64	0.45
74S74	0.70
74S85	1.50
74S86	1.00
74S112	1.50
74S113	1.20
74S114	2.00
74S124	3.00
74S132	1.00
74S133	0.60
74S138	1.80
74S139	1.80
74S140	1.00
74S151	1.50
74S153	1.50
74S157	2.00
74S158	2.00
74S163	3.00
74S169	5.50
74S174	3.00
74S175	3.20
74S188	1.80
74S189	1.80
74S194	3.00
74S195	3.00
74S196	3.50
74S200	4.50
74S201	3.20
74S225	5.20
74S240	4.00
74S241	4.00
74S244	4.00
74S251	2.50
74S257	2.50
74S258	2.50
74S260	1.00
74S261	3.00
74S283	2.70
74S287	2.25
74S288	2.00
74S289	2.25
74S299	4.50
74S373	4.00
74S374	4.00
74S387	2.25

74C SERIES

74C00	0.70
74C04	0.50
74C08	0.70
74C10	0.70
74C14	0.50
74C20	0.70
74C32	1.00
74C42	1.50
74C48	1.50
74C73	1.00
74C74	1.20
74C76	1.00
74C83	2.00
74C85	2.25
74C86	0.50
74C90	1.90
74C93	1.50
74C95	1.60
74C107	1.00
74C150	5.00
74C151	2.00
74C157	2.50
74C160	1.80
74C161	1.80
74C162	1.80
74C163	1.80
74C173	1.00
74C174	1.50
74C175	1.50
74C193	1.50
74C194	1.50
74C195	1.50
74C221	2.50
74C244	2.00
74C245	2.25
74C373	2.25
74C374	2.25
74C902	1.20
74C911	9.00
74C912	4.50
74C922	6.00
74C923	6.50
74C925	6.50
74C926	7.50

74ALS SERIES

74ALS00	0.45
74ALS02	0.45
74ALS04	0.50
74ALS08	0.50
74ALS10	0.45
74ALS20	0.45
74ALS32	0.45
74ALS74	0.70
74ALS138	1.50
74ALS139	1.50
74ALS244	4.00
74ALS245	4.75
74ALS573	2.60
74ALS574	4.50
74ALS580	2.60

4000 SERIES

4000	0.20
4001	0.24
4002	0.25
4006	0.70
4007	0.25
4008	0.60
4009	0.45
4010	0.60
4011	0.24
4012	0.25
4013	0.36
4014	0.60
4015	0.70
4016	0.36
4017	0.55
4018	0.60
4019	0.60
4020	0.80
4021	0.60
4022	0.70
4023	0.30
4024	0.48
4025	0.24
4026	0.90
4027	0.40
4028	0.60
4029	0.75
4030	0.35
4031	1.25
4032	1.00
4033	1.25
4034	2.50
4035	0.70
4036	2.50
4037	1.10
4038	1.00
4040	0.60
4041	0.55
4042	0.50
4043	0.60
4044	0.60
4045	1.00
4046	0.60
4047	0.60
4048	0.55
4049	0.36
4050	0.35
4051	0.65
4052	0.60
4053	0.60
4054	0.80
4055	0.80
4056	0.85
4060	0.70
4063	0.85
4066	0.40
4067	2.30
4068	0.25
4069	0.24
4070	0.24
4071	0.24
4072	0.24
4073	0.24
4075	0.24
4076	0.65
4077	0.25
4078	0.25
4081	0.24
4082	0.25
4085	0.60
4086	0.75
4089	1.20
4093	0.35
4094	0.90
4095	0.95
4096	0.90
4097	2.70
4098	0.75
4099	0.99
4501	0.36
4502	0.55
4503	0.36
4504	0.95
4505	3.60
4506	0.90
4507	0.35
4508	1.20
4510	0.55
4511	0.55
4512	0.55
4513	1.50
4514	1.10
4515	1.10
4516	0.55
4517	2.20
4518	0.48
4519	0.32
4520	0.60
4521	1.15
4522	0.80
4526	0.70
4527	0.80
4528	0.65
4529	1.00
4531	0.75
4532	0.65
4534	3.80
4536	2.50
4538	0.75
4539	0.75
4541	0.90
4543	0.70
4551	1.00
4553	2.40
4555	0.36
4556	0.50
4557	2.40
4560	1.40
4566	1.40
4568	2.40
4569	1.70
4572	0.45
4583	0.90
4584	0.48
4585	0.60
4724	1.50
14411	7.50
14412	7.50
14416	3.00
14419	2.60
14490	4.20
14495	4.50
145000	6.50
14599	2.00
22100	3.50
22101	7.00
22102	7.00
40014	0.48
40085	1.20
40097	0.36
40098	0.40
40100	1.50
40101	1.25
40102	1.30
40103	2.00
40104	1.20
40105	1.50
40106	0.48
40107	0.55
40108	3.20
40109	0.80
40110	2.25
40114	2.25
40147	2.80
40163	1.00
40173	1.20
40174	1.00
40175	1.00
40192	1.00
40193	1.00
40194	1.00
40244	1.50
40245	1.50
40257	1.80
40373	1.80
40374	1.80
80C95	0.75
80C97	0.75
80C98	0.75

LINEAR ICs

AD7581	12.00
ADC0808	11.90
AM79100C	25.00
AN103	2.00
AN-1-5050	1.00
AY-3-1350	3.50
AY-3-8910	4.90
AY-3-8912	5.00
CA3019A	1.00
CA3020	3.50
CA3028A	1.10
CA3046	0.70
CA3059	3.25
CA3060	3.50
CA3080E	0.70
CA3085	1.50
CA3086	0.60
CA3089E	2.50
CA3090AQ	3.75
CA3130E	0.90
CA3130T	1.30
CA3140E	0.45
CA3140T	1.00
CA3146	2.25
CA3160E	1.50
CA3161E	2.00
CA3162E	6.00
CA3189E	2.70
CA3240E	1.50
CA3280G	3.00
D7002	6.00
DAC1408-8	3.00
DAC0800	3.00
DAC0808	3.00
DG308	3.00
HA1366	1.90
ICL7106	6.75
ICL7611	0.95
ICL7650	4.00
ICL7660	2.50
ICL8038	4.00
ICM7555	0.90
ICM7556	1.40
LC7120	3.00
LC7130	3.00
LC7131	3.50
LC7137	3.50
LF347	1.20
LF351	0.60
LF353	0.90
LF355	0.90
LF356N	1.10
LF357	1.00
LF398	4.00
LM10CLH	4.50
LM301A	0.30
LM307	0.45
LM308CN	0.75
LM310	2.25
LM311	0.60
LM318	1.50
LM319	1.80
LM324	0.45
LM334Z	1.15
LM335Z	1.30
LM336	1.60
LM339	0.40
LM348	0.60
LM358P	0.50
LM377	3.00
LM380N-8	1.50
LM380N	1.50
LM381AN	1.70
LM381N	3.00
LM383	3.25
LM384	2.20
LM386N-1	1.00
LM387	2.70
LM391	1.80
LM392N	1.10
LM393	0.85
LM394CH	4.00
LM709	0.35
LM710	0.48
LM711	1.00
LM723	0.60
LM725CN	3.00
LM733	0.65
LM741	0.22
LM7747	0.70
LM748	0.30
LM1011	4.80
LM1014	1.50
LM1801	3.00
LM1830	2.50
LM1871	3.00
LM1872	3.00
LM1886	6.00
LM1889	4.50
LM2917	3.00
LM3302	0.90
LM3900	0.80
LM3909	1.00
LM3911	1.80
LM3914	3.50
LM3915	3.40
LM3916	3.40
LM13600	1.50
M51513L	2.30
M51516L	4.50
MB3712	2.00
MC1310P	1.50
1413	0.75
MC1458	0.45
MC1495	3.00
MC1496	0.70
MC3340P	2.00
MC3401	0.70
MC3403	0.65
MF10CN	4.10
MK50240	9.00
ML902	5.00
ML922	4.00
MM6221A	3.00
NE529	2.20
NE531	1.20
NE544	1.90
NE555	0.22
NE556	0.60
NE564	4.00
NE565	1.20
NE566	1.50
NE567	1.25
NE570	4.00
NE571	3.00
NE592	0.90
NE5532P	1.50
NE5533P	1.60
NE5534P	1.20
NE5534AP	1.50
OP-07EP	3.50
PLL02A	5.00
RC4136	0.55
RC4151	2.00
RC4195	1.50
RC4558	0.55
S50240	9.00
SAA1900	16.00
SFF96364	8.00
SL490	3.00
SN76013N	3.00
SN76023N	3.00
SN76033N	3.00
SN76115N	2.15
SN76489	4.00
SN76495	4.00
SN76660	1.20
SPO256AL2	7.00
SP8515	7.50
TA7120	1.20
TA7130	1.40
TA7204	1.50
TA7205	0.90
TA7222	1.50
TA7310	1.50
TBA231	1.20
TBA800	0.80
TBA810	0.90
TBA20	0.80
TBA820M	0.75
TBA920	2.00
TBA950	2.25
TC9109	5.00
TCA210	3.50
TCA220	3.50
TCA270	3.50
TCA940	1.75
TDA1004A	5.00
TDA1010	2.25
TDA1022	4.50
TDA1024	1.10
TDA1170S	3.00
TDA2002	3.25
TDA2003	1.90
TDA2004	2.40
TDA2006	3.20
TDA2020	3.20
TDA2030	2.50
TDA2593	5.00
TDA2653	7.00
TDA3560	7.50
TDA3810	7.50
TDA7000	3.50
TEA1002	7.00
TL061CP	0.40
TL062	0.60
TL064	0.90
TL071	0.40
TL072	0.70
TL074	1.10
TL081	0.35
TL082	0.55
TL083	0.75
TL084	1.00
TL094	2.00
TL430C	1.20
UA759	3.20
UA2240	1.20
UCN5801A	6.00
ULN2001A	0.75
ULN2002A	0.75
ULN2003A	0.75
ULN2004A	0.75
ULN2068	2.90
ULN2802	1.90
ULN2803	1.80
ULN2804	1.90
UPC575	2.75
UPC592H	2.00
UPC1156H	3.00
UPC1185H	5.00
XR210	4.00
XR2206	4.50
XR2207	3.75
XR2211	5.75
XR2216	6.75
XR2240	1.20
ZN404	1.00
ZN414	0.80
ZN419P	1.75
ZN423E	1.30
ZN424E	1.30
ZN425E8	3.50
ZN426E8	3.00
ZN427E8	6.00
ZN428E8	4.50
ZN429E8	2.25
ZN447E	9.00
ZN448	7.50
ZN449E	3.00
ZN450E	7.50
ZN459CP	3.00
ZN1034E	2.00
ZNA1040	6.60
ZNA134H	23.00
ZNA234E	9.50

VOLTAGE REGULATORS

1A FIXED VOLTAGE PLASTIC TO220

+VE		−VE	
5V 7805	0.45	7905	0.50
6V 7806	0.50	7906	0.50
6V 7808	0.50	7908	0.50
12V 7812	0.45	7912	0.50
15V 7815	0.50	7915	0.15
18V 7818	0.50	7918	0.50
24V 7824	0.50	7924	0.50

1A FIXED VOLTAGE PLASTIC TO92

5V 78L05	0.30	5V 79L05	0.45
6V 78L06	0.30	12V 79L12	0.50
8V 78L08	0.30	15V 79L15	0.50
12V 78L12	0.30		
15V 78L15	0.30		

OTHER REGULATORS

FIXED REGULATORS

LM309K	1A 5V	1.40
LM323K	3A 5V	3.50
78H05KC	3A 5V	5.40
78H12	5A 12V	6.40
78P05	10A 5V	9.00

VARIABLE REGULATORS

LM305AH		2.50
LM317T	TO-220	1.20
LM317K	TO3	2.40
LM337T		2.25
LM350T	10A+VAR	4.00
LM396K	10A+VAR	15.00
LM723N		0.50
78HH05KC	5A 5V	5.75
78HGKC	5A+VAR	6.50
78GUIC	1A+VAR	2.25
79HGKC	5A−VAR	6.75
79GUIC	1A−VAR	2.50

SWITCHING REGULATIONS

ICL7660	2.50
SG3524	3.00
TL494	3.00
TL497	2.25
78S40	2.50
RC4195	1.50

COMPUTER COMPONENTS

CPU

1802CE	6.50
2650A	10.50
6502	4.50
65C02-2MHz	12.00
6502A	6.50
6502B	8.00
6800	2.50
6802	3.00
6809	6.50
6809E	10.00
68B09	10.00
68B09E	12.00
68000-L8	36.00
8035	3.50
80C35	6.00
8039	4.20
80C39	7.00
8080A	7.40
8085A	3.00
80C85A	7.50
8086	22.00
8087-5	£120
8087-8	£160
8088	17.50
8741	15.00
8748	12.00
TMS1601	12.00
TMS9980	14.50
TMS9995	18.00
V20-8	10.00
V30-8	10.00
Z80	2.50
Z80A	2.90
Z80B	5.50
Z80H	7.50

SUPPORT DEVICES

2651	12.00
2816-30	20.00
3242	8.00
3245	4.50
6520	3.00
6522	3.50
6522A	5.50
6532	4.80
6551A	6.00
6821	
68B21	2.50
6829	12.50
6840	3.75
68B40	6.00
6850	1.80
68B50	2.50
6852	2.50
6854	6.50
68B54	8.00
6875	5.00
8154	8.50
8155	3.80
8156	3.80
8205	2.25
8212	2.00
8216	1.60
8224	P.O.A
8226	4.25
8228	5.50
8243	2.60
8250	12.00
8251A	3.25
8253C-5	3.50
8255AC-5	3.20
8256	18.00
8257C-5	54.00
8259C-5	4.00
8275	29.00
8279C-5	4.80
8282	4.00
8284	4.60
8287	3.80
8288D	6.50
8755A	16.00

OTHERS

2N5777	0.50
BPX25	1.80
BPW21	2.80
OCP71	1.80
ORP12	1.20
ORP60	1.20
ORP61	1.20
SFH205	1.00
TIL32	0.55
TIL78	0.55
TIL31B	1.20
TIL81	1.20
TIL100	0.75

DISPLAYS

FND357	1.00
FND500/TIL730	1.00
FND507/TIL729	1.00
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TELEVISION BROADCAST

Educating video discs

Despite the good technical quality of video discs, the system, lacking the record facility of video tape cassette machines, has so far proved to have little appeal to consumers. The disc, however, may well come into its own as a means of storing enormous amounts of visual information as, for example, in the much-publicized "Domesday Project" and in interactive form as a powerful educational tool.

It is, admittedly, always dangerous to predict the influence of new technology on the world of talk and chalk. Closed-circuit television and computer-aided instruction have never had the predicted impact on education. Computer-aided instruction using conventional graphics presented on a visual display unit has proved too often too impersonal to hold the attention of students. But interactive laser-disc systems are being promoted as offering the "action" of recorded television images with the interactive facilities of computer-aided instruction, thus marrying the emotional and visual power of television to the speed and intelligence of the computer.

A small US company, Interactive Medical Communications, headed by Jim Mason, a former writer and broadcaster, has recently landed a contract to provide an interactive video disc educational system to provide health and safety training for the 400,000 employees at the 147 sites of General Motors.

The workforce is being trained to appreciate safety hazards by using a touch screen to answer questions as they watch training videos. The system has been designed to keep track of each worker's progress by assessing incorrectly answered questions and generating individual review programmes. Each worker-student thus trains individually at his or her own pace and without being intimidated or ridiculed when failing to answer questions correctly. The system comprises a colour touch-screen monitor, a microcomputer with 640K of random-access memory and a 20-megabit hard disc together with a laser-disc player. The branching software used to create revision programmes adapted to progress can generate up to a million paths.

The company stresses that it is essential to produce the video disc material to a standard comparable to television broadcasting, claiming it took eight months to develop the single disc plus software for the General Motors health and safety training programme.

A similar training system designed to reduce adult illiteracy has also recently been developed by IBM, who claim that it is achieving very positive test results.

Pirate decoders

The introduction last year of scrambling of programmes distributed by satellite to US cable systems decimated for a time the sale of the 4 GHz C-band television receivers to home viewers. There are now upwards of eleven channels using the Video Cipher II system and General Instruments have received some 75,000 orders for decoders, authorized through the programme companies, outstripping production. Another 20 channels are also planning to adopt Video Cipher II.

However it is clear that a number of viewers are endeavouring to avoid paying programme subscription charges with a number of companies now marketing "pirate" decoders or offering to modify decoders to permit owners to receive additional programmes without payment. The Video Cipher marketing people claim this to be "in violation of state and federal law".

The number of subscribers to UK cable systems is now approaching 200,000 although a large majority of these are still connected to the older "upgraded" systems offering a limited choice of channels. With the number of "homes passed" by cable now over 1,100,000 the ratio of subscribers to homes passed fell slightly, from 16.4% to 15.9%, in the third quarter of 1986. The Cable Authority has reminded Ulster Cablevision and Merseyside Cablevision, both of which were in the first batch of franchises awarded more than three years ago, reminding them that the Authority could not be expected to wait indefinitely for construction to begin.

Amer-connector standardized

The US Electronic Industries Association has standardized a 20-plus-shield pin connector for providing a baseband (audio/video) interface between NTSC television receivers and peripheral devices. The socket is basically similar to the Peritel/SCART Euro-connector now being fitted to an increasing number of PAL and SECAM receivers but with some differences in the facilities provided and in the connections to the pin numbers. Known as the EIA IS-15 Amer-connector, the concept is similar to Peritel in Europe and is seen as providing an interface for pay-tv descramblers etc., as well as improving the technical quality available from video cassette recorders by elimination of the modulator/demodulator degradation.

Cable television has had a significant impact on American receiver design with some 40-million "cable-ready" receivers sold and with about two-thirds of current production in this category. As in Europe there has been a growing need for a standard interface permitting connection of RGB and/or composite video and to facilitate the use of a television receiver with an external stereo audio system.

As noted in the February issue, it has been proving extremely difficult to establish teletext services on either American broadcast or cable channels. But, in addition to the "Electra" teletext service described in that issue, there is at least one cable "subscription" teletext service providing a "Market Watch" service of financial information and available on the Financial News Network channel on cable. This is run by the Data Broadcasting Corporation of Vienna, Virginia, a firm founded in 1984 by the Financial News Network and Biotech Capital Corporation but currently having the large Merrill Lynch brokerage house as majority owner.

Market Watch promises virtually instant quotations on either 8000 stocks and indices or on up to 20,000 stocks, indices and options at a cost to the subscriber ranging from about £25 to £60 per month. However, it faces strong competition from various Radio Teletext (SCA) services of financial information carried on v.h.f./f.m. broadcast stations as planned to be introduced shortly in the London area.

Market Watch was launched in April 1986 and is currently carried on about 120 cable systems that serve about 2.5 million homes with its basic channels. But by December 1986 there were just 650 subscribers to the Market Watch teletext service mostly located in Texas, Florida and California, and the company are not predicting at what rate it hopes to penetrate an "emerging market".

The IBA offer of a three-channel d.b.s. contract last December to BSB did not include any satellite teletext services for which a separate contract was advertised last April. This attracted an application from Direct Business Satellite Systems Ltd who proposed a broadcast message service, akin to electronic mail, but intended for multiple addresses in the form of text, data, facsimile or digitized voice. DBSS have commented that the satellite-teletext award is subject to further discussion of the regulatory position which at present appears to forbid "closed user group services" but permits encrypted subscription teletext. It would seem that there requires to be a more precise definition of where the line needs to be drawn by existing legislation between a broadcast service and a telecommunications service.

Television Broadcast was written by PAT HAWKER.

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74LS21	20p	74LS240	70p
74LS30	20p	74LS241	70p
74LS32	20p	74LS244	70p
74LS51	20p	74LS245	70p
74LS74	20p	74LS257	70p
74LS86	20p	74LS273	70p
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RADIO COMMUNICATIONS

Telecomms privacy

The attempt by the US government to strengthen the privacy of American telecommunications services by the introduction of a new "Electronic Communication Privacy Act" appears to have ground to a halt, at least for the time being. This is despite the fact that a watered-down version of the original draft Bill, as S-2575, received unanimous approval from the Senate Judiciary Committee last September. But when S-2575 reached the Senate in October, it failed to be sent for ratification by President Reagan. This means that S-2575 automatically died on December 31, 1986 in accordance with US legislatory practice. Unless a new Bill is introduced to Congress and is duly ratified, protection against unauthorized interception of radio traffic or telephone "bugging" looks like being constrained only by the relatively mild provisions of such current legislation as the Communications Act, 1934.

This Act does not attempt to forbid listening to radio transmissions of any services or on any frequencies although it does, nominally, make it an offence to use or disclose the contents of private messages.

Strong opposition to the US Electronics Communications Privacy Act, even in its modified form, came from various bodies representing short-wave listeners and enthusiasts and by radio amateurs on the grounds that it gave the US Government the right to forbid listening on frequencies not used for broadcasting or other public services.

Various amendments exclude from the original provisions the monitoring of any radio communications "for the use of the general public" or those "readily accessible to the public" thus excluding cordless telephones, marine and aircraft transmissions, law enforcement and public safety services (police and fire services) etc.

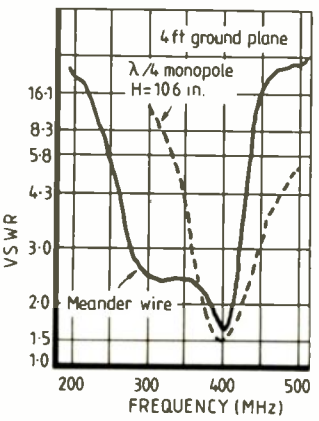
In this watered-down version it was expected to become law during 1986 but was strongly attacked by the influential columnist Jack Anderson as being motivated in support of the cellular radio-telephone industry.

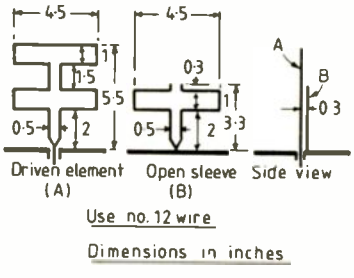
In the UK, the privacy provisions of the Wireless Telegraphy Acts were further strengthened last year by the "Interception of Communications Act" which came into force in April 1986, making it a criminal offence for any person intentionally to intercept a communication in the course of its transmission by post or by means of a public telecommunications system. While no prosecutions under this Act have been reported, three men were charged during January 1987 following the discovery in November 1986 of a listening device buried in a tin and connected to the telephone line of a director of the electrical and electronics firm Comet. The charge in this case (conspiring to intercept a public communication) was brought under the 1977 Criminal Law Act.

Compact broadband monopole

The use of very closely spaced parasitic elements as "open sleeves" in order to extend the bandwidth of driven antenna elements has been used in a number of novel designs in recent years. Jimmy Wong and Howard King of The Aerospace Corporation have described (*IEEE Transactions on Antennas & Propagation*, May 1986, pages 716-7) a height-reduced meander zigzag broadband monopole developed to provide a performance comparable with a quarter-wave monopole but over a 3:1 bandwidth and with a height of under one-third wavelength at the lowest operating frequency.

Their report describes results achieved with model wire antennas constructed from 12-gauge wire and with measurements made over the range 250 to 750 MHz. The antenna is being developed for use in a spacecraft operating over 50 to 80 MHz and 100 to 150 MHz. It has been shown that, with proper choice of design parameters, this antenna could be operated over two widely-separated bands with little or no change in pattern performance relative to a resonant quarter-wave monopole. The diagram shows the v.s.w.r. of a meander monopole with one open sleeve. For dual-band operation, two open sleeves are used. The single-band model operates over 250 to 400 MHz and is compared with the bandwidth performance of a 7.06-in straight-wire monopole with a resonant frequency of approximately 400 MHz. Overall height of the meander is 5.5 inches (a tuned monopole for 250 MHz would require a wire length of about 11 inches). Measured radiation patterns were presented showing that when a meander monopole is used in conjunction with a suitable ground plane the basic quarter-wave monopole pattern characteristics are preserved over a 3:1 frequency range.





Military packets

The possibility of using "packet-radio" systems for military communications networks, including battlefield "Combat Net Radio", has been under active investigation by a team of the Royal Signals and Radar Establishment since 1983, when a decision was made to build a five-station research demonstration system. Military mobile systems pose particular problems in having to be able to operate in hostile electromagnetic environments resulting from enemy jamming and the siting of units behind hills out of sight of the enemy.

The RSRE demonstration system has initially been based only on single-address "acknowledged datagram" service and unlike more recent civilian and US military systems does not use an ISO "open system interconnection" model. Instead it has been based on the US Defence Advanced Research Project Agency (DARPA) internet architecture, transmission control and internet protocols for end-to-end and internet working respectively. More recently the US Department of Defense has endorsed ISO rather than DARPA standards. However the RSRE team has been able to link their demonstration equipment into the US ARPANET and also demonstrated to NATO in 1985 interworking with a more complex US Packet Radio demonstrator.

Digital transmission with packet-switching techniques is possible with current military v.h.f. transceivers such as the Clansman VRC353, Jaguar V and Scimitar V in conjunction with an RSRE in-house data interface unit and DEC-11/33 station processor with RSRE software.

The RSRE is continuing design work with a view to developing a production specification model. There is need to refine the demonstration system by providing protection against spoofing and additional interfaces, gateways, etc, for services other than single-address datagrams.

In a recent report on RSRE work on Packet Radio, Major R.D.M. Graham and Dr B.H. Davies noted that future use of this technique for a variety of roles in military communications raises a number of important management issues that have not yet been tackled, in particular the question of achieving the necessary degree of operational control within a mix of battlefield systems co-ordinated by senior communication managers and their staffs.

Radio Broadcast was compiled by PAT HAWKER.

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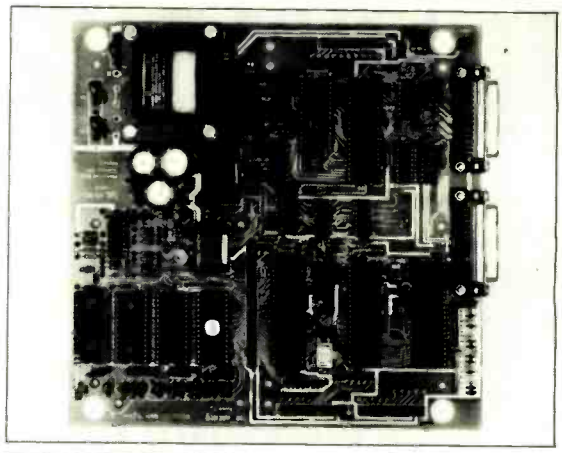

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Editorial Feature List

APRIL 1987

Spectrum analysers. Advances in the application of microprocessors to these instruments have opened up a wider area of use. Those on the UK market are listed and new techniques examined.

MAY 1987

Logic analysers are clearly indispensable to those working on digital system design or maintenance. This feature presents the characteristics of those available and discusses applications.

JUNE 1987

Batteries. Recent developments in battery design mean that an investigation into the new types available is needed. We discuss design and applications and characterize the types on the market.

For further advertising details please ring Ashley Wallis on: 661 8641

68020 display processing

With its large address range and some special instructions, the 68020 can control text and graphics display as well as performing the main processing tasks.

DAVID BURNS AND DAVID JONES

Although the 68020 is a 32-bit general-purpose microprocessor, it can handle many more specific tasks, such as display processing. In bit-mapped displays for example, using this processor can remove the need for a dedicated controller.

Advantages of using the 68020 for display processing are threefold. The device has a linear address space large enough to allow a substantial display bit plane to be implemented in memory. Secondly, this display memory is addressed contiguously from least-significant to most-significant locations, i.e. the bit plane is stored in memory as it will be represented on a terminal. Thirdly, the processor has a group of instructions which is specially designed for addressing and manipulating individual bits or blocks of memory.

Take for example a simple text processing application. Typically, characters are stored in rom and each character is defined as a matrix of, say, eight-by-twelve dots, Fig. 1. Looking after a system like this is not too difficult. Each character is a single byte wide, assuming one bit per pixel, and so movement of these elements is easy provided that the base address of the character rom is known.

In this situation, the character set could be stored contiguously in rom so that each character falls on a 12-byte boundary. You can see from the following example of pseudo code that extraction of the character could simply involve moving bytes by automatically incrementing addresses.

```
FOR D0 EQ #0 TO #11 D0
  MOVE.B letter B(A0)+,D0*linesize(A1)
ENDF
```

In this example, the variable 'letterB' indicates the byte offset of the character within the character rom, which is 12 bytes in this case. Address register A0 holds the base address of the character rom.

By automatically incrementing register A0, it is possible to read the 12 bytes that make up the character 'B'. The destination address for this move is held in address register A1 and this will be altered on each line scan with respect to the line length of the display bit plane.

Length of the scan line in the final display bit plane is represented by the variable 'linesize'. Inclusion of this variable allows the final character to be correctly lined up in the final image, Fig. 1(a).

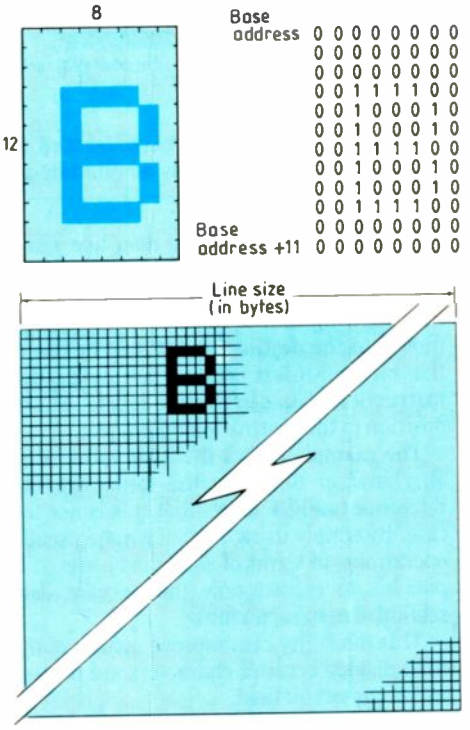


Fig.1. Display characters are normally stored in rom and defined as a matrix like this one (a) of eight-by-twelve dots. Characters can be located anywhere in the bit-plane memory map (b). Since the character is defined as a matrix, each horizontal character dot is exactly one line size away from the dot directly underneath it.

Fig.2. Within a microprocessor, data can be defined in various forms to suit the kind of manipulation required. The 68020 has a data form called a bit field which is especially useful for character and graphics-element manipulation. Bit fields allow any size of data block to be placed at any bit offset in memory.

Flexible as this system may appear, the limitations start to become obvious when you consider character fonts that do not conveniently take up a byte of storage as with, for example, a character in a ten-by-16-bit matrix. Further problems arise when you consider the possibility that the character, or graphics primitive, may have to be moved to a non-byte boundary within the destination bit plane.

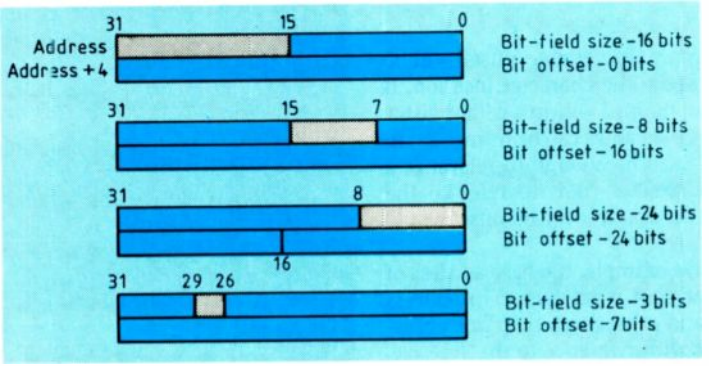
With the 68020 this problem can be circumvented using a new data form called bit fields, Fig.2. These fields can be any size from one to thirty-two bits and they can be located on any bit boundary, i.e. you are not restricted to accessing bytes, words or long-words.

To accompany this new data form, a new range of instructions is available for manipulating bit fields. Functions of these new instructions are change bit field, clear, extract, find first one, insert, set and test.

Figure three illustrates an immediately apparent use for these new instructions. In this example, character fonts are stored in sequential form whereby they are adjacent in terms of scan lines, that is scan line #0 for letter 'b' follows scan line #0 for letter 'a'. This method of storing character fonts makes it impossible to use the previous method of moving bytes. In this situation, bit fields have to be used.

Firstly, consider a situation where the character 'b' needs to be moved from character rom into the graphics bit plane. Address A holds the first scan line of the character but this scan line is situated at an offset of ten bits. Subsequent scan lines are located at addresses corresponding to the offset of the line length, Fig. 3(a). Bit-field instructions can be used to extract this information.

With the bit-field-extract instruction it is possible to extract a bit field ten bits wide with an offset of ten bits from the base



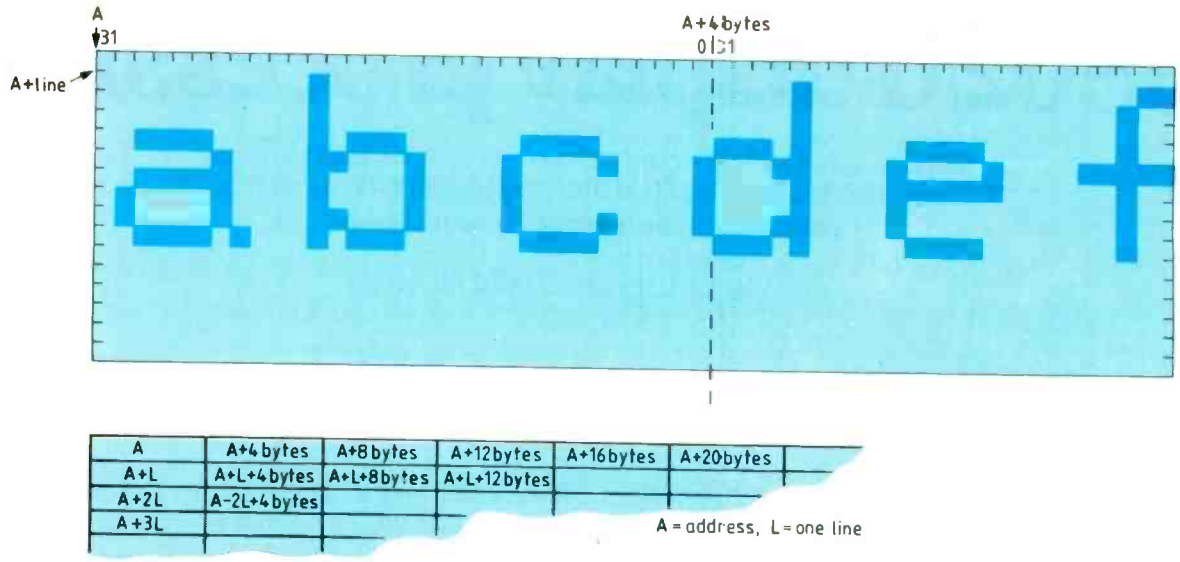


Fig.3. Characters are stored sequentially in memory and can easily be extracted using bit fields and dynamic bus sizing. Even characters on a 32-bit data-word boundary, like the letter d here, are easily manipulated because of 68020 dynamic bus sizing.

address held in register A0. For the next scan line, the base address can then be found by adding the line length of the character rom to the present base address.

The next scan line of character 'b' is found at the same bit-field location, i.e. at a bit offset of ten and a bit field width of ten. Again this bit field can be extracted using the bit-field extract instruction. By now sequencing down through all the scan lines of the character, 16 in this case, the whole character can be moved simply.

In this example, letter 'b' is contained completely within one memory location, that is none of the scan lines crosses a long-word boundary. Letter 'd' however does cross a boundary and could present problems. To deal with this, the 68020 dynamic bus sizing mechanism can be used.

Letter 'd' is located at a bit offset of 30 from the character base address and with a bit-field width of ten bits it is contained primarily in two adjacent memory locations. When presented with this type of situation, the 68020 uses its dynamic bus sizing mechanism in order to extract the relevant bit field. This is done automatically by the processor and the programmer need not worry about data-alignment problems.

Pseudo code for this data movement is as follows.

```
FOR D0 EQ #0 TO #15 D0
  BFEXTU D0*linesize(A0)
    {offset:width},D1
  BFINS D1,D0*linesize(A1) {offset:width}
ENDF
```

For this example the variable 'offset' will be enough to specify the character location. If the offset is contained within a data register then the range of values can be from -2^{31} to $2^{31}-1$ which will be enough to reference a very large character rom; to refer to the character 'q' for example the bit offset would be 160.

In the above example, the base address of the character rom is contained in address register A0 and subsequent scan line accesses are made with reference to the 'linesize' variable and the associated scan-line number which will be contained in data register D0. Data extracted from the character rom is now held in data register D1 and can be moved to the destination bit field by use of the bit-field-insert instruction. With this instruction, data can thus be located at any position in the destination bit plane.

The example makes the assumption that all character fonts are the same size but reference to Fig.4 shows that this is not the case. By simply using a set of equate pseudo operations, in terms of width and offset, it is possible to extract only the relevant data section of a character font.

This flexibility can improve quality of the final display because characters are located in the correct bit field.

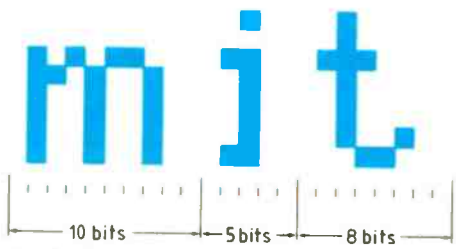
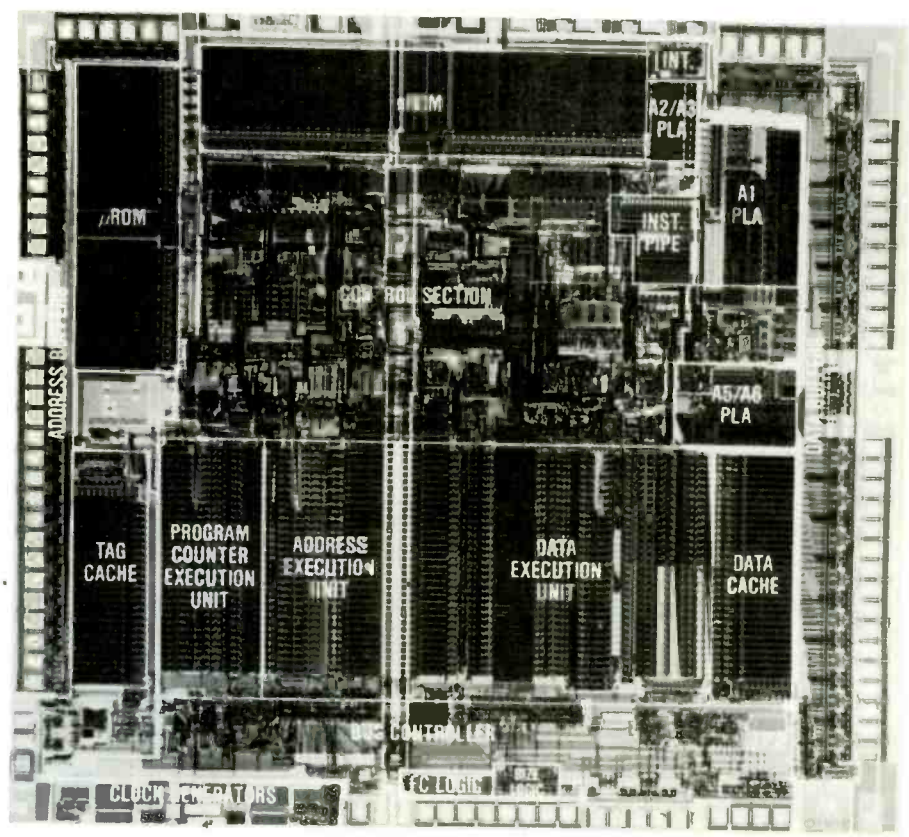


Fig.4. Proportionally-spaced characters like these make a professional display but they are difficult to control unless you have access to a facility like the 68020's bit-field manipulation feature.

David Burns and David Jones are applications engineers at Motorola's East Kilbride plant.



UPDATE

Dolby a.d.m. system for tv distribution

A consortium of independent Australian tv broadcasters together with Dolby Laboratories and Graham Patten Systems of the USA have been working jointly to develop a distribution system for vision and multichannel sound, for television programme interchange. The digital audio system has been ordered by the commercial Networks 7, 9 and 10 in Sydney and is expected to become the standard for the interchange of programmes on the Satellite Programme Service by all commercial television networks in Australia.

The system uses a video-audio multiplex process using adaptive delta modulation developed by Dolby Laboratories, known as Vamp/adm and described on page 286 of this issue. Based on a 6.6MHz subcarrier, it is designed for use in both satellite and terrestial links. The system employs differential quadrature phase shift keying (q.p.s.k.) at 750ksymbols/s to compress the subcarrier signal to about 1MHz bandwidth. For use in Australia, the 6.6MHz q.p.s.k. signal is combined with a PAL video signal for transmission.

At the heart of the four-channel system are two Dolby DP85 a.d.m. encoders (pictured on page 289) and a newly developed bit-stream multiplexer/demultiplexer. The new format operates at an aggregate bit rate of 1.500 Mbit/s and provides capacity for up to four broadcast-quality digital audio channels, although a two-channel mode at half the data rate is available as an option.

The system makes provision for scrambling of the transmitted data if required using a seed key set at the encoder site. At the decoder, the key may be either preset or received as part of the transmitted data. The system includes an additional 50kbit/s scrambled auxiliary data stream, a portion of which is used to convey video re-synchronization information. Future developments may enable each decoder to be addressed individually.

The Vamp/adm approach provides excellent audio performance at a significantly lower cost per station in the network than other available digital systems. Transmission packages for vision and four-channel sound cost about $25,000 for the encoder and $2,500 for the decoder (US dollars). Dolby Laboratories are at 346 Clapham Road, London SW9 and at 100 Potrero Avenue, San Francisco.

Winning graphics

A computer graphics design system from a young British company has won a British Design Award. The 6000 series from Sigmex combines high-speed, high-resolution graphics with ease of use, attractive styling and a carefully considered range of peripheral devices. It is the result of a design and development programme spanning more than three years.

Development of the 6000 series began with the need to create a graphic display system that could make the best use of the new international graphics standard, Graphical Kernel System (GKS).

The system utilizes advanced technology and incorporates a high-speed processor to ensure the fastest response to all operations. The clarity and the complex quality of the graphic display is enhanced by firmware which was developed by Sigmex in line with GKS objectives. The system's capability has now been extended beyond GKS to give extra performance features to enhance ease of use, give added speed, provide multiple viewports and special image modes; all without violating basic GKS principles.

The result of this development work is a range of equipment that satisfies more than 90 per cent of all two and three-dimensional applications using fast, clear graphics for purposes as diverse as slide presentations and air traffic control. The availability of a variety of peripheral devices: including joystick, trackerball, dials and mouse, ensures that the 6000 series is easy to handle and simple to control. The system is flexible and can be upgraded easily.

Ken Sadler Associates were the consultant industrial designers and transformed the original functional metal boxes in to the present external design.

Floating point Transputer

A new version of the Inmos transputer has been developed which is claimed to be the world's fastest 32-bit processor. The TMS T800-20, available soon, can sustain over 1.5M floating point operations per second (miflops) while the T800-30, available towards over 2.25 miflops.

The new devices have been developed as part of the European Esprit programme to construct a supercomputer from a reconfigurable collection of transputers. This is part of an investigation of parallel processing architecture in computers. The first transputer, TNS T414, has found applications in system simulation, robot control image synthesis and digital signal processing. All of these applications have used concurrency: running several transputers in parallel. One application has been found in computerizing the fingerprinting identification system of the British police. It has increased the speed of processing by 25 times and cut the cost of one tenth.

Pin-compatible with the earlier devices, the T800 retains their capabilities, while incorporating an on-chip floating-point unit, more on-chip memory and new instructions to support graphics. It also doubles the data rate on the communications link. However, it is thought that it is particularly suited to high-speed, high-definition colour graphics, and is claimed to be "a complete high-performance scientific computer on a single chip."

UPDATE

Link with industry

The Government has announced another scheme to link university, government and technological research projects with industry. The 'Link' initiative has earmarked £210M to support up to half the cost of collaborative programmes between the scientific community and industry. Overall, the initiative will generate expenditure by government and industry of at least £420 million over the next five years. Provided industry matches the Government funding, Link will consist of a range of programmes, each in a strategic area of science and technology. They will cover the entire spectrum of science based technology from molecular electronics to transport systems, materials technology to food process engineering. Link will have a steering committee of industrialists, Government officials, representatives of the academic community and the research councils, chaired by an eminent industrialist. All government departments that have a significant research and development programme are involved in Link.

The scientific input to Link projects will mainly come from the Research Councils and from higher education in which a considerable proportion of Link expenditure will be invested.

Recent reports suggest that underfunding of universities and underpayment of lecturers and research staff is promoting another 'brain drain' to North America.

Telephone network code

A provisional Network code of practice (NCOP) for the design of private telecommunication branch networks suitable for connection to public Switched Telephone Networks (PSTNs) is being published by Oftel, following consultations with the public telecommunication operators and representatives from TEMA, TMA, BEITA and members of the appropriate BSI standards committees.

The Code of Practice will allow private branch networks to take maximum advantage of the proposed relaxation of routing restrictions under the revised Branch Systems General Licence (BSSL) whilst continuing to protect the integrity of the PSTNs, and ensure that the public operators can continue to meet the UK's international obligations through the CCITT.

The NCOP specifies the design criteria which should be met if a network is to provide satisfactory telephone service. It includes design parameters for safety, call addressing, call control, call progress indications, call path transmission quality, traffic handling capacity and network management. In particular it specifies requirements for private network telephone calls which are also routed over the PSTNs. These requirements are to be made mandatory by the director general of telecommunications in the revised BSSL and other licences issued under Section 7 of the Telecommunications Act 1984, when the Code of Practice is issued in final form.

These mandatory requirements for public network traffic and the related recommendations for non-public network traffic refer to call impairments which include loss, delay, quantizing distortion and cross talk. The NCOP also includes direct dialling inwards (DDI) calls, because networks can be designed to route these over many hops. The NCOP recognises that good network design depends largely on the suitability of all the links, switches and terminal apparatus to work together. The parameters included are designed on the basis that the collective result of the degradations of performance of each element in a call path will not result in unsatisfactory overall call quality.

Taking advantage of the flexibility offered by the proposed relaxation of routing restrictions may entail changes to private networks and it is likely that only the most up-to-date, fully digitalk multi-hop networks will be able to meet the code of practice requirements in full. To be fully usuable, the NCOP requires information on all port-to-port impairments of PBXs and other call routing apparatus. The necessary ports requirements are not yet available as part of BS 6450, and CRA are therefore not able currently to gain approval for switching PSTN traffic over tandem circuits within a private network.

In conjunction with the NCOP, Oftel is therefore producing a set of interim ports requirements for CRA, the first of which should be available early in 1987, BT is making similar provisions in BTR 1050. A final Code of Practice will be issued when work on these ports' requirements is complete.

Video game for the forces

Those expensive aerial war games that cost millions, tie up thousands of men and hundreds of aircraft, could well soon be a thing of the past with the introduction of a new computerized training system from Marconi Radar. Called the Force Management Trainer, the new system could be described as the ultimate video game: it enables the top brass to develop and maintain their tactical command skills inexpensively and without the 'other side' watching.

The system can simulate the functions and interactions of the lower echelons in the face of an attack – the bit that costs the money. Senior commanders will be able to hold any number of war games and play them through to their likely conclusions.

The Force Management Trainer is based on a commercially available computer and can be used by people with little or no knowledge of programming.

To set up an exercise, the controller first provides the system with information on the 'battlefield' and sets the environmental conditions such as day or night, weather, etc. He/she then allocates to the defender resources such as radars, aircraft and weapons. Finally he determines the size and nature of the threatening forces.

When the exercises is running, the commanders are able to 'deploy' and commit their resources as the threat develops; the system decides on the effectiveness of their response based on the numbers and comparative performance of the various weapons and weapons platforms used by both 'sides'. The commander's ability to make those decisions is assessed at each stage.

Institutions united

The Institution of Electrical Engineers and The Institution of Electrical and Electronic Engineers are to merge. A 97% vote by members of both institutions set the seal on the combined institution to meet the future needs of the profession.

Work will start today to implement the proposals which will take full effect on 1 October 1988. The new body will retain the long established title "The Institution of Electrical Engineers" and will be the largest of the UK chartered engineering institutions having more than 100,000 members, nearly 25% of them being resident overseas.

The new Institution will cover the art, science and practice of electrical, electronic and software engineering activities in such fields as power, control, instrumentation, broadcasting, radio, telecommunications, computers and information systems.

The Institution, whose members range from students to chief executives, will act as the voice of the electrical and electronics engineering profession in all matters of public concern. It will also speak with greater authority to government and other influential bodies, both in the UK and overseas. The merger of the two institutions will also lead to an enhanced and broadened range of learned society activities and the adoption of common standards for professional qualifications. The need for technical standards and professional updating in fields of fast-moving technology will be more readily met and the vital work of encouraging young people to enter the profession will also be strengthened. Other benefits of scale and rationalization are anticipated.

The decision to merge comes after nearly three years of extensive negotiations and discussions during which time care was taken to seek the views of the membership before finalizing the terms of the agreement. This approach has been amply justified by the result of the votes.

The move will be seen as a strengthening of the profession at a time when the need for high technology engineers has never been greater.

C.r.t. drivers for high-resolution tubes

Principles, requirements and design using the Motorola MRF542 and MRF543

PETER BISSMIRE

This article discusses the design of transistor high-speed, high-voltage amplifiers for driving high-resolution colour cathode-ray tubes. It will become evident that once the problems of driving a colour tube have been solved, the drive of a monochrome tube is simple.

GENERAL REQUIREMENTS

A c.r.t. driver is required to work with input signals which lie within the supply voltage range of typical signal-processing circuitry – usually 5V to 10V. The output voltage must be of the order of 100V. The brightness of each picture element has to be continuously controlled, which requires a stable, defined d.c. response, and to set adjacent elements to different brightnesses, high speed is needed.

The design of picture tubes is a compromise between conflicting elements, which among other effects means that there is little variation in input characteristics in spite of the wide range of tube types available for consideration.

For each primary colour channel, the c.r.t. driver effectively 'sees' a vacuum triode with a grid/cathode capacitance of 15 pF. It has become normal practice to make these tubes with a common connection to the three grids and also to the first three anodes. It is therefore necessary to use cathode drive and to include in the drive voltage a correction for the differences in cathode voltage at cut-off between the three guns, amounting to about 40V.

Drive voltage can be estimated. For a given accelerating voltage and gun exit dimensions, the density of electrons in the spot is limited. As phosphor triad spacing diminishes, the spot size must keep step to realise the available screen resolution. The maximum current therefore varies as the square of the triad spacing. The transfer characteristic of the tube is usually quoted as

$$I = V^{gamma}$$

where V is the voltage difference between cutoff and beam current I. We have

$$I = k \times d^2$$

where d is the triad spacing, from which

$$V = c \times d^{(2/\Gamma)}.$$

Putting $\Gamma = 2.8$ gives $V = c \times d^{0.7}$

This voltage swing must be increased by 10% or so to allow for brightness control, which must pass through the signal drivers to maintain colour balance, and the 40V differential referred to above must be added to find the total output swing required. Starting from the standard tv tube, which is known to be difficult to focus beyond 80V drive, the following table emerges for a selection of pitches (triad spacings).

pitch (mm)	voltage swing (V)
0.82	128
0.62	112
0.43	96
0.31	85
0.21	74

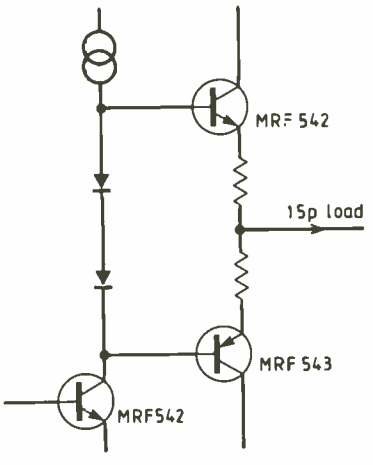


Fig.1. Basic output circuit, using MRF542/543 as a pair of emitter followers. Their input capacitance is considerably less than the 15pF of the tube.

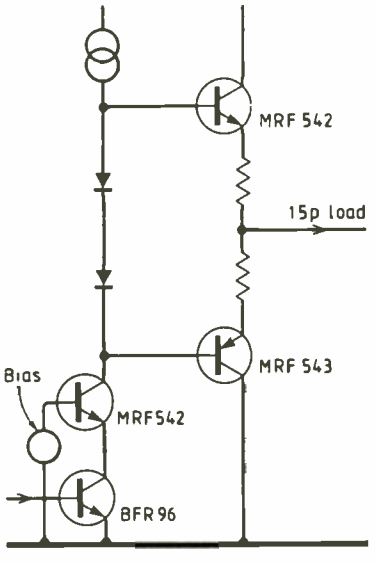


Fig.2. Addition of cascode input completes basic amplifier.

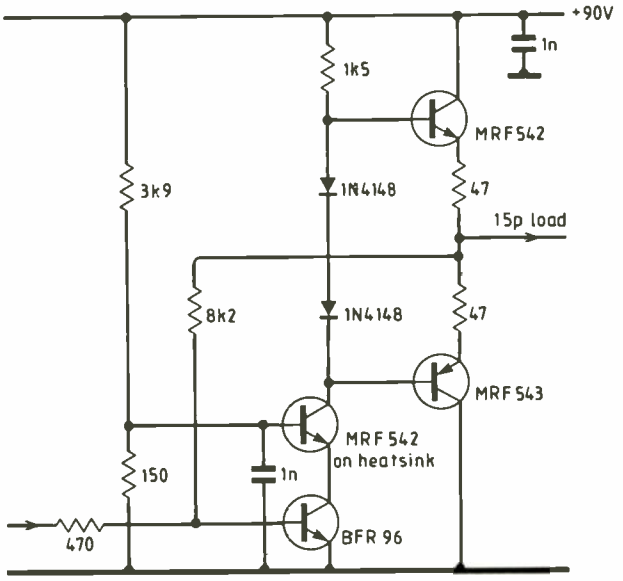


Fig.3. Complete low-cost version of amplifier, using a resistive collector load.

DEVICES

It is most helpful to concentrate on the most challenging end of the scale – tubes with pitches of 0.31mm or less. The Motorola transistors MRF542 and MRF543 (n-p-n and p-n-p respectively) are high-voltage r.f. devices in a power macro-X package. They are capable of operating with a supply voltage of 90V and allow a peak-to-peak dynamic range of 85V to be realised, thanks to their high-frequency saturation performance.

The key parameters of these devices are summarized below.

parameter	n-p-n	p-n-p	units
collector/base breakdown, V_{CBO}	>120	>100	V
collector/base capacitance, C_{CB}	1.5	2	pF
transition frequency, f_T	>1.1	>1.1	GHz
current gain, h_{FE}	>15	>15	

PERFORMANCE SPECIFICATION

The points of interest in a c.r.t. driver are its response at high frequencies and its ability to retain this response at high amplitudes. It is common practice to quote rise time, but the amplitude at which it applies is often vaguely specified or, in some cases, not at all. By quoting slew rate, one can incorporate these two linked elements into a single parameter. Furthermore, slew-rate limitation is of direct practical importance. It is rarely symmetrical so that, when it occurs, not only is the amplitude response curtailed, but the alternating part of the signal is partially rectified. In visible terms, an area of picture containing high frequencies shows errors not only in the amplitude of the high-frequency component (contrast and accutance) but also in mean brightness. This effect is only absent if the amplifier is able to respond sinusoidally or symmetrically to the required frequency, a limitation which may be up to three times more severe than that seemingly implied by risetime. In addition to small-signal bandwidth to show how well the amplifier responds at small output amplitudes, it is also necessary to consider slew rate, to indicate the response at high output amplitudes.

AMPLIFIER DESIGN

A transistor amplifier with voltage gain and a high output voltage must clearly be collector loaded. Bandwidth and slew-rate performance will be limited by capacitance at this collector.

If the input capacitance of a push-pull pair of emitter followers is less than the 15pF of the tube, it is interesting to add them between the amplifier transistor and the tube cathode. With the MRF542, 543, the difference is considerable, leading to a basic configuration shown in Fig.1.

At the input side, if the amplifier transistor is driven in grounded-emitter, the current in the collector/base capacitance of the device, which we already have to cope with at a high-voltage point, flows also at the input (Miller feedback). Grounding the base avoids this problem. Since a flat frequency response is needed, a further transistor in grounded-emitter is introduced, the BFR96 being used. This arrangement gives the cascode configuration shown in Fig.2.

The essential factor in arriving at a practical circuit is the choice of a replacement for the theoretical current source shown in Figs. 1 and 2.

Low-cost design. The simplest choice is a resistor. Compromising between operating current and the additional capacitance of a heat sink leads to the circuit shown in Fig. 3.

The maximum dissipation in the amplifier transistor is 1.35W. For an ambient temperature of 60°C a heat sink of 42.5°C/W is required. This can be furnished by a simple copper tab of about 5cm^2 which can be oriented to contribute a capacitance of about 4pF. Further increase in dissipation is not very interesting, as the capacitance of the additional heatsink required totally absorbs the additional available current.

To examine the performance of the circuit, 2pF is allowed for the capacitance of any interconnection, leading to a total capacitance of

$$1.5 + 2 + 2 + 4 + 2 = 11.5\text{pF}$$

driven upwards by 1.5kΩ and downwards by the transistor. The capacitance therefore takes a current of +0·67mA per volt across the resistor for upward slewing and 100mA minus the current in the resistor in the downward direction. This leads to a slewing rate which depends on the output voltage level, as shown in Fig.4.

The small-signal bandwidth is almost impossible to estimate, since most of the major determining factors are in the physical layout.

A prototype amplifier has been made in several versions and the results agree very closely with the graph in Fig.4. Small-signal bandwidth (10V p-p output at low frequency) reached 75MHz at –3dB.

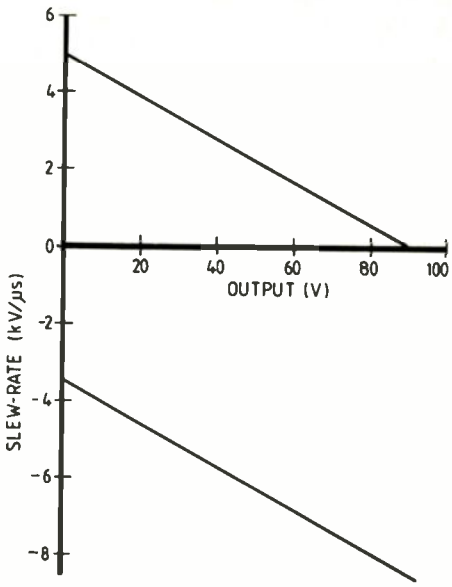


Fig.4. Slewing performance of low-cost circuit.

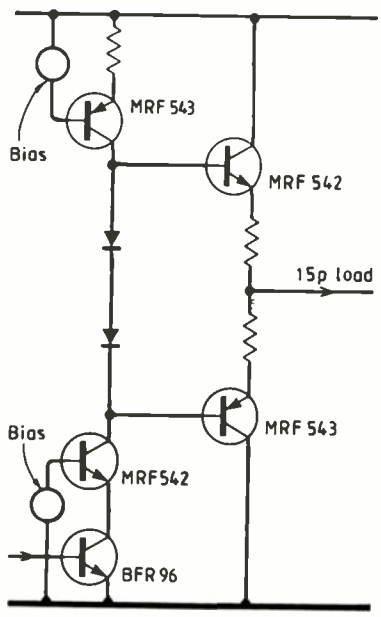
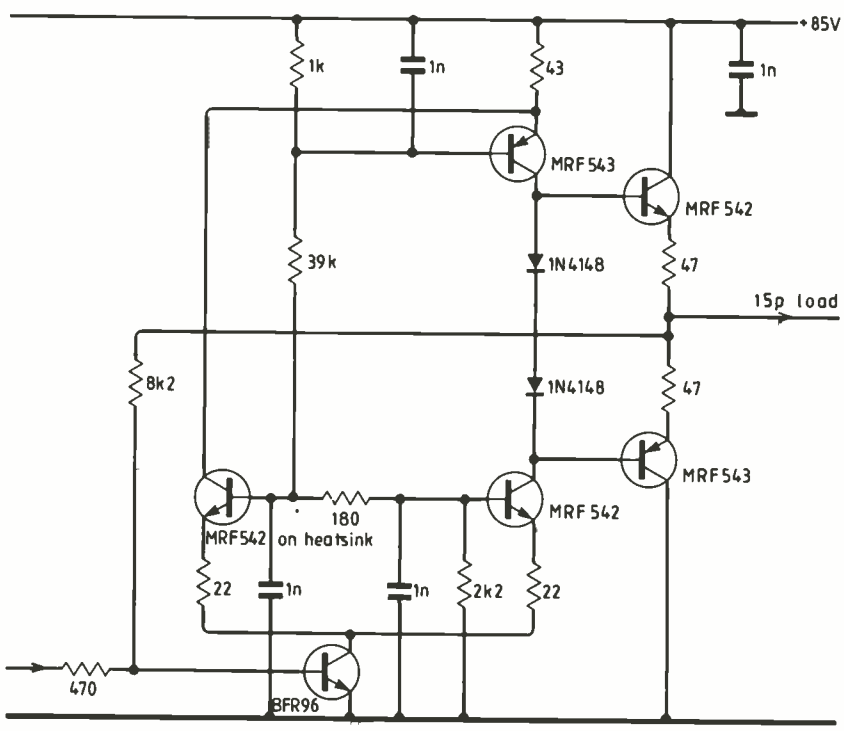


Fig.5. Constant-current source as collector load reduces differential slewing.

Fig.6. Complete high-performance circuit, with current-source modulation.



High-performance design. It has been remarked that, for the type of amplifier discussed above, the slewing rate is variable, as can be seen in Fig.4. This can be a disadvantage in a colour monitor where, due to tube spreads, there can be an appreciable difference in the d.c. levels of the three cathodes. The consequent differential slewing would lead to colouration of fast edges. This can be improved by using a transistor to make the current source of the basic configuration constant, as shown in Fig.5.

Unfortunately, there is a complication with this solution. If one tries to match the mid-range performance of the Fig.3 amplifier by setting a current of 30mA, the transistors must support this current at nearly the full supply voltage. The consequent dissipation of 2.7W requires a heat sink whose capacitance is intolerably high on both the amplifier and current source collectors. A solution is to modulate the current source so that these two collectors are operated in class AB and shift the dissipation to a collector where the signal voltage level is low. The penalty that yet another high-voltage r.f. transistor is needed: the benefit is that no heat sink at all is necessary on the high-voltage node. The resulting practical circuit is shown in Fig.6.

This arrangement sets a quiescent current of about 5mA in the amplifier stage, while the peak current available is 35mA for upward slewing and higher for the downward direction. The total capacitance at the amplifier collector is about 9pF, giving an upward slewing rate of 3.9kV/μs. The greater complexity of this current limits the small-signal bandwidth to around 60MHz. Figure 7 shows the slewing performance of this circuit compared with that of the low-cost solution. The gain in slew-rate/voltage area is considerable. As in the low-cost case, the performance of this design has been confirmed by the construction of prototypes.

GAIN

In both of the designs presented, the amplifier gain is determined by a feedback resistor. This configuration is that of a transresistance amplifier (current in, voltage out), the 470Ω resistor serving merely to ease testing with a signal generator as source. The approach offers particularly good temperature stability when current-driven, as the output temperature is –2mV/°C due to the V_{BE} of the input transistor. A further advantage of taking feedback from the amplifier output is that it gives excellent power-supply rejection. In the case of the high-performance design, this is the only possibility as, open-loop, the circuit has an extremely high low-frequency gain and the output d.c. level is indeterminate.

In the case of the low-cost design, the feedback resistor could be replaced by a resistor in the emitter of the input device. For thermal stability this would require drive from a source having a V_{BE} temperature coefficient to compensate that at the input. This would not, however, deal with transient drift due to local temperature differences, for example warm-up drift. The output dynamic range would be slightly reduced by the voltage drop across this resistor and additional measures would be needed to maintain the high-frequency response.

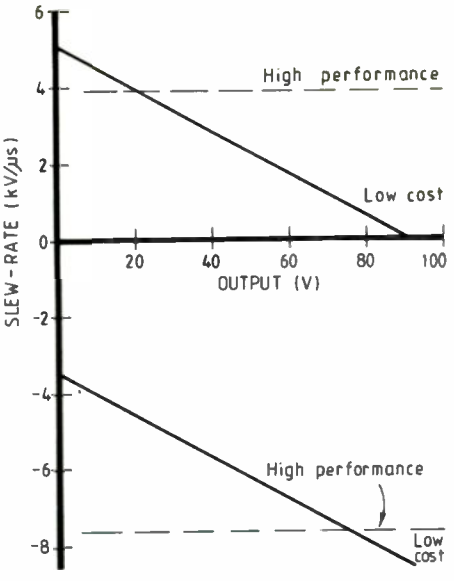


Fig.7. Slewing performance of the two circuits.

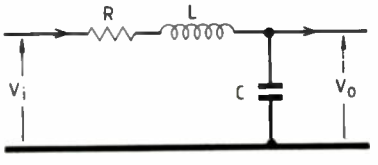


Fig.8. Series resonant compensation.

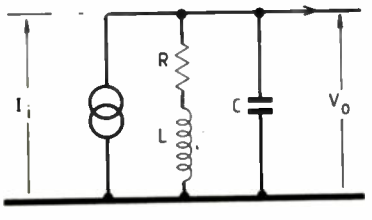


Fig.9. Series/parallel compensation.

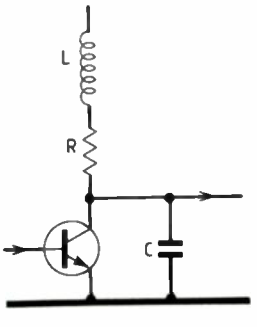


Fig.10. Typical application of series/parallel compensation.

COMPENSATION

A number of techniques can be used to improve or control the high-frequency behaviour of wide-band amplifiers.

Resonant compensation is often applied with success and consists of the addition of inductors which combine with the unwanted capacitance to form a resonant circuit. The object is to produce a low-pass filter structure which can be adjusted to give a more or less flat response below cutoff. Normally, this compensation is second-order and the two most useful configurations are series and series-parallel.

Series compensation. The circuit is as shown in Fig.8. The response of this circuit is given by

$$L\ (V_o/V_i) = \frac{R+sL}{1+sRC+s^2LC}$$

from which natural frequency is

$$\omega_n = 1\sqrt{LC}$$

and damping factor

$$\zeta = 1/2R\sqrt{C/L}$$

Thus

$$L = \frac{1}{\omega_n{}^2C}$$

where ω_n is the chosen natural frequency and

$$R = \frac{2\zeta}{\omega_nC}$$

Frequency response for

$$\zeta = 1/\sqrt{2}$$

is maximally flat, giving

$$R = \sqrt{2}/(\omega_nC)$$

The most interesting position for this type of compensation is between the amplifier output and the tube cathode. C would be the cathode itself and L and R would be additional components. The presence of a resistor at this point is usually considered obligatory. Putting $\omega_n = 377 \times 10^6$ (60MHz) gives L = 0.47μH and R = 250Ω.

A word of caution here. When driving an uncompensated capacitor, the amplifier delivers zero current on voltage peaks. This means that if the amplifier is overdriven the transistors tend to saturate with low collector current even at high frequencies. The impedance of a compensated load is resistive at resonance, so current and voltage are in phase. The consequence is that compensation improves the overall frequency response up to resonance, but degrades the high-frequency saturation behaviour of the amplifier.

Series/parallel compensation. The basic circuit is seen in Fig.9.

The response is given by

$$L\ (V_o/V_i) = \frac{R+sL}{1+sRC+s^2LC}$$

As before,

$$L = \frac{1}{\omega_n{}^2C}$$

and

$$R = \frac{2\zeta}{\omega_nC}$$

As R and C frequently exist already, it is interesting to rewrite

$$\omega_n = \frac{2\zeta}{RC}$$

Figure 10 shows an example of this type of compensation, from which one can see that it could be used in the low-cost design. Putting R = 1.5kΩ and C = 11.5pF gives ω_n = 82(13MHz). Such a low value for the resonant frequency throws considerable doubt on the usefulness of this type of compensation in these amplifiers.

The author is senior Staff Engineer at Motorola Semiconductors European headquarters, Geneva.

TELECOMMS TOPICS

British Telecom's local network goes opto

BT has started on a major programme to introduce optical fibre into its network in the City of London. This optical-fibre network, currently being installed at a cost of £50million, extends from King's Cross in the north to the Elephant and Castle in the south as well as from Covent Garden and Wapping in the west and east respectively.

It will use single-mode fibre, initially operating at either 8 or 34Mbit/s transmission rate. About 100 Dealerinterlink customers will shortly be the first to use this network. Dealerinterlink was set up about a year ago as part of BT's support for the all-electronic trading "Big Bang" on the London Stock Exchange. Customers rent groups of circuits which are all connected to a central point. This enables a private speech circuit to be set up with any other Dealerinterlink user within 24 hours. Other services will then be progressively provided through optical-fibre connections including telex, packet switched data lines, private circuits, Kilostream and Megastream (64kbit/s and 2Mbit/s circuits). These optical-fibre links can also be used by customers to connect to the Public Switched Telephone Network (PSTN).

IDD to China

International direct dialling (IDD) is now available to China via the Indian Ocean satellite, a new ground station at the capital Beijing (Peking), and BT's Goonhilly earth station. It will initially serve three of China's major cities: Beijing, Shanghai, and Guangzhou. According to British Telecom International, these cities account for 85% of the calls from the UK to China. For the time being, calls to other areas must still be made via the operator.

During the initial period in which China is still modernizing its exchanges, the service will be on a trial basis. It is intended to extend this facility to other cities and areas as soon as possible.

With a population of over 1,000 million, China only has one telephone per 100 of the population as compared with the UK's 50 per 100. The number of phones in China is expected to double by the end of the decade and rise to more than 30 million by the year 2000.

British army goes digital

The first phase in what will be the largest and most advanced private digital telecommunications network has been completed. It is the start of re-equipping the British Army's entire fixed telecommunications network in the UK with a system that has been designed to evolve into an ISDN has been completed. British Telecom has provided, at a cost of £13million, Fastnet 1, which is the higher order network of primary, minor, transit and satellite switches together with provision for a network manufacture structure.

This first phase covers the Army's 50 largest sites. The exchanges and their digital interconnections will form a single integrated system, handling voice, data, fax and teleprinter traffic.

A total of 72 Merlin DX digital p.a.b.xs (Plessey ISDXs) have been installed under the contract. They are linked by 119 wideband digital leased circuits with signalling being carried out by d.p.n.s.s. (digital private network signalling system). This will allow specialized user features (such as call transfer and ring-when-free) which will work across the entire network.

Fastnet 2 and 3, the subsequent phases, will respectively provide the terminal exchanges and the data overlay to the speech network. These phases will add a further 200 smaller switches and provide a data and office automation overlay together with the network management facilities. They have not yet reached the tendering stage but, when they are implemented, will provide an ISDN facility roughly 20 years in advance of the public network.

Channel Islands back-up

The directors of telecommunications of channel islands, Jersey and Guernsey, are having discussions with representatives of British Telecom and the French PTT with the objective of setting up an alternative path between the Islands and the UK mainland. This will be of major importance when the planned digital optical-fibre cable between Guernsey and Dartmouth in South West England comes into service in 1989. It is believed that this 150km cable will be the longest unrepeatered submarine cable to be installed.

It is expected that the back-up will take the form of a microwave link between Jersey and France, a distance of less than 30km, and then via the recently installed digital microwave link across the English Channel. This will provide higher capacity and lower cost per channel than could be obtained by means of satellites. Both Jersey and Guernsey have opted for System X and are planning for major telecommunications growth.

British Telecom buys LaserCard licence

British Telecom has bought a licence to sell credit-card sized "memory-bank" cards, known as LaserCards, from the Drexler Technology Corporation of California. Up to 2Mbytes of data can be imprinted by photographic techniques on the cards at the time of manufacture or subsequently by low-cost lasers. They are claimed to be intrinsically secure and difficult to corrupt, either accidentally or deliberately.

Mr Ron Back, managing director of BT Business Services, said: "The agreement with Drexler Technology opens the way for a range of service and equipment products which will extend the already comprehensive offerings that British Telecom is able to make to its customers, especially in the health, financial and network security areas."

British Telecom is negotiating with a major London hospital which is considering the use of LaserCards for holding maternity records. It anticipates that the combination of the secure personal data record with the facilities of communications networks will lead to even more effective solutions to many of the requirements of information technology users. It will also supply the equipment for recording and reading data on the cards. Currently, the cards and the recording equipment are made in the USA and Japan but, should customer demand warrant, there is a possibility of local manufacture.

Siemens h.p.as for ESA

Siemens design and development group at Congleton, Cheshire is to supply high-power amplifiers (h.p.as) for use by the European Space Agency. It has been awarded a contract worth

TELECOMMS TOPICS

some £750,000 for the supply of 30GHz, 350W amplifiers for installation in three advanced transportable earth stations that will be used for teleconferencing and business communications experiments via ESA's Olympus satellite. The prime contractor for the £3million ($4.5m) project is Marconi Defence Systems.

The h.p.as. which can be used in either dual-carrier uplink or single-carrier redundant modes, use Siemens own travelling wave tubes (t.w.ts).

Satellite communications is one of the key areas of the Congleton group. It has also supplied state-of-the-art microwave equipment for BT's Goonhilly Down ground station. Similar systems have also been designed and supplied to the West German and the Dutch PTTs and for installation in Saudi Arabia.

BTI circuit control contract to STC/ICL

STC and ICL have won an £8million contract from BT International to develop, supply and install four automatic digital distribution frames. These are computer-controlled systems to connect and test international telecommunications circuits. They will allow faulty circuits to be isolated without any service interruption.

Each system wil consist of solid-state crosspoints operating from 140 down to 2Mbit/s. The systems will be managed by ICL Series 39/30 mainframes and DRS 300 distributed microcomputers, using ICL network management software. The first system will be ready for operation early next year.

UK s.l.c. production for APT

AT & T and Philips Telecommunications (APT) is to set up a manufacturing facility for subscriber loop transmission equipment at Malmesbury, Wilts. It is investing £17.5 million on the venture. This includes purchase of the site (previously used by Philips Telecommunications TMC Ltd which is moving to Bishopbriggs near Glasgow) and the expenditure on new buildings and plant.

It will be manufacturing a 30 channel version of the 24-channel subscriber loop carrier (s.l.c.) equipment developed by AT & T in the USA. To be known as SLC120, it will be marketed in the UK, the rest of Europe and elsewhere in the world that 2Mbit/s trunks are used.

In addition, the factory will act as an overflow for production of APT transmission equipment developed in The Netherlands.

British Telecom quality of service

National Utility Services (NUS) and the Office of Telecommunications (Oftel) have both reported on the quality of British Telecom's services.

A survey by NUS of 200 top UK companies revealed that 37% think that BT's services have improved, 24% say no-change and 39% are of the opinion that they have deteriorated since privatization. The Oftel report indicated that the results of the recent surveys were broadly similar to those carried out from 1969 onwards. It also noted that 12% of consumers thought that the telephone service has improved since BT was privatized, 10% thought it had deteriorated and 72% thought it had remained the same. This latter concentrated on the residential telephone service and call boxes and made use of surveys carried out by 1600 members of Oftel's Telecommunications Advisory Committees.

According to NUS, its survey also revealed that over 90% believe that the level of service must improve as competition increases. However, only 13% of respondents were considering replacing BT with Mercury as their network supplier and of these all said they would only use Mercury partially rather than for all network services.

The Director General of Oftel in his report, which mainly covered residential services, stressed that he regarded BT's obligation to provide a universal service as meaning that the service must be of acceptable quality and that any deterioration in services would be the same as a price increase.

BT's managing director of Inland Communications responded to the Oftel report and said that it was encouraging to note that it rejects any suggestion that there has been general deterioration in the quality of service. He pointed out that, over the last few years, there had been a marked increase in customers' expectations. Nevertheless, despite this, and the rapid expansion in the size of the network and the services on it, BT was "keeping pace".

He pointed out that BT is well into a £160million programme to modernize all its payphones – both public and privately rented. By August, all public boxes will have been fitted out with modern equipment. He went on: "We are wholly committed to improving the quality of service we provide to all our customers. We are continuing to make substantial investments in modernizing our network. Soon, the local network will begin to show improvements as more local digital exchanges come into service.

"Mega" project developments by Philips and Siemens

Two years into the Mega project, the Siemens new semiconductor plant at Regensburg, West Germany has produced its first 1Mbit dynamic random-access memories (drams). These chips will use 1.2 micrometre geometry, while the 4Mbit memory chips will use 0.8.

In addition, the company will shortly be opening a new mask centre and a further processing line in Munich for very high-speed, bipolar, logic circuits. They will deal primarily with e.c.l. (emitter coupled logic) gate arrays for c.p.us and for superfast communications circuits. A third 'logic' project is a design centre, scheduled later this vear, for application-specific integrated circuits (asics) and custom c-mos circuits for communications, data technology etc.

Also as part of the Mega project, Philips has opened a new integrated circuit R & D centre at its Eindhoven headquarters.

The Mega project was launched in 1984 by Philips and Siemens as a joint research effort aimed at a quantum leap in semiconductor technology – one of the key areas in electronics. It has a projected cost of some $700 million (nearly £500million) with the companies investing one-third each and their governments (Netherlands and West Germany respectively) contributing the rest.

British Telecom majors in viewdata

British Telcom has announced two viewdata-based information services aimed at vertical markets; BT Travel Service (BTTS) and Optionline from BT Insurance Services.

The former is a comprehensive information service for the UK-based travel industry, based on BT's Prestel Travel Service which is already taken by 90% of the UK travel agents. This new service will allow a wide choice of screen-based methods of on-line communications so that standard videotex terminals may be used or, through automatic switching controlled by BTTS, on dual-purpose terminals.

Optionline, also based on Prestel, provides a customized videotex service for the insurance and financial services sector. To date the Halifax building society, with over 700 branches, and a further 5 major building societies have signed to put a total of nearly 3,000 of their branches online to the service. It provides building societies, banks and larger brokerages with the facilities of an in-house network and displays comparative information to assist clients when taking decisions on mortgages.

The Halifax has been providing quotations on the Prestel network for over a year and has seen a 24% increase in total customer take-up of insurance-linked mortgage loans.

Telecomms Topics was written by Adrian Morant.

Low-resistance measurement

Attention to the nulling of lead resistances enables measurement down to 100 micro-ohms.

D.J. HAIGH

Low-ohms meters have many uses in varied fields such as reliability testing of switches and relays, measurement of the diameter of fine wire, and testing electrical installations. This article describes an instrument suitable for these and many other applications where an accurate reading of low resistance is required.

Measurement of low resistance requires a special technique to null test lead and contact resistances. This technique entails the use of two pairs of test leads, called force and sense leads. One pair is used to apply a stimulus to the resistor under test in the form of a current, and the other pair to sense the voltage across the resistor (see Fig. 1). If the current in the sense leads is kept small, so that the voltage dropped across the contact and lead resistances is small compared with that across the resistor under test, then a measurement of V will give an accurate value for R_x by ohms law

$$R_X = \frac{V_.}{I_F}$$

Conventional low-ohms meters use this technique, but usually suffer from one or two drawbacks. One is the joining of force and sense leads before they make contact with the resistance under test (see Fig. 2), which will eliminate the resistance of the leads, but not that of the test probes, or their contacts with the resistor under test. The use of four completely independent test leads easily solves this problem. A second drawback of conventional meters is that measurement is often performed with a direct current, and amplification of the sensed voltage is at d.c.. Amplifier offset voltage then puts a limitation on the measurement, and a zero control must be provided and frequently adjusted.

The meter described in this article does not suffer from either of these limitations. Four test leads are provided, and the measurement is made at a.c.. A block diagram of the meter is shown in Fig. 3.

R_X is supplied by a current from the a.c. current generator and the voltage across R_X is sensed by the differential amplifier. A differential amplifier was used to ensure attenuation of any common-mode signal due to lead and contact resistances of the force leads. The resulting single-ended signal is amplified by a factor of 10, and any out-of-band noise eliminated by a band-pass filter. The signal is then amplified further by a switched-gain amplifier, which sets the full scale reading of the meter. A synchronous detector is then used to mix the signal to d.c. so that a moving-coil meter can be driven. Each component of the block diagram is described in more detail below.

A.C. CURRENT GENERATOR

The a.c. current generator applies a square-wave variation in current to the resistor under test of between 0 and 100 mA. A circuit diagram is shown in Fig. 4.

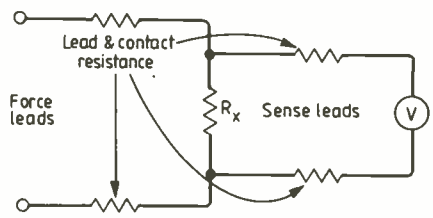


Fig.1. Conventional arrangement of test leads for low-R measurement.

Fig.2. Arrangement of Fig.1 does not eliminate resistance of contacts to unknown R.

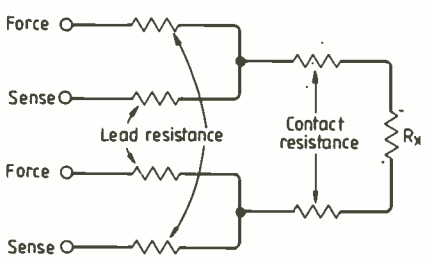


Fig.3. Block diagram of the low-ohms meter, in which four separate leads are used.

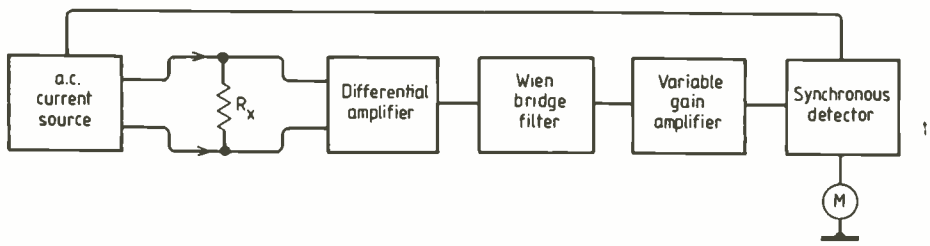
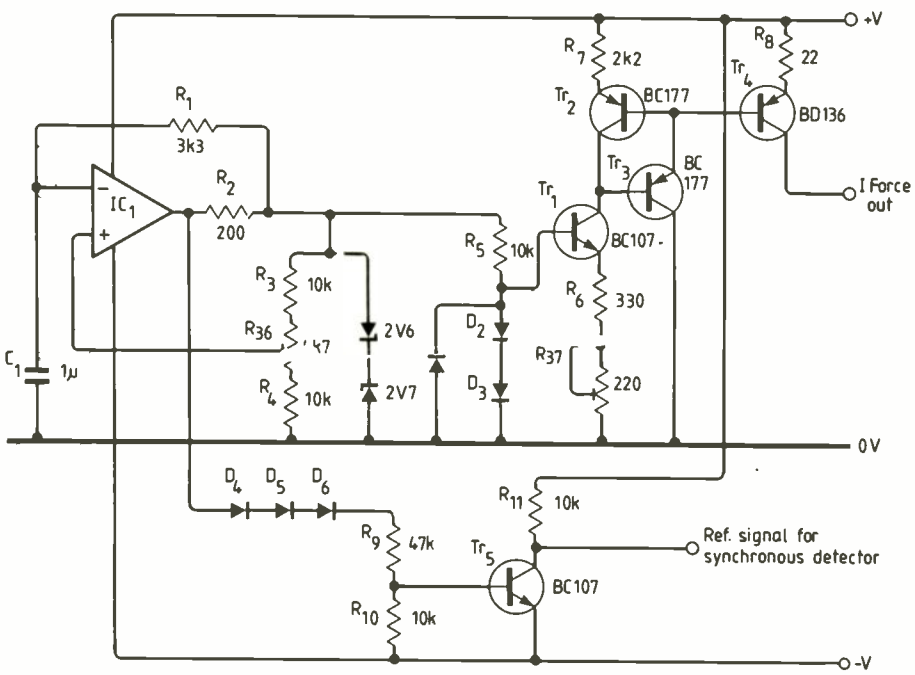


Fig.4. Alternating current force generator, providing a square-wave output, and a reference waveform for the synchronous detector.



IC_1 is configured as a square-wave generator with its frequency (approximately 130Hz) set by R_{36}. The square-wave generator drives Tr_1 via R_5, D_2 and D_3. The high-level collector current of Tr_1 is set by its emitter resistance, and low-level collector current is zero, as the base emitter junction of Tr_1 is reverse biassed. Diode D_1 prevents breakdown of this junction. Transistors Tr_2, Tr_3 and Tr_4 are configured as a current mirror with a ratio of 1:100, R_{37} being used to adjust the high-level I_{out} to about 100mA. Transistor Tr_5 provides a reference signal for the synchronous detector.

If the resistance under test is grounded at one end, the resistance of the ground lead will produce a common-mode signal which must be rejected by the differential amplifier (see Fig. 5). To avoid the use of an unduly complicated differential amplifier, the circuit shown in Fig. 6 is employed. This circuit reduces the common-mode signal due to the lead resistances by a factor of the open-loop gain of IC_2, at the frequency of I_{FORCE}. The voltage at A is adjusted so that the instantaneous voltage across R_X is centred about ground potential. Thus, the voltage across R_x is put into the differential mode, although some common-mode signal will persist due to mismatching of R_{13} and R_{14}. It is important that the ground reference required by IC_2 is taken at the differential amplifier to prevent errors due to ground track resistances.

DIFFERENTIAL AMPLIFIER

The next part of the circuit is a differential amplifier, which eliminates any remaining common-mode signal and transforms the voltage across R_X to a single-ended signal referred to ground. The circuit is shown in Fig. 7, in which the differential amplifier is the standard, single operational amplifier configuration, with unity gain and a common-mode rejection ratio limited by the matching of the ratios Rw_{15}/R_{16} and R_{17}/R_{18}.

BANDPASS FILTER

This is simply a Wien-bridge oscillator with its loop gain set to less than that needed for oscillation. The circuit is shown in Fig. 8. Centre frequency is set to 130Hz to ensure rejection of mains harmonics, whilst keeping the signal well within the bandwidth of the following amplifiers. The bandpass filter serves three functions: it attenuates harmonics of the signal so that the switched-gain amplifier is not required to amplify signals outside its bandwidth; it attenuates most of the noise so that the switched-gain amplifier is not saturated at high gains; and it acts as a preamplification stage.

SWITCHED-GAIN AMPLIFIER

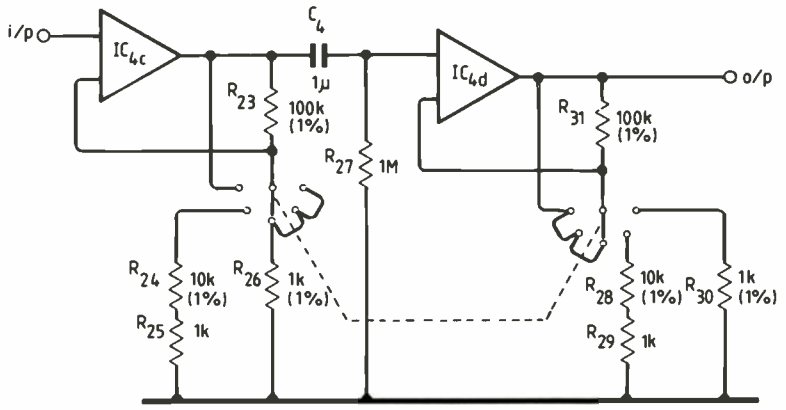
Following the bandpass filter is a cascaded pair of non-inverting operational amplifiers forming a switched-gain amplifier, the circuit being shown in Fig. 9. The amplifier provides switched gains from 1 to 10000 in decade steps. At the frequency of 130Hz, the amplifiers have ample open-loop gain to perform their required function. The amplifier is arranged to give full-scale readings of 1Ω, 100mΩ, 10mΩ, 1mΩ and 100μΩ.

SYNCHRONOUS DETECTOR

The final stage of the circuit is the synchronous detector — a solution which was considered preferable to simple rectification, since a rectifier would produce erroneous results if the noise level were comparable to the signal level. The circuit for the synchronous detector is shown in Fig. 10, and works by multiplying the signal alternately by +1 and −1 at the signal frequency. This gives a d.c. component exclusively to the required signal, so that low pass filtering of the output will remove almost all undesirable effects, such as noise and mains hum. Operation of the circuit is simple: when the analogue switch is closed the amplifier is in the inverting configuration with a gain of −1, and when the switch is open the inverting gain is −1 and the non-inverting gain is +2. This results in an overall gain of 2−1=+1. The output of IC_5 drives a moving coil meter via R_m, which should be calculated to give a full-scale deflection of 0·5V. The mechanical damping of the meter gives adequate low pass filtering.

POWER-SUPPLY CONSIDERATIONS

The instrument requires a split supply of about ±6V, a mains supply being used in the prototype to avoid frequent battery replacement. If it is desired to use batteries, it would be wise to reduce the maximum force current to 10mA, and employ a test switch so that the meter is only on when a measurement is required. The complete circuit diagram shows a suitable modification to reduce the maximum force current to 10mA. With battery operation the range of full scale measurement is 1mΩ to 10Ω.



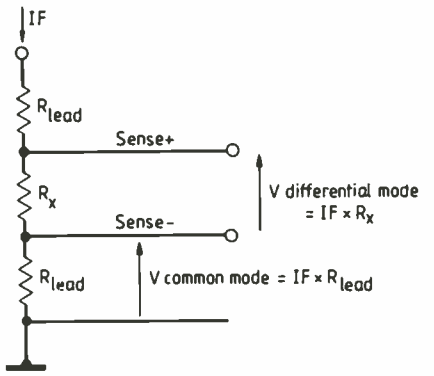


Fig.5. Grounding one end of R_X results in a common-mode voltage.

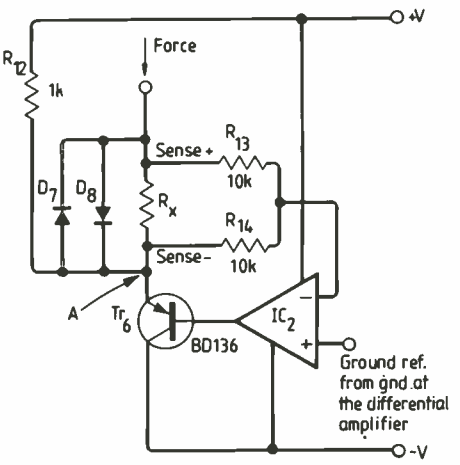


Fig.6. To reduce the common-mode voltage, R_X is connected in a feedback loop, centering the voltage about ground and rendering it a differential-mode signal.

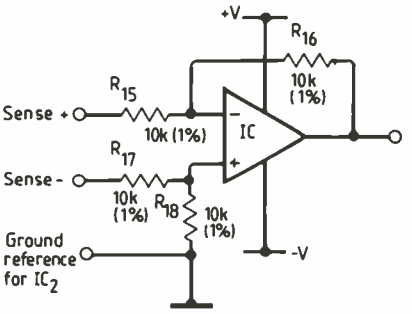


Fig.7. Differential amplifier.

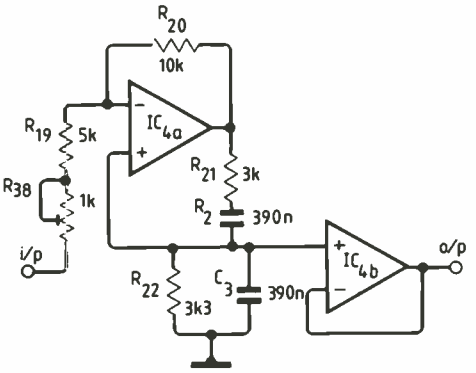


Fig.8. Bandpass filter, using Wien-bridge circuit with feedback less than that for oscillation.

Fig.9. Switched-gain amplifier.

CONSTRUCTION

Figure 12. shows the complete circuit diagram. The prototype was built on unclad Veroboard, and connections were made on the underside of the board with fine, single-strand copper-wire. This method of construction was found to be quite satisfactory, and there is no advantage in using a printed-circuit board, though anyone wishing to do so should find that the layout is non critical except for the following:

- separate boards, or sections of a single board, should be used for the force and sense circuitry, to minimize pickup;
- the ground reference voltage for IC_2 should be taken directly from the ground connection of R_{18};
- current in the ground leads of the sense circuitry, and particularly at R_{18} should be minimized;
- the leads to and from the switch must be screened.

SETTING UP

Setting up the low ohms meter is not difficult. A 22Ω resistor should be obtained, and the instrument wired up to measure it. The range switch should be set to minimum sensitivity, and the power switched on. The voltage across R_8 should be measured to give a d.c. reading of 1V. R_{38} is then adjusted to its maximum value to ensure that the Wien-bridge filter does not oscillate. R_{36} can then be adjusted to give a maximum output, using the range switch to give a sensible reading. The 22Ω resistor is then exchanged for a 1Ω, 1% resistor, and R_{38} adjusted for full-scale deflection, with the range switch set to minimum sensitivity. If the battery option is used, a 10Ω resistor must be used instead of 1Ω.

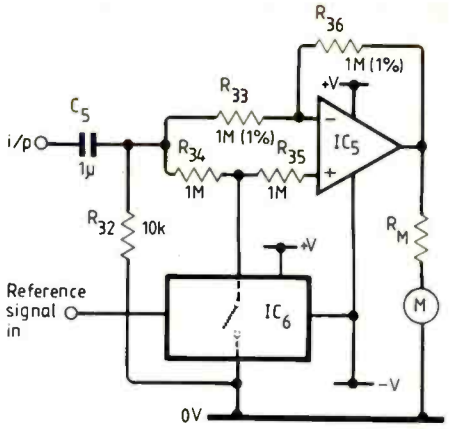


Fig.10. Synchronous detector and meter driver.

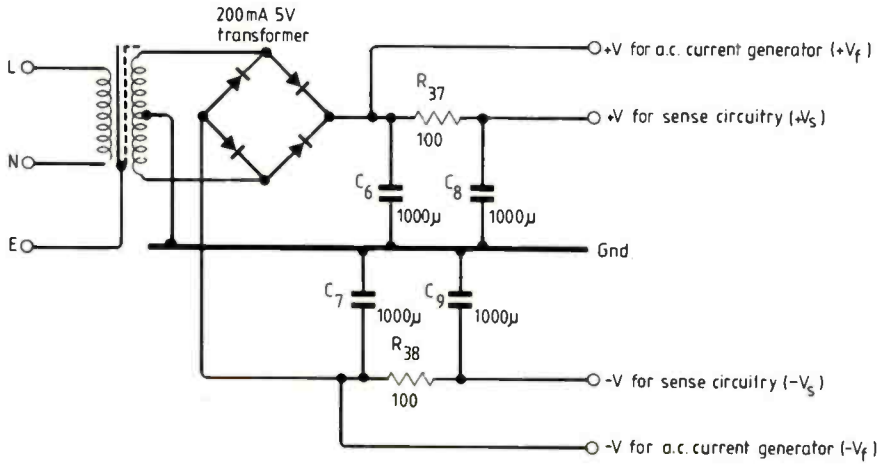
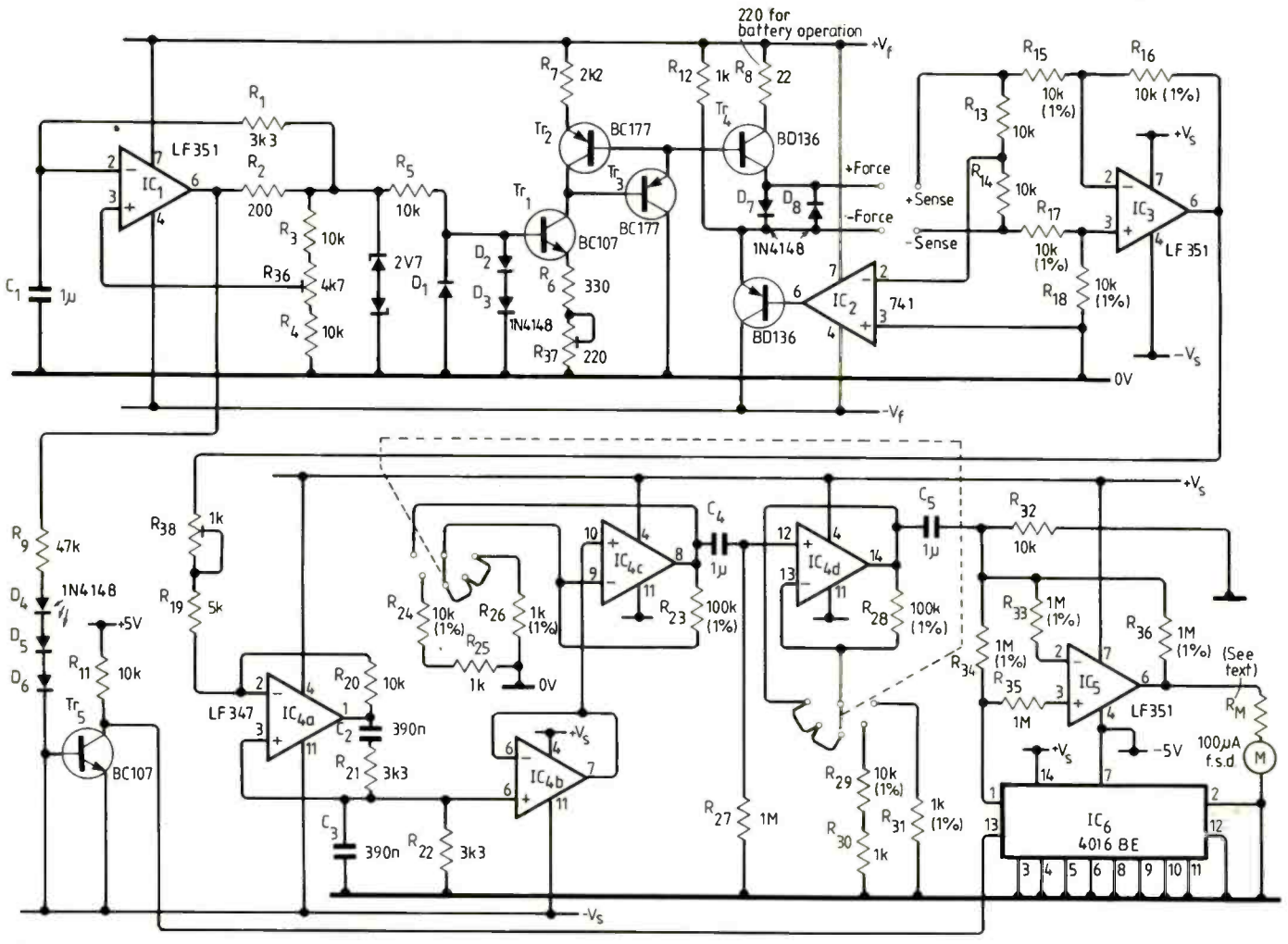


Fig.11. Mains power supply.

Fig.12. Complete circuit diagram.



WORKFILE

Audio Engineers

If you want good audio engineers you need to train them yourself – or take them from the BBC, so the current wisdom seems to go. If good engineers seem to be hard to come by, then so are the jobs. "Jobs for audio engineers are very few and far between," said Keith Darlig of Castle Recruitment, "but it's even harder to get the people."

This sentiment is widely echoed in the education world. Tim Whitely from B/TEC said their courses were very much demand orientated. "If there was more demand from industry for audio courses we would promote them (these courses) more widely among our students and in the community at large." Whitely said the lack of demand for highly skilled technicians is partly due to advances in technology. "The whole thing has become deskilled," Whitely pointed out. "There is not a great need any more for diagnostically skilled technicians. People now want the cheapest possible person to change the bit."

At present, there are few courses offered in audio engineering. B/TEC does organize a National Certificate in audio, television and radio. The course runs for two years with most entrants having 'O' level or CSE passes in maths, physics and English. There is a core of basic electronics and maths and audio engineering subjects. This B/TEC course is on offer, among other places, at Wigan College in Lancashire. Roy Hesford, who runs the course, said there is no shortage of students. He said this is largely due to the high reputation of the college and the degree of marketing carried out by the staff. Perhaps even more important, Hesford said there is no lack of takers for his pupils after their studies. "About half our young people get jobs quite quickly," said Hesford. "In the present industrial climate it just takes longer to get jobs. Most succeed though if they persevere."

To combat the lack of an overall training approach the Association of Cinematography, Television, and allied Technicians (ACTT) has joined together with employers and trainers to try and come to grips with the requirements of the film, television, radio and video industries. The newly formed training federation has held three meetings in its attempt to assess the training needs and provisions in these fields. In the cinema, the ACTT has also been involved in a scheme called "jobfit". The two-year programme would give freelance technicians an ACTT card and would train them to work in the film industry. ACTT training officer Ann Rawcliffe-King said this course is the first of its kind.

Different employers in the field may have different recruitment needs, but certain similarities seem to run through the market. A premium is set in the business on experience. "As a general principle it's difficult to get adequate training at college. You need on site training coupled with classroom training – the classroom on its own is not enough," said Roger Francis from the London Broadcasting Company.

There seems to be two types of recruitment in the market. People are taken on either at an operator level, or as graduate entry engineers. At an operator level most people come to their employers straight out of school.

The BBC is a large employer of young people and sets quite exacting criteria. A "good standard of education" is requested, to include 'O' levels in Maths, Physics and English. However, education is not the only quality needed for entry into the BBC's three year training scheme. "Other factors are equally important," said recruitment officer Ray Seymour, "we need the right personality, interest in the job at hand, and a high degree of motivation."

This emphasis on personal development is equally important at Abbey Road recording studios – part of the Thorn EMI group. Personnel officer Barbara Rotterova said most of the vacancies are at entry level. Promotion tends to be from within and the majority of staff stay at Abbey Road for quite a large part of their careers. Rotterova said recruitment centres on 17/18 year olds with good 'O' levels in maths, physics and English. Again, a keen and flexible approach is the key to success. Rotterova was quick to point out the scarcity of opportunities at Abbey Road. "People do write in to us. We keep the letters on file but at the moment it's unlikely we would take them on as we only employ two to three extra staff a year – and that's in a good year!"

According to Whitely, fewer skilled audio technicians now exist because more young people are going into general electronics, where there is "more demand for their skills and intelligence." With more people going on to general electronics degrees the competition for these graduates is becoming ever more intense. As graduates train in digital equipment they are courted actively by companies in communications, ultrasonics, and other related fields. The Ministry of Defence is another primary employer of new talent. This is, perhaps, mirrored at the second level of recruitment for audio engineers where employers are principally interested in electronics graduates. Again, the BBC has a large role to play and recruits graduates for its 12-18 months training course. All directorates are represented from television/radio to research and maintenance support. Even the BBC finds it difficult to find such a high-calibre intake. "It's getting more and more difficult as time goes by," noted Seymour. "Two years ago it was a lot more straightforward. It's a common problem now... we're competing for that same group of highly qualified staff."

The BBC has found it so difficult to recruit engineering graduates it has started to run graduate conversion courses – for those who got it wrong the first time around!! This conversion course has proved so successful – almost 3,000 applications for 30 places – the BBC said it is set to continue.

Graduate recruitment for other employers is a lot less structured. At Abbey Road they are experimenting with music graduates who show an "aptitude for electronics." The Independent Broadcasting Authority (IBA) employs engineers both at the specialist level and for its 18 month training courses. These positions are general in nature and largely deal with u.h.f. transmitter design and maintenance. The IBA employs 21/22 field teams responsible for this work. It also works with a limited number of audio engineers in the quality control field.

At the London Broadcasting Company there are three grades for audio engineers – L1, L2 and L3. Most engineers, according to Roger Francis, start at L3 and work their way up the ladder internally. "Broadly speaking, we're looking for people with 2/3 years experience as a sound technician on local radio, at the BBC, or in recording studios," said Francis. The salary for an L3 grade engineer is around £11,500 rising to £15,000 for an L1 grade. These wages include money for shiftwork, and again, the need for flexibility is underlined.

Audio engineers are also at a premium in the broadcast-equipment manufacturing sector. One leading supplier told me it employs engineers in both its service and product management support teams. The role of the latter is to give technical and marketing support to new products. The team shows people how to get the most out of the equipment they have just bought – be it a camera or a complete recording studio. A personnel spokesman said they prefer to employ people with three to five years experience. The engineer can then progress from a technician grade, through system engineering to management. The company prefers experienced engineers but finds these hard to come by because of its stringent requirements and the lack of skilled people in the market place. "If we're that desperate I'll take a graduate," the spokesman said.

Francis is quite optimistic concerning future demand for audio engineers. He said the expansion of ITN, the introduction of satellite direct broadcasting, along with a continued drive from the BBC, should provide a ready source of jobs for qualified people. Indeed, Francis is not worried about increased automation. As jobs become more specialized he said there will remain a strong need for well qualified and highly skilled maintenance engineers in all fields.

Workfile is written by Stephen Horn, Employment Editor, Electronics Weekly.

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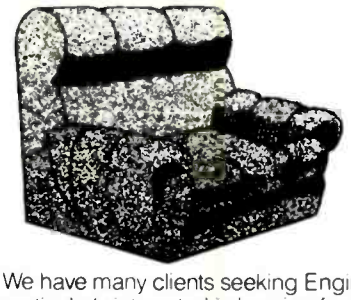

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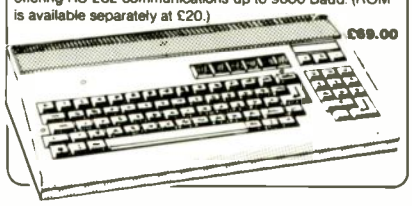

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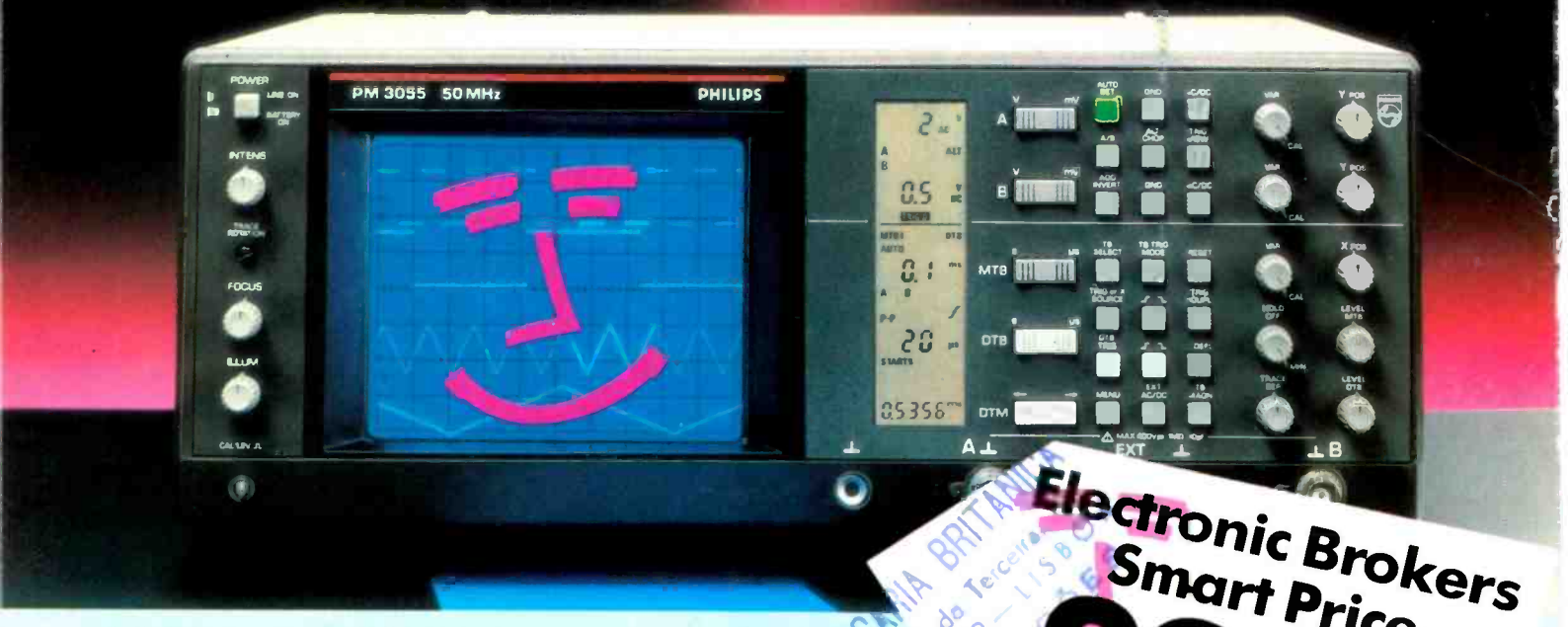

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